

PCN Number: PCN-2016-36

PCN Notification Date: 04/07/2016

Amended: Informational (ver. 2.0_05-10-2016)

WM8983 Assembly and Test Transfer to ASE ChungLi

Dear Customer,

This notification is to advise you of the following amended change(s) from the original notification listed in bullets.

This notification is to advise you of the following change(s).

WM8983 assembly and test is being transferred from Unisem to ASE ChungLi

- Cover page title changed amended version/date
- Changes To Product Identification Resulting From This PCN section on pg. 5 -
 - CURRENT MARKING mark code image changed to YMTTRFD

If you have any questions, please contact your Sales Representative.

Sincerely,

Quality Systems Administrator Cirrus Logic Corporate Quality Phone: +1(512) 851-4000



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Products Affected:

The devices listed on this page are the complete list of affected devices. According to our records, these are the devices that you have purchased within the past twenty-four (24) months. The corresponding customer part number is also listed, if available.

Technical details of this Process / Product Change follow on the next page(s).

Title: WM8983 Assemb		bly and Test Transfer to ASE ChungLi								
Customer Contact: Local Field Sales			s Rep	Representative Phone: (512) 851-4000			Dept:	Corp	orate Quality	
Proposed 1 st Ship Date:			Estin 2010	mated Q3	Sample Availability Date: March 20			March 2016		
Change Type:										
х	Assembly Site		x Assembly		Process		х	Assembly Materials		terials
	Wafer Fab Site			Wafer Fab Process			Wafer Fab Materials		aterials	
	Wafer Bump Site			Wafer Bump Process		SS		Wafer Bump Material		Material
Х	x Test Site			Test Process			Design			
	Electrical Specification			Mechanical Specification			Part Number			
х	x Packing/Shipping/Labeling			Other						
Con	Comments:									

PCN Details

Description of Change:

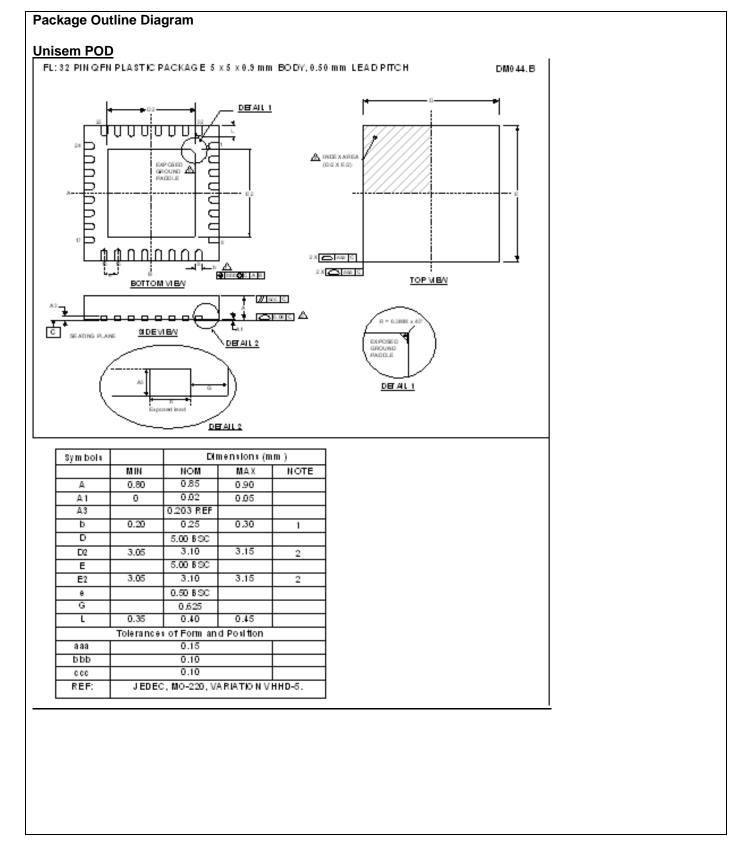
- Assembly and test will move from Unisem Malaysia to ASE ChungLi
- Assembly materials will change, details in below table
- Package quantity in Reel will remain the same : 3.5k, and in Tube will reduce from 95 to 60
- Marking will change, and logo will be removed, details below
- Minor changes in Package Outline Diagram (POD) details below
- Key Changes for new Site:

	CURRENT	NEW	
Assembly & Test Location	Unisem Malaysia	ASE ChungLi	
Bond wire	1 mil Au	1 mil Cu	
Leadframe	440467 (HD)	Samsung / CDA 194	
Die attach Sumitomo / CRM1076NS		Hitachi / 4900G	
Mold compound	old compound Sumitomo / EMEG770HCD		
Tube Package Quantity	95	60	
Marking	See	Below	
Package Outline Diagram	See Below		



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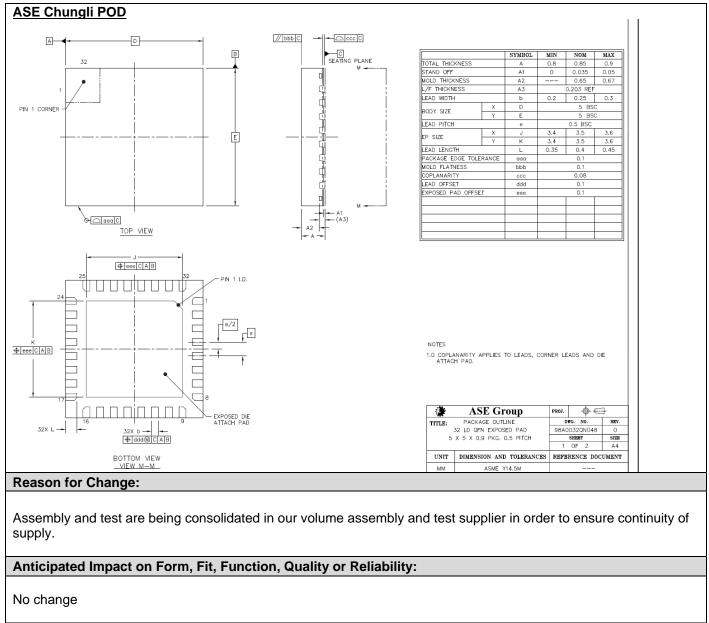
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Product Affected:

Device	Cirrus Logic Part Number
1	WM8983GEFL/RV
2	WM8983GEFL/V



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CURRENT MARKING	NEW MARKING
WM8983G	WM8983G
YMTTRFD	YMTTCT8

Qualification Data:

This qualification has been specifically developed for the validation of this change. The qualification data validates that the proposed change meets the applicable released technical specifications.						
Qualification Complete	March 2016	Status:	PASSED			



Reliability Engineering Interim Qualification Report

WM8983GEFL

Wafer Fabrication - Chartered Semiconductor Manufacturing Ltd: Fab 2 Package Assembly – ASE Chungli, QFN32 (5x5x0.9)

Aves M MMA

Russell McMillan, Senior Reliability Engineer

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Dan Liu, Senior Reliability Engineer

Gary Mortoh, Manager of Supply Chain PTE

Andrew McLean, Director of Ouality

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Cirrus Logic International Semiconductor Ltd. 2015

28/10/15 Date :

28/10/18 Date :

Date :

6/11/15. 29/10/15 Date :

Summary

The WM8983GEFL device is being tested to Cirrus product qualification requirements.

Silicon level reliability

- 1000 hours of High Temperature Operating Life (HTOL) testing.
- Electrostatic Discharge (ESD) testing.
- Latch-Up testing.

The package level reliability was qualified by similarity to the WM8758BGEFL device which is assembled in an ASE Chungli QFN32 (5x5x0.9) package.

Package level reliability

- 1000 hours of High Temperature Storage (HTS) testing.
- 96 hours of Highly Accelerated Temperature and Humidity Stress Test (HAST) testing.
- Moisture Sensitivity Level (MSL) testing at MSL 3 and subsequent Temperature Cycling for 1000 cycles.

Reliability Test Results

Test Lots: (1) 9905.\$1 (53AABV3) (2) 9905.\$2 (54AABV3) (3) 10319 (55AAUY9) (4) 22651 (04AAMGT)

Silicon Level Tests

Stress Test	Test Conditions	JESD22 Spec	Pre- condition	Test Duration	Fails/Passes (Lot)
High Temperature Operating Life (HTOL) testing	125°C V1= 5.5V V2=3.6V Dynamic	A108	-	1000 hours	0/40(1) 0/39(2)
Electrostatic Discharge (ESD) Sensitivity Testing Human Body Model (HBM)	>= Class 2 ESD pulse of 2000V HBM	A114	-	-	0/3 (3)
Electrostatic Discharge (ESD) Sensitivity Testing Machine Model (MM)	>= Class B ESD pulse of 200V MM	A115	-	*	0/3 (3)
IC Latch-Up Test	Class II Level A +/-100mA Current Injection and 1.5xMax Vsupply Overvoltage	JESD78	-	•	0/3 (3)

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Package Level Tests

Stress Test	Test Conditions	JESD22 Spec	Pre- condition	Test Duration	Fails/Passes (Lot)
High Temperature Storage (HTS) testing	150°C No bias	A103	-	1000 hours	0/77 (4)
Highly Accelerated Temperature and Humidity Stress Test (HAST)	130°C, 85% R.H. 230kPa[33.3psi] V1= 3.6V	A110	(a)	96 hours	0/77 (4)
Moisture Sensitivity Level (MSL) testing	MSL 3 (Peak IR reflow temperature = 260°C)	J-STD-020	-	-	0/77 (4)
Temperature Cycling	-65°C to +150°C Transfer time < 1 minute Soak time > 10 minutes	A104	(a)	1000 cycles	0/77 (4)

(a) Pre-condition: JEDEC Moisture Sensitivity Level 3 (JESD22 – A113)

Revision History

Revision	Date	Originator	Change
1.0	28/10/2015	Russell McMillan	Initial release