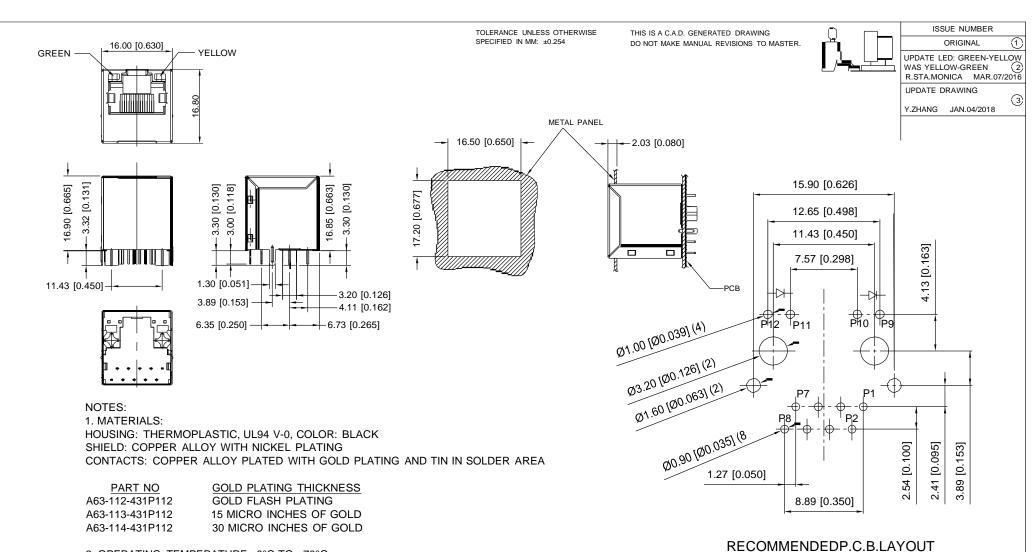


# 161 Alden Road, Units 7 & 8, Markham, Ontario Canada L3R 3W7

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Email: info@edac.net http://www.edac.net
ENGINEERING CHANGE NOTICE

		ENC	SINEERING CHANGE NOTIC	<u> </u>
			Date:	Document Number
			January 3, 2018	ECN20180103-00
_			Revision Number	SHEET
			1	1 of 7
CHANGE TYPE				
CLASS I	Customer notification and ap	proval required prior to imple	mentation	
X CLASS II	Customer notification only, ne	o approval required		
CLASS III	No customer notification requ	uired		
REASON OF CHANGE				
Old tooling has worn out and not Affected P/Ns: 1. A63			nts. 24. A63-113-413P112 5. A6	50-115-231P190
DESCRIPTION OF CHANGE:				
1.Design change in shield to e	liminate the use of glue to ho	old LEDs in place.  CHANGE TO		
2.Design improvement to the h	ousing to allow easy insertion		do pre-bending on the leads.	
3. Implementation:Running Ch	ange	CHANGE TO		
PARTIES AFFECTED				
X Customer X Distributors Suppliers		NORCOMP MH X ETW	<u>X</u> I	ECA EDG EDAC UK
KEY TARGET DUE DATES IF C Submit Quote Prod. Trial Run Run at Rate	HANGE IS APPROVED TO PR	ROCEED (check if applicable a	PPAP from Supplier MRD of Production Parts	n)
ACKNO	OWLEDGEMENT FOR ECN 1	INITIATION: (OPTIONALS)		STATUS
Tooling Rep Mfg Eng Rep Production Rep Materials Rep Quality Rep		Process Eng Rep Facilities Rep Sales Rep. Product Eng. Rep. Purchasing Rep	APPRO	
	APPROVALS FOR ECN INIT	TATION (REQUIRED)	REJEC	CTED
President  Vice President		Engineering Manager  Mechanical Engineer	Ronnie Sta. Monica	e REJECTED by:
MINIMUM OF TW	O SIGNATURES REQUIRED			



2. OPERATING TEMPERATURE: 0°C TO +70°C

3. STORAGE TEMPERATURE: -40°C TO +85°C

- 4. MATE WITH MODULAR PLUG CONFORMING TO FCC PART 68, SUBPART F.
- 5. RECOMMENDED TEMPERATURE FOR WAVE SOLDERING IS 260°C MAX, 10 SEC MAX

6. DIMENSION: MM [INCHES]

# RJ45 MAGNETIC JACK WITH LED, VERTICAL, 8P, 8C SHIELDED, 10/100Mbps FILTER

THIS SERIES FULLY CONFORMS TO THE EUROPEAN UNION DIRECTIVES 2002/95/EC AND 2002/96/EC FOR RoHS COMPLIANCY.

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**■C■**D REFERENCE NO.: **■**63-11X-431P112 DR■■N: R.ST■.■ONIC■ **D**■TE: JUN.2 1/2012

CHECKED: D**⊪**TE:

TOLERANCE: ±0.10[±0.004]

SEE NOTE

SHEET 1 OF 2

DR■ⅢING NU■EER

P■RT NU■BER

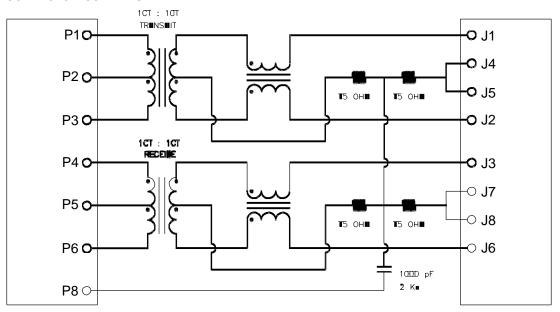
A63-11X-431P112

ISSUE 3

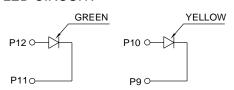
## **ELECTRICAL CIRCUIT:**



### CONNECTOR SOLDER SIDE



## LED CIRCUIT:



LED SPECIFICATIONS (WITH FORWARD CURRENT OF 20 mA)			
STANDARD LED	WAVELENGTH	FORWARD V (MAX)	TYP
GREEN	565 nm	2.4 V	2.2 V
YELLOW	590 nm	2.5 V	2.1 V

## TEST NOTES:(25±5°C)

1.TR:(100KHz,0.1V);

PINS:(P1-P3):(J1-J2)=1:1±3% PINS:(P4-P6):(J3-J6)=1:1±3%

2.LX:(100KHz,100mV,8mA, DC Bias)

PINS: (P1,P3),(P4,P6)=350uH MINIMUM

3.DC RESISTANCE:

PINS:(J1-J2),(J3-J6)= 1.2 OHMS MAXIMUM

4.HIPOT:

PINS(P1,P3)TO(J1,J2)=1500VAC PINS(P4,P6)TO(J3,J6)=1500VAC

5.INSERTION LOSS: RS = RL = 100 OHM

-1.2dB MAXIMUM AT 100KHz TO 100MHz:

6.RETURN LOSS:

-18dB MINIMUM AT 1MHz TO 30MHz:

-12dB MINIMUM AT 30MHz TO 80MHz

7.CROSS TALK:

-35dB TYPICAL AT 1MHz TO 100MHz

8.COMMON TO COMMON MODE ATENUATION:

-30dB TYPICAL AT 30MHz TO 100MHz

9. LEAKAGE INDUCTANCE:

PINS: P1-P3. WITH J1-J2 SHORTED=0.35 uH MAX @1MHz PINS: P4-P6, WITH J3-J6 SHORTED=0.35 uH MAX @1MHz

10. INTERWINDING CAPACITANCE:

PINS:(P1-P3):(J1-J2)= 40 pF MAX @1MHz

PINS:(P4-P6):(J3-J6)= 40 pF MAX @1MHz

# RJ45 MAGNETIC JACK WITH LED, VERTICAL, 8P, 8C SHIELDED, 10/100Mbps FILTER



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**■**C**■**D REFERENCE NO.: **■**63-11X-431P112 DR■■N: R.ST■.■ONIC■ D■TE: JUN.21/2012

D∎TE:

CHECKED: P■RT NU■BER

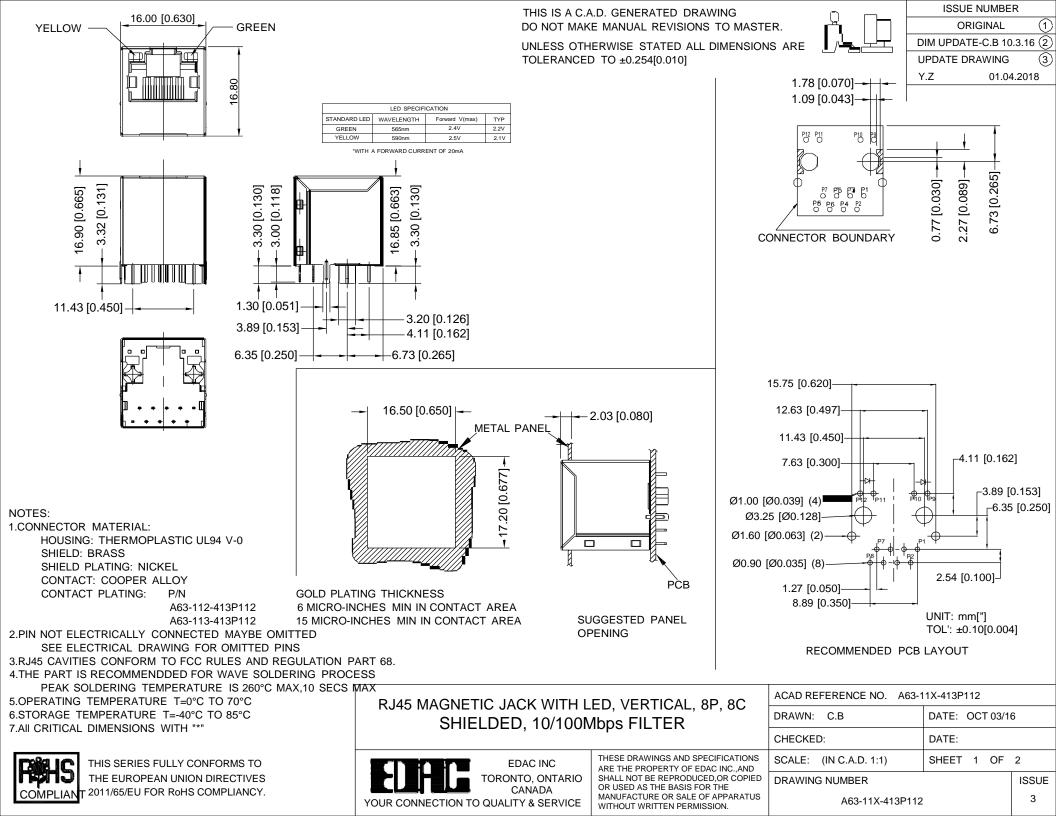
SEE NOTE

SHEET 2 OF 2

DREWING NUMBER

A63-11X-431P112

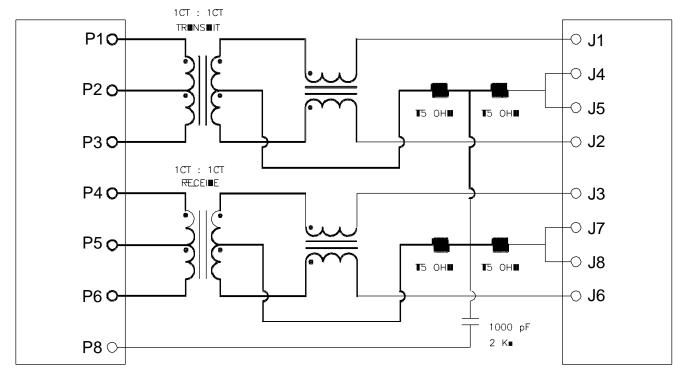
ISSUE 3



**ISSUE NUMBER** 

**ORIGINAL** 

## CONNECTOR SOLDER SIDE

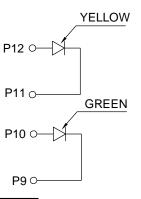


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COMPLIANT 2011/65/EU FOR RoHS COMPLIANCY.

# LED CIRCUIT:



LED SPECIFICATIONS (WITH FORWARD CURRENT OF 20 mA)			
STANDARD LED	WAVELENGTH	FORWARD V (MAX)	TYP
GREEN	565 nm	2.4 V	2.2 V
YELLOW	590 nm	2.5 V	2.1 V

## TEST NOTES:(25±5°C)

1.TR: @100KHz:

PINS:(P1-P3):(J1-J2)=1:1±2%

PINS:(P4-P6):(J3-J6)=1:1±2%

2.LX:(100KHz,100mV,8mA, DC Bias)

PINS: (P1,P3),(P4,P6)=350uH MINIMUM

3.DC RESISTANCE:

PINS:(J1-J2),(J3-J6)= 1.2 OHMS MAXIMUM

4.HIPOT:

PINS(P1,P3)TO(J1,J2)=1500VAC

PINS(P4,P6)TO(J3,J6)=1500VAC

5.INSERTION LOSS: RS = RL = 100 OHM

-1.0dB MAXIMUM AT 1MHz TO 100MHz;

6.RETURN LOSS:

- -18dB MINIMUM AT 1MHz TO 30MHz;
- -16dB MINIMUM AT 30MHz TO 60MHz
- -16dB MINIMUM AT 30MHz TO 60MHz
- 7.CROSS TALK:
  - -30dB min AT 1MHz TO 100MHz
- 8.COMMON TO COMMON MODE ATENUATION:
  - -30dB TYPICAL AT 1MHz TO 100MHz
- 9. LEAKAGE INDUCTANCE:

PINS: P1-P3, WITH J1-J2 SHORTED=0.35 uH MAX @1MHz

PINS: P4-P6, WITH J3-J6 SHORTED=0.35 uH MAX @1MHz

10. INTERWINDING CAPACITANCE:

PINS:(P1-P3):(J1-J2)= 40 pF MAX @1MHz

PINS:(P4-P6):(J3-J6)= 40 pF MAX @1MHz

11. ISOLATION: PHY SIDE TO LINE SIDE: 1500VAC or 2250VDC

## RJ45 MAGNETIC JACK WITH LED, VERTICAL, 8P, 8C SHIELDED, 10/100Mbps FILTER



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	ACAD REFERENCE NO. A6	3-11X-413P112
	DRAWN: C.B	DATE: OCT. 03/16
	CHECKED:	DATE:
3	SCALE: N.T.S	SHEET 2 OF 2
D	DRAWING NUMBER	ISSLIE

DRAWING NUMBER 3 A63-11X-413P112

THIS IS A C.A.D. GENERATED DRAWING DO NOT MAKE MANUAL REVISIONS TO MASTER.



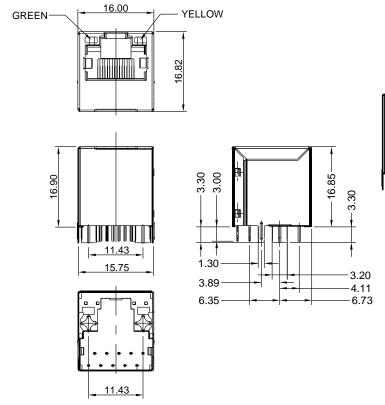
ISSUE NUMBER ORIGINAL

UPDATE DRAWING

<u>(1)</u>

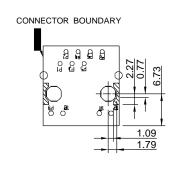
Y.ZHANG JAN.04/2018





UNLESS SPECIFIED ALL DIMENSIONS TOLERANCE IS ±0.254

METAL PANEL 16.50 -2.03 PLUG INSIDE 17.20-LABEL PCB RECOMMENDED PANEL OPENING (TOLERANCE:±0.10)



KEEP OUT AREA

12.63 11.43 7.63 3.89 6.35 54 7\*1.27=8.89 15.75

> RECOMMENDED PCB LAYOUT (TOLERANCE:±0.10)

### Connector Material:

1. Housing: ThermoPlastic UL94V-0

Contact: Copper Alloy. Selective 50u" Gold Plated Min In Contact Area.

Shield: Brass Nickel Plated

2. Operating Temperature: 0°C to +70°C

- 3. Storage Temperature: -40°C to +85°C
- 4. Recommended Wave Soldering Temerature: 260°C Max, 10 Seconds Max
- 5. Pins Not Electrically Connected Maybe Omitted. See electrial Diagram for Omitted Pins.
- 6. Cavity Conforms To FCC Rules And Regulation Part 68 Subpart F.

# 180° RJ45 10/100 BASE-T JACK WITH **MAGNETIC MODULE**

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A60-115-231P190 ACAD REFERENCE NO.

DATE: May. 09/16 DRAWN: N.SONDH

PART NUMBER

SEE ABOVE

SHEET 1 OF 2

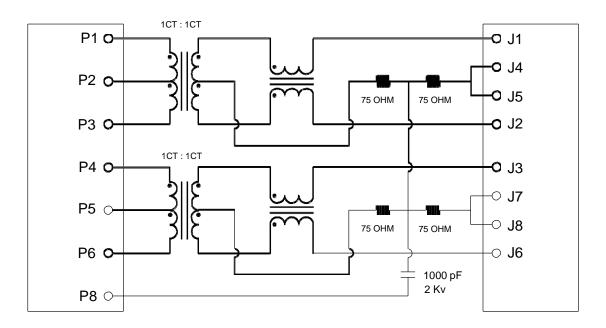
DRAWING NUMBER

A60-115-231P190

**ISSUE** 2

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## CIRCUIT SCHEMATIC



**Electrical Specifications:** 

1. Turn Ratio @100KHz:

PINS:(P1~P3):(J1~J2)=1:1±5% PINS:(P4~P6):(J3~J6)=1:1±5%

- 2. Primary Inductance: 350uH Min @100KHz,0.1V 8mA DC Bias
- 3. DC Resistance: (J1~J2),(J3~J6)= 1.2 Ohms Max
- 4. Insertion Loss: 1-100MHz 1.2dB MAX
- 5. Return Loss:

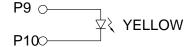
1MHz - 30MHz -1.2 dB Max 30MHz - 60MHz -16 dB Min 60MHz - 80MHz -10dB Min

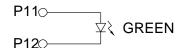
- 6. Cross Talk: 1MHz 100MHz 30dB Min
- 7. Common To Common Mode Atenuation:

1MHz - 100MHz -30dB Min

8. Isolation:

PHY Side To Line Side: 1500VAC OR 2250VDC





LED SPECIFICATIONS			
STANDARD LED	WAVELENGTH	Forward V (max)	TYP
GREEN	565 nm	2.4V	2.2V
YELLOW	590 nm	2.5V	2.1V

WITH FORWARD CURRENT OF 20mA

## 180° RJ45 10/100 BASE-T JACK WITH MAGNETIC MODULE



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ACAD REFERENCE NO.

A60-115-231P190

DRAWN: N.SONDH

DATE: May. 09/16

PART NUMBER

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**ISSUE** 

2

DRAWING NUMBER

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