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Product Change Notification

Change Notification #:	118143 - 01
Change Title:	Select Intel® FPGA devices,
	PCN 118143-01, Software,
	Intel® Quartus® Prime Standard Edition
	Software Update
	Reason for Revision: Updated Table 1 content
Date of Publication:	June 3, 2021

Key Characteristics of the Change:

Software

Forecasted Key Milestones:

Availability of Software Update	Now

Description of Change to the Customer:

Reason for Revision: Updated Table 1 content

This is the same change described in ADV2110 Revision 1.1.0 issued on May 28, 2021.

Intel is notifying customers of an important update to the Intel[®] Quartus[®] Prime Standard edition software. Refer to table 1 for further details and link to the KDB article containing recommendations and further actions.

Table 1: Intel Quartus Prime Standard Edition Software Fix

	Update Details	Impacted Software Versions	KDB Article (with patches)
Inf	erred RAM Fix	Ver 19.1 and	
•	RAM data corruption when reading from a Sequential Access Memory (SAM) that is implemented using an inferred True Dual Port RAM or Simple dual port RAM, in impacted versions of the software. Permanent fix implemented in Intel Quartus Prime Standard edition software ver 20.1 and above.	earlier	Why is there seed dependent RAM data corruption in designs compiled in the Intel® Quartus® Prime Standard edition software
•	Patches are available for Intel Quartus Prime Standard edition software ver 18.1, and 19.1.		version 19.1 and earlier?

Customer Impact of Change and Recommended Action:

Patches for the issue described in this customer advisory are available now. Refer to the KDB link in table 1.

For more information, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via: https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html

Products Affected / Intel Ordering Codes: All Arria[®] II, Arria[®] V, Cyclone[®] IV, Cyclone[®] V, MAX Series, Stratix[®] IV, Stratix[®] V devices.

The list of affected part numbers (OPNs) can be downloaded in Excel form:

https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv2110-opn-list.xlsx

PCN Revision History:

Date of Revision:	Revision Number:	Reason:
March 15, 2021	00	Originally Published PCN
June 3, 2021	01	Updated Table 1 content

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: <u>asmo.pcn@intel.com</u> Asia Pacific/PRC Contact: <u>apacgccb@intel.com</u> Europe Email: <u>eccb@intel.com</u> Japan Email: jccb.ijkk@intel.com

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Page 3 of 3