# PCN AO-PCN-2022-035-A

# Introduction of 6" InGaAIP Thinfilm Chip for Firefly

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**CALL OSRAM** 

01.09.2022

Dear Customer,

please review this **PCN** and provide your feedback in the **Customer approval form** (at the end of this PCN document) to your ams OSRAM sales partner before **10.10.2022** \*).

Your prompt reply will help ams OSRAM to assure a smooth and well executed transition. If ams OSRAM does not hear from your side by the due date, we will assume your (if you are a Distributor: and your customer's) full acceptance to this proposed change and its implementation.

ams OSRAM understands the time requirements your organization needs to approve this PCN. However, if you can provide ams OSRAM an estimated date your organization will have finalized this PCN review, ams OSRAM can use this date to plan continued production to secure your order needs during the transition time.

Your attention and response to this matter is highly appreciated.

#### Please direct your inquiries to your local Sales office.

\*) ams OSRAM aligns with the widely recognized JEDEC/ECIA/IPC Joint Standard No. 46, which stipulates:

- Customers should acknowledge receipt of the PCN within 30 days of delivery of the PCN.
- Lack of acknowledgement of the PCN within 30 days constitutes acceptance of the change.
- After acknowledgement, lack of additional response within the 90 day period constitutes acceptance of the change. If the customer requires additional time to perform sample testing, beyond the 90 day review period, an extension must be negotiated with the supplier.

Subject of change:	Introduction of 6" InGaAIP Thinfilm Chip for Firefly		
Affected products:	LR VH9F		
Reason for change:	<ul> <li>Introduction of latest 6" chip technology to secure continuous supply</li> <li>Fulfill market demands for higher brightness</li> <li>Update of datasheet to latest format and correction of input, where needed.</li> </ul>		
	Current status	New status	
Description of change:	For details refer to document 2_cip_AO-PCN-2022-035-A		
Product identification:	Date code: 0923 (WWYY)		
Time schedule for PCN material:	Final qualification report:	01.09.2022	
	Samples available:	01.09.2022	
(after implementation of change):	Intended Start of delivery:	01.03.2022 <sup>*)</sup> *) or earlier if released by customer and upon mutual agreement	
Time schedule for Pre-PCN material:	Last time order date (LTO):	01.09.2023 <sup>**)</sup> **) Lead time and LTO quantity shall be mutually agreed between OSRAM OS and customer.	
(prior to implementation of change):	Last time delivery date (LTD):	31.05.2024 ***) ***) planned last date for delivery of products of current status	
Assessment:	No change of product reliability		
Documentation:	Customer information package 2_cip_AO-PCN-2022-035-A; 3_cip_AO-PCN-2022-035-A_Rel		

as described in the PCN.

PCN material: Products with implementation of the changes as described in the PCN.

# Customer approval form AO-PCN-2022-035-A

## Introduction of 6" InGaAIP Thinfilm Chip for Firefly

Please list product(s) affected in your application(s):			
Please check the appropriate box below:			
O Approval: We agree with the proposed change and accept start of the shipment upon availability of PCN material	O Not relevant: Change is not relevant for products in use.		
O Change cannot be accepted:	·		
<ul> <li>We have objections:</li> </ul>			
• We request following Information:			
<ul> <li>We request following Samples:</li> </ul>			
<ul> <li>Expected approval date:</li> </ul>			
<ul> <li>Volume requirements for Pre-PCN mater</li> </ul>	ial:		
O Remarks:			
Sender:			
Company:			
Address / Location:			
Signature:	Date:		
Please return this approval form to your Sales	partner.		
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# PCN AO-PCN-2022-035-A Introduction of 6" InGaAIP Thinfilm Chip for Firefly

Customer information package

R&D-PD-LED-TLM and OS Q CQM ICI 2022-09-01

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# Agenda

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### **Reason for change**

ltem	Description
1.	Introduction of latest 6" chip technology to secure continuous supply
2.	Fulfill market demands for higher brightness
3.	Update of datasheet to latest format and correction of input, where needed.

### **Description of change: new chip**

4

ltem	Current status	2 <sup>nd</sup> source chip A
Picture (exemplary)		
Wafer size [mm]	100 (4")	150 (6")
Chip carrier substrate	Ge	Si
Chip size [µm]	250 x 250	175 x 175
Height [µm]	150	120

### Changes in the datasheets:

Page	Change Item	Reason for change	Old	New
1	Typ. Radiation	Correction of datasheet	140° (horizontal), 165° (vertical)	140° (horizontal), 150° (vertical)
1	Optical efficacy	Change of new chip	38 lm/W	50 lm/W
2	Datasheet Q#	Change of new chip	P2R1 (56…140mcd) - Q65110A8088	Q2S1 (90mcd224mcd) – Q65113A5238
4	Peak Wavelength	Change of new chip	632	634
4	Spectral Bandwidth at 50% I_relmax	Change of new chip	25nm	16nm
4	Viewing angle at 50% $\mbox{Iv}$ -90°	Correction of datasheet	165°	150°
4	Typ. $V_F$ at $I_F$ = 5mA	Change of new chip	2.00V	1.90V
5	Brightness Groups	Change of new chip	P2-R1	Q2-S1

### Changes in the datasheets: Ordering Information

Old			New		
Ordering Informatio	n		Ordering Information		
Туре	Luminous Intensity <sup>1)</sup> I <sub>F</sub> = 5 mA I <sub>v</sub>	Ordering Code	Туре	Luminous Intensity <sup>1)</sup> I <sub>F</sub> = 5 mA I <sub>v</sub>	Ordering Code
LR VH9F-P2R1-1	56 140 mcd	Q65110A8088	LR VH9F-Q2S1-1-0-Z	90 224 mcd	Q65113A5238

### Changes in the datasheets:

#### Characteristics

I <sub>F</sub> = 5 mA; T <sub>S</sub> = 25 °C			
Parameter	Symbol		Values
Peak Wavelength	$\lambda_{peak}$	typ.	632 nm
Dominant Wavelength <sup>3)</sup>	λ <sub>dom</sub>	min.	620 nm
	Gon	typ.	625 nm
		max.	633 nm
Spectral Bandwidth at 50% I <sub>rel,max</sub>	Δλ	typ.	25 nm
Viewing angle at 50% I <sub>v</sub>	2φ	typ.	140 °
values for 0°, 90°		typ.	165 °
Forward Voltage 4)	V <sub>F</sub>	min.	1.80 V
I <sub>F</sub> = 5 mA		typ.	2.00 V
•		max.	2.40 V

Old

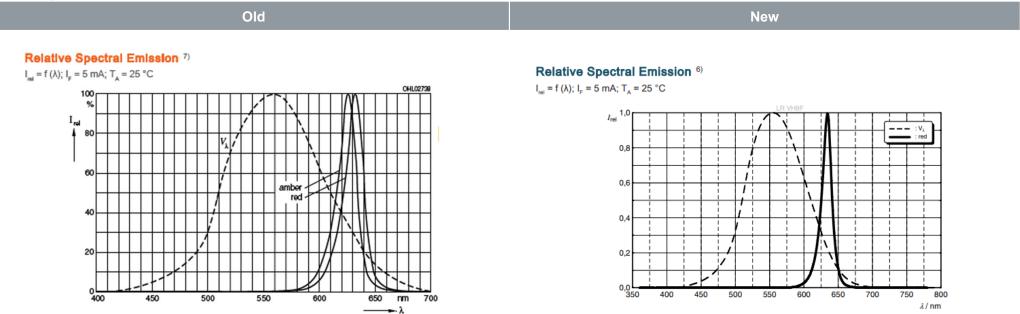
Characteristics

I <sub>F</sub> = 5 mA; T <sub>S</sub> = 25 °C			
Parameter	Symbol		Values
Peak Wavelength	$\lambda_{peak}$	typ.	634 nm
Dominant Wavelength 3)	$\lambda_{dom}$	min.	620 nm
		typ.	625 nm
		max.	633 nm
Spectral Bandwidth at 50% I <sub>rel.max</sub>	$\Delta \lambda$	typ.	16 nm
Viewing angle at 50% I <sub>v</sub>	2φ	typ.	140 °
values for 0°, 90°		typ.	150 °
Forward Voltage 4)	V <sub>F</sub>	min.	1.80 V
$I_{\rm F} = 5  \rm{mA}$		typ.	1.90 V
•		max.	2.40 V

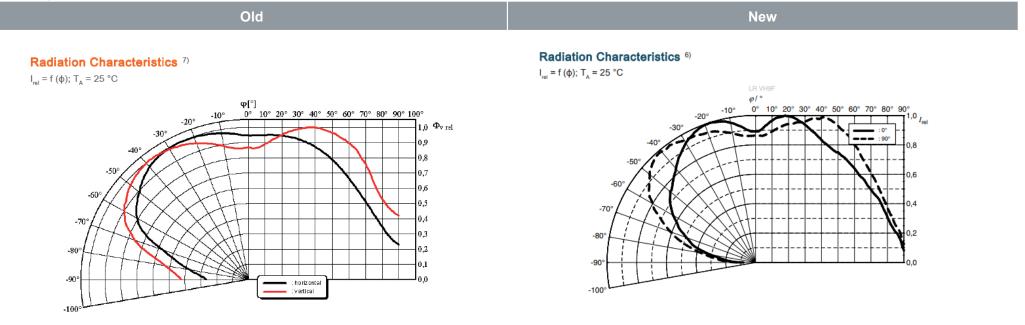
New

### Changes in the datasheets: Spectral Emission:

8

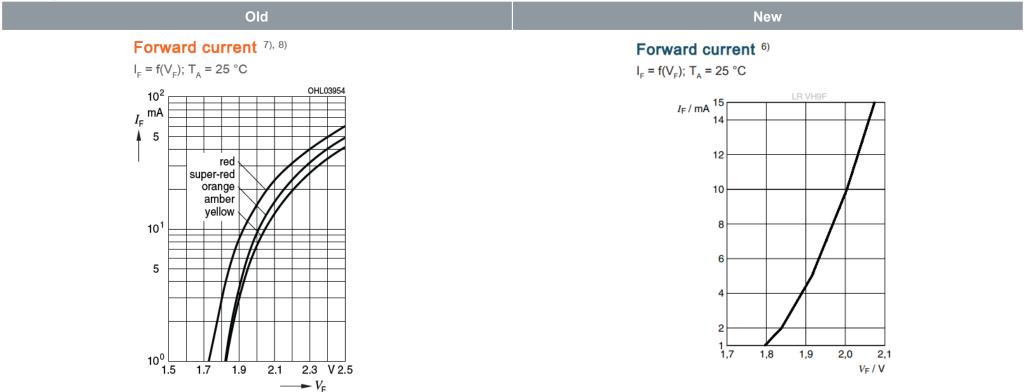


### **Changes in the datasheets: Radiation Characteristics:**



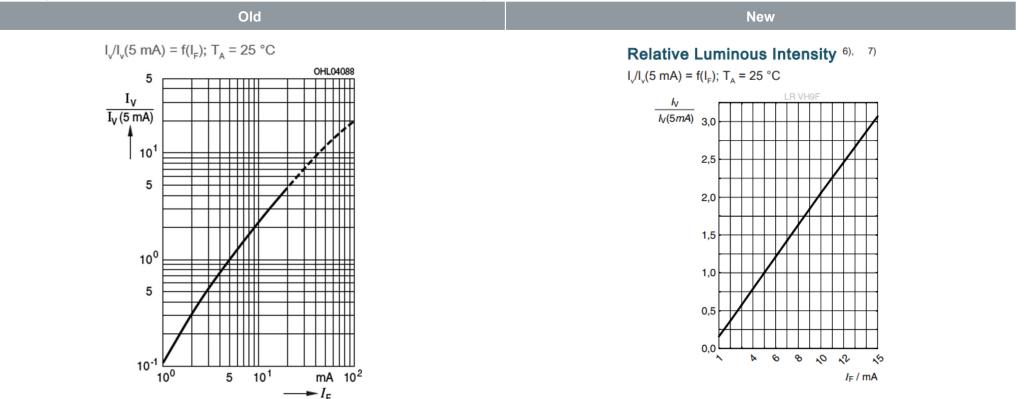
#### **Changes in the datasheets: Forward Current:**

10



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#### Changes in the datasheets: Relative Luminous Intensity (If):

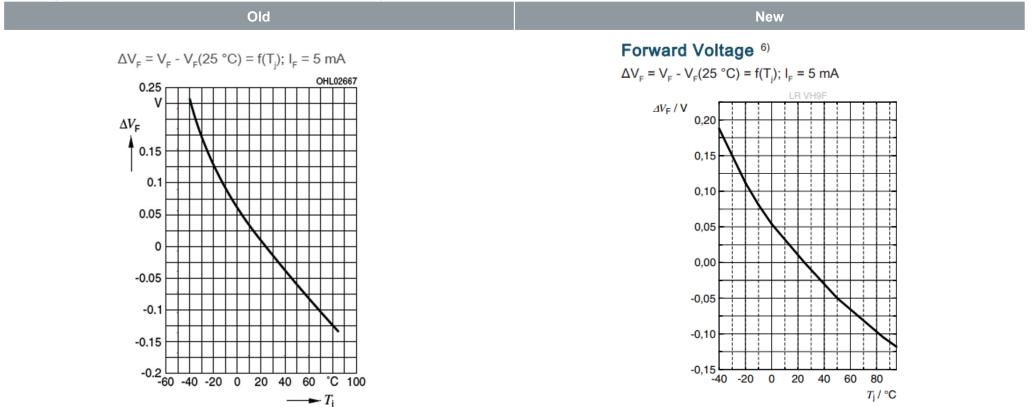


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#### **CIMUN OSRAM**

#### Changes in the datasheets: Forward Voltage:





### Changes in the datasheets: Relative Luminous Intensity (Tj):

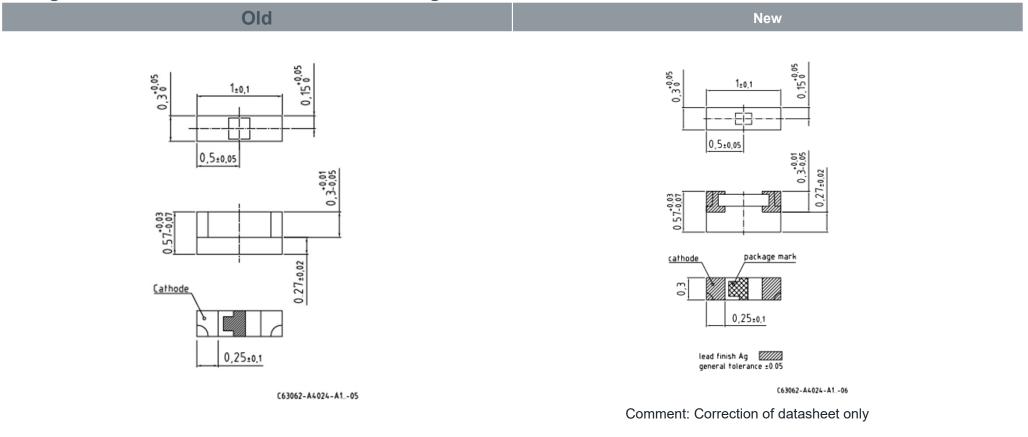
Old	New
N/A in old Datasheet	<b>Relative Luminous Intensity</b> <sup>6)</sup> $I_v/I_v(25 \text{ °C}) = f(T_j); I_F = 5 \text{ mA}$
	$\frac{l_v}{l_v(25^\circ\text{C})} \begin{array}{c} 1,6 \\ 1,4 \end{array}$
	1,2
	1,0
	0,8
	0,4 -40 -20 0 20 40 60 80
	Tj / °C



#### Changes in the datasheets: Dominant Wavelength:



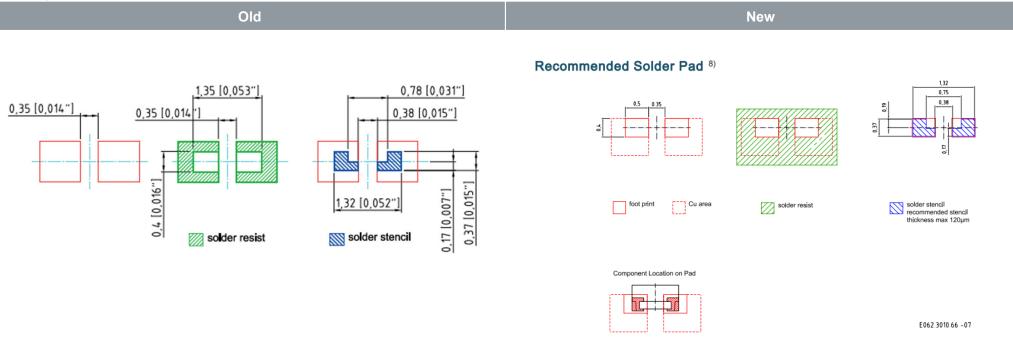
### Changes in the datasheets: Dimensional Drawing



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### Changes in the datasheets: Recommended Solder Pad



Comment: Correction of datasheet only

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### List of affected products

Brand	
Firefly	LR VH9F

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**PCN Samples** 

Brand	
Firefly	LR VH9F

Color code: available

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on request

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### Time schedule

for PCN material ( <u>after</u> implementation of change):				
Final qualification report	01.09.2022			
Samples available	01.09.2022			
Intended Start of delivery	01.03.2023*)	*) or earlier if released by customer and upon mutual agreement		

or Pre-PCN material ( <u>prior to</u> implementation of change):				
Last time order date (LTO)	01.09.2023 **)	**) Lead time and LTO quantity shall be mutually agreed between OSRAM OS and customer.		
Last time delivery date (LTD)	31.05.2024***)	***) planned last date for delivery of products of current status		

#### Note:

Pre-PCN material:Products of current status, means before implementation of the changes as described in the PCN.PCN material:Products with implementation of the changes as described in the PCN.

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# OSRAM Reliability Report

# 220037C1

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Subject	Reliability Report of next generation red chip according to AO-PCN-2022-035-A
Date	01.09.2022
Tested device	LR VH9F
Brand (including sub brands)	FIREFLY 0402
Applies to	LR VH9F

ams-OSRAM AG Tobelbader Strasse 30 8141 Premstaetten, Austria Phone +43 3136 500-0 ams-osram.com

#### Pre-conditioning according to Jedec Level II

Toot Douformed	Condition	Duration	Sample Size	Failures		
Test Performed				EI.	Opt.	Vis
Resistance to Solder Heat RSH JESD22-A113	Reflow soldering 260°C	Зх	3x30	0	0	0
Wet High Temperature Operating Life WHTOL JESD22-A101	T <sub>A</sub> = 40°C; r.H.= 93% I <sub>F</sub> = 5mA	1000h	4x30	0	0	0
Temperature Cycling TC JESD22-A104	T <sub>A</sub> = -40°C/+85°C 15min each extreme	500c	4x30	0	0	0
High Temperature Operating Life HTOL JESD22-A108	$T_A = 25^{\circ}C$ I <sub>F</sub> = 15mA	1000h	4x30	0	0	0
High Temperature Operating Life HTOL JESD22-A108	T <sub>A</sub> = 85°C I <sub>F</sub> = 5mA	1000h	4x30	0	0	0
Pulsed Operating Life PLT JESD22-A108	T <sub>A</sub> = 25°C I <sub>F</sub> =100mA; tp = 0,01ms; D = 3%	1000h	4x30	0	0	0

#### Remark: Lot A = Control lot; Lot B/C/D = Evaluation lot

Failure criteria:

Electrical failures:	VF (If = $5mA$ )	> 2,40V; ± 10% from initial value
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Optical failures: IV (If = 5mA) absolute limit:  $\pm$  50% max. and  $| \Delta\%$  max -  $\Delta\%$ min| > 50%

Visual failures: broken or damaged package or leads

Conclusion: The tested devices representing the product family as stated in the applies to section fulfill the reliability requirements.



### Disclaimer

PLEASE CAREFULLY READ THE BELOW TERMS AND CONDITIONS BEFORE USING THE INFORMATION. IF YOU DO NOT AGREE WITH ANY OF THESE TERMS AND CONDITIONS, DO NOT USE THE INFORMATION.

The Information contained in this Document does not constitute an independent warranty. The committed behavior is described in the Product data sheet and/or further, mutually agreed specifications.

Distribution of part or all of the contents of this Document to any 3rd party in any form without the prior permission of ams-OSRAM International GmbH is prohibited except in accordance with applicable mandatory law.

#### Further explanations:

Data: The Data used in this Document consider the reliability test results under the mentioned driving conditions only. For Product information on the maximum operating conditions and the OSRAM standard qualification profile please refer to the Product data sheet or contact your local sales partner.

Conditions: The conditions for the generation of the Data are as follows:

1. The Data and curves shown in this Document are based on experiments carried out under laboratory conditions on a random sample size of LED/IRED/Laser/Detector with readouts at discrete readout times (where applicable). Thus, the Data above represent a limited number of production lots only and may differ between different assembly lots over time (including chip or package changes). Thus, the behavior of the LED/IRED/Laser/Detector in the final application may differ from the Data. The behavior of the LED/IRED/Laser/Detector at conditions or readout times deviating from those stated above may not be deduced from the Data.

2. If applicable:

a) Extended driving conditions:

The tested driving conditions exceed the maximum limits stated in the Product data sheet. Therefore, a reduced lifetime or an accelerated degradation is expected. Failure limits noted in the Document refer to the testing condition according to the OSRAM standard Product qualification profile and not to the actual testing condition. b) Extended testing duration:

The testing duration exceed the OSRAM standard qualification profile of the mentioned Product. Failure limits noted in the Document refer to the testing duration according to the OSRAM standard Product qualification profile and not to the actual testing duration.

c) Exceeding standard qualification conditions - (Product data sheet limits not affected):

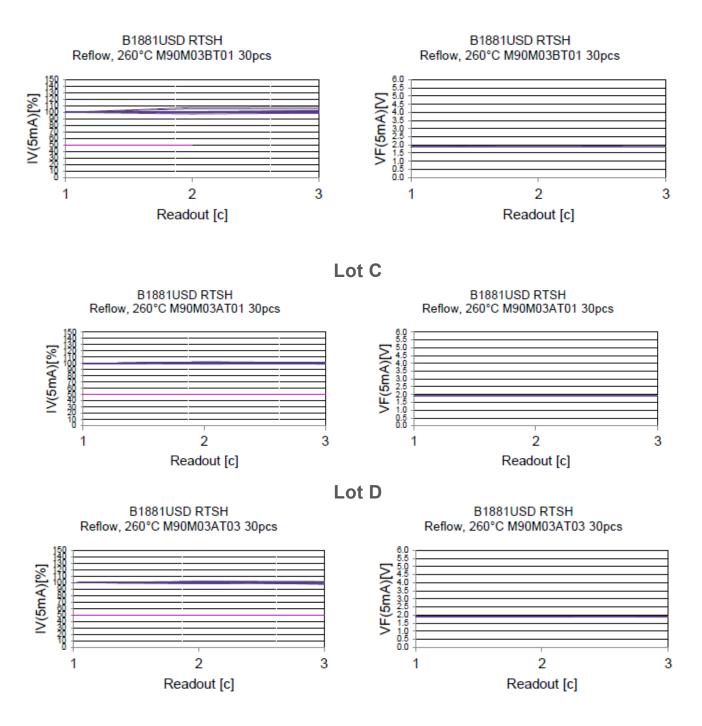
The tested driving conditions exceed the OSRAM standard qualification profile of the mentioned Product. Therefore a reduced lifetime or an accelerated degradation is expected. Failure limits noted in the Document refer to the testing condition according to the OSRAM standard Product qualification profile and not to the actual testing condition.

- 3. For long term operation additional failure modes of the chip or package can occur which are not shown in this Document.
- 4. Possible differences in the thermal management of OSRAM and customer's setup may lead to a different aging behavior.



### **RSH Resistance to Solder Heat**

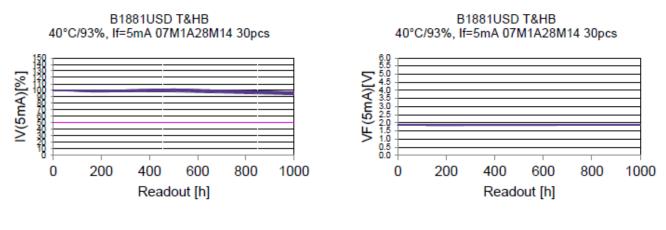
### Lot B



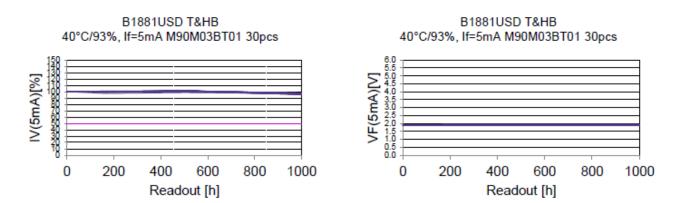


### WHTOL 40°C/93%r.H.; 5mA

#### Lot A



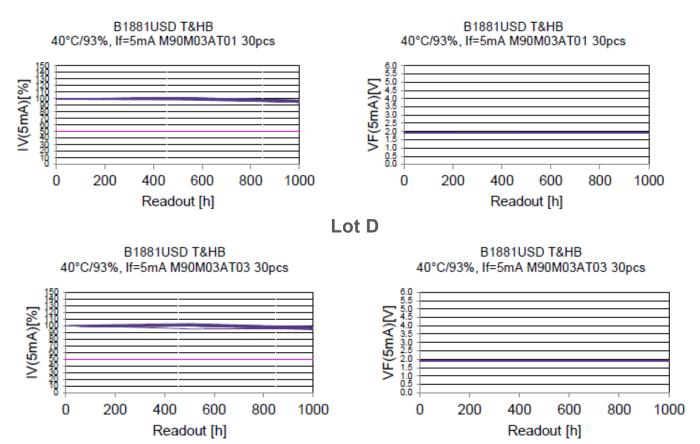
Lot B





### WHTOL 40°C/93%r.H.; 5mA

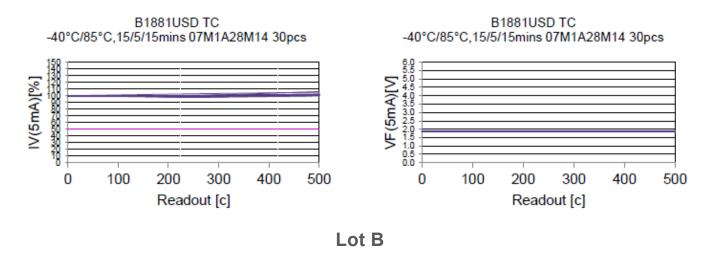
Lot C

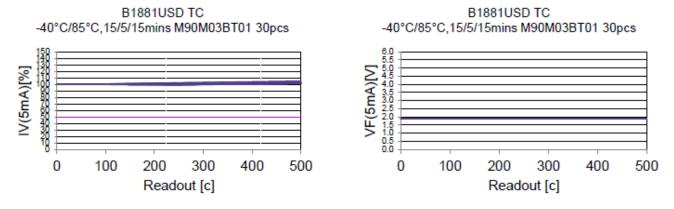




### TC -40°C/85°C

#### Lot A

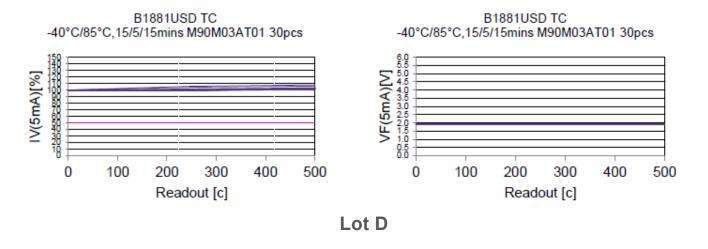






### TC -40°C/85°C

#### Lot C



B1881USD TC -40°C/85°C,15/5/15mins M90M03AT03 30pcs

200

300

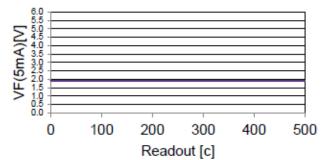
Readout [c]

400

500

100





540×1-0000/00040×1-0

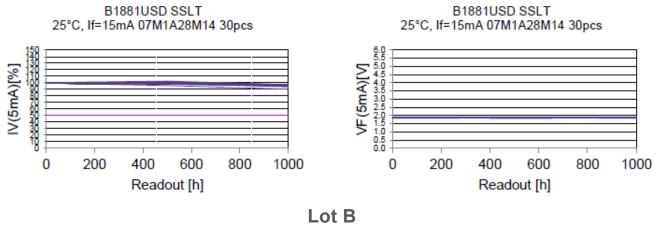
0

IV(5mA)[%]

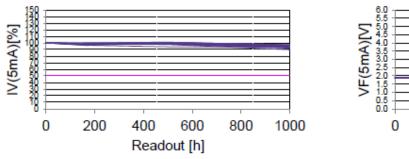


### HTOL 25°C/15mA

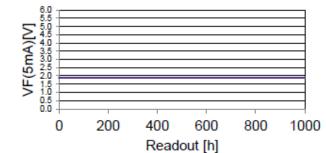
Lot A







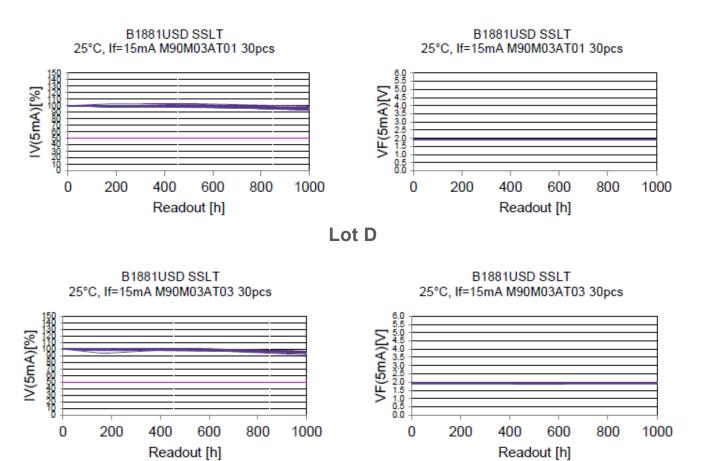
B1881USD SSLT 25°C, If=15mA M90M03BT01 30pcs





### HTOL 25°C/15mA

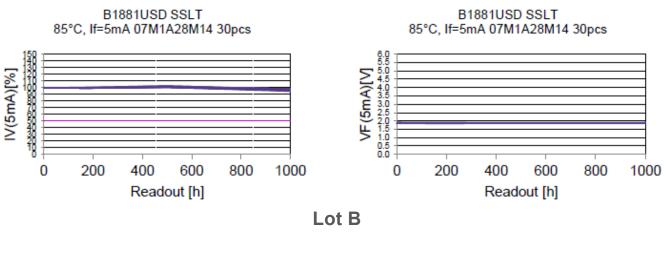
Lot C



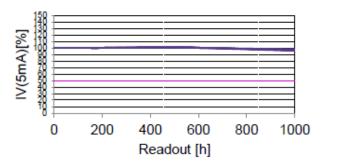


### HTOL 85°C/5mA

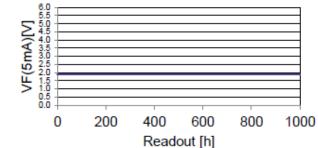
Lot A



B1881USD SSLT 85°C, If=5mA M90M03BT01 30pcs



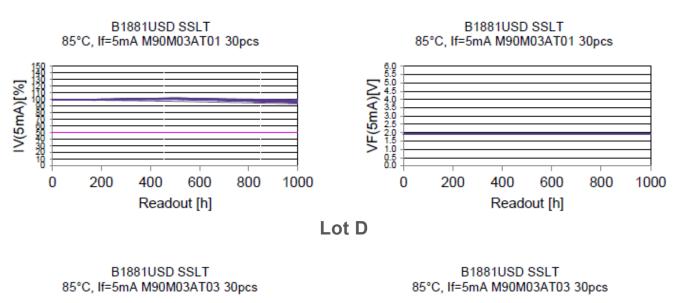
B1881USD SSLT 85°C, If=5mA M90M03BT01 30pcs

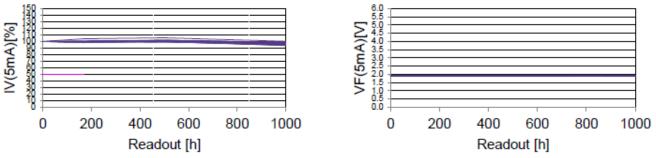




### HTOL 85°C/5mA

Lot C

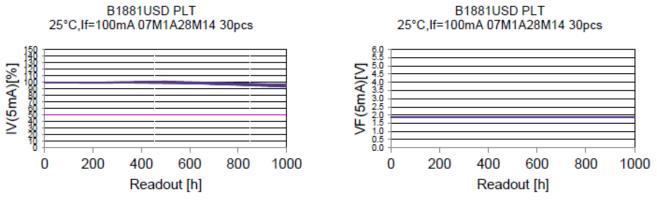




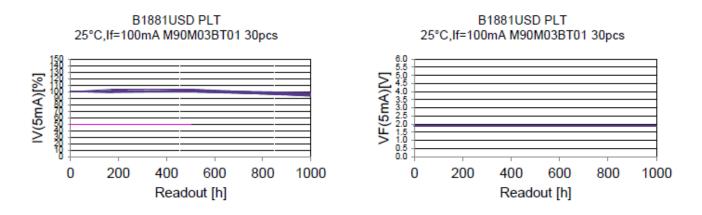


### PLT 25°C/100mA; tp=0,01ms;D=3%

Lot A



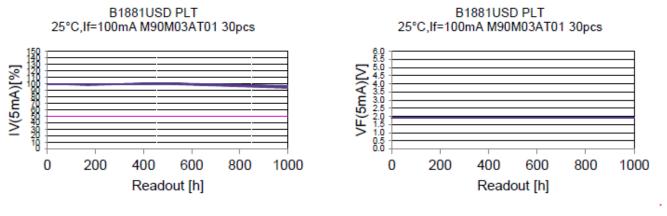
Lot B



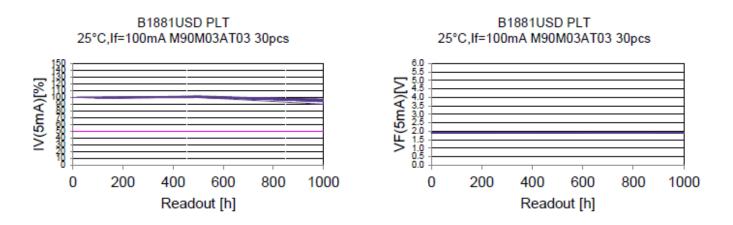


### PLT 25°C/100mA; tp=0,01ms;D=3%

Lot C



Lot D





END OF DOCUMENT

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 Notification
 Material (Q-no.) Q Description

 AO-IN-2022-035-A Q65110A8992
 LR VH9F-M1N1-1-2A3B-2-R18-NK

 AO-IN-2022-035-A Q65110A8088
 LR VH9F-P2R1-1-0-5-R18-Z

 AO-IN-2022-035-A Q65113A2156
 LR VH9F-Q2-1-0-5-R18-Z-XX