



210202985 EFR32MG14 Datasheet Release v1.4

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Effective Date: 2/2/2021

Description of Change

Silicon Labs is pleased to announce the release of datasheet version 1.4 for EFR32MG14.

Reason for Change

The datasheet has been updated to remove erroneous OPNs listed in the ordering table. The specific features and mentions of these erroneous OPNs have been removed from all sections of the datasheet.

Changes in EFR32MG14 Datasheet Revision 1.4:

- Removed EFR32MG14P731F256GM48-B from Table 2.1 Ordering Information on page 3.
- Changed maximum available number of GPIO from 32 to 31 throughout document due to the removal of OPNEFR32MG14P731F256GM48-B.
- Removed section 6.3 QFN48 Sub-GHz Device Pinout.
- Removed PD9 from all pin functionality and pin mapping tables throughout document.

Product Identification

Existing Part #
EFR32MG14P632F256GM32-B
EFR32MG14P632F256GM32-BR
EFR32MG14P632F256GM48-B
EFR32MG14P632F256GM48-BR
EFR32MG14P732F256GM32-B
EFR32MG14P732F256GM32-BR
EFR32MG14P732F256GM48-B
EFR32MG14P732F256GM48-BR
EFR32MG14P732F256IM32-B
EFR32MG14P732F256IM32-BR
EFR32MG14P732F256IM48-B
EFR32MG14P732F256IM48-BR
EFR32MG14P733F256GM48-B
EFR32MG14P733F256GM48-BR
EFR32MG14P733F256IM48-B
EFR32MG14P733F256IM48-BR

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

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Customer Actions Needed:

None.

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