PCN Number: 20220617									PCN Date:	July 18,	
Title:	for select devices										
Custo	omer Contact:	P	CN Manager		Dept:		Quality Se	rvic	es		
Proposed 1 st Ship Date: Sept 22)22		le request epted until		Jul 22, 2022	*	
*Sam	ple requests re	ceived	d after (Jul	22	nd 2022)	will not	be support	ed.			
	ge Type:		-		-						
	Assembly Site				Design				Wafer Bum	p Site	
	Assembly Proce	ess			Data S	neet			Wafer Bum	p Materia	
	Assembly Mate	rials			Part nu	mber cl	nange		Wafer Bum	p Process	
	Mechanical Spe	cificat	ion		Test Si	te			Wafer Fab	Site	
	Packing/Shipping	ng/Lab	peling		Test Pr	ocess			Wafer Fab	Materials	
									Wafer Fab	Process	
					DCN	Detail					
					PCN	Detail	5				
	ription of Cha		sed to anno	Oun				S C	hinPac Kores	a as additi	onal
Texas Assen	ription of Cha s Instruments is mbly Site and u rial differences	pleas	d BOMs for		ce the q	ualificat	ion of STAT		•		onal
Texas Assen	s Instruments is mbly Site and u	pleas pdated are as	d BOMs for follows.	r Se	ce the q	ualificat ices list	ion of STAT	rod	uct Affected'		onal
Texas Assen	s Instruments is mbly Site and u rial differences	pleas pdated are as	d BOMs for follows.	r Se	ce the q elect Dev	ualificat ices list	ion of STAT ed in the "P	rod	uct Affected' Assei	' Section.	onal
Texas Assen Mater	s Instruments is mbly Site and u rial differences Assembly Sit	pleas pdated are as	d BOMs for follows.	r Se ly Si	ce the q elect Dev	ualificat ices list	ion of STAT ed in the "P	rod	e Asser	' Section.	onal
Texas Assen Mater	Instruments is mbly Site and urial differences Assembly Sit Amkor K4	s pleas pdated are as e Korea	Assemble differences	y Si AMF SCk	ce the q elect Dev	Assem	ion of STAT ed in the "P nbly Country (KOR KOR	rod	Asser Gw Ind	' Section. mbly City vangju cheon	
Texas Assen Mater	Assembly Site Amkor K4 STATS ChipPac	s pleas pdated are as e Korea	Assemble differences mkor K4 (y Si AMF SCK	ce the q elect Dev	Assem	ion of STAT ed in the "P hbly Country (KOR KOR KOR	rod	Asser Gw Inc	' Section. mbly City vangju cheon pPac Kor	
Texas Assen Mater S Const	Assembly Site Amkor K4 STATS ChipPac truction and pro-	s pleas pdated are as e Korea	Assemble differences mkor K4 (y Si AMF SCK	ce the q elect Dev	Assem	ion of STAT ed in the "P hbly Country of KOR KOR KOR K4 (New) n/a	rod	Asser Gw Inc	' Section. mbly City vangju cheon pPac Kor	
Texas Assen Mater S Const	Assembly Site Amkor K4 STATS ChipPac truction and pro- inderfill olding process	s pleas pdated are as e Korea	Assemble differences mkor K4 (10135442)	SCK	ce the q elect Dev	Assem	ion of STAT ed in the "P hbly Country (KOR KOR K4 (New) n/a MUF	rod	Asser Gw Inc STATS Chi	' Section. mbly City vangju cheon pPac Kor n/a	
Texas Assen Mater S Const	Assembly Site Amkor K4 STATS ChipPac truction and pro-	s pleas pdated are as e Korea	Assemble differences mkor K4 (SCK	ce the q elect Dev	Assem	ion of STAT ed in the "P hbly Country of KOR KOR KOR K4 (New) n/a	rod	Asser Gw Inc STATS Chi	' Section. mbly City vangju cheon pPac Kor	
Texas Assen Mater S Const	Assembly Site Amkor K4 STATS ChipPac truction and pro- inderfill olding process	s pleas pdated are as e Korea At	Assemble differences mkor K4 (10135442)	SCK	ce the q elect Dev	Assem	ion of STAT ed in the "P hbly Country (KOR KOR K4 (New) n/a MUF	rod	Asser Gw Inc STATS Chi	' Section. mbly City vangju cheon pPac Kor n/a	

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

None

Impact on Environmental Ratings

Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.

RoHS	REACH	Green Status	IEC 62474
	🛛 No Change	🛛 No Change	

Changes to product identification resulting from this PCN:

Assembly Site							
Amkor K4	Assembly Site Origin (22L)	ASO: AMP					
STATS ChipPac Korea	Assembly Site Origin (22L)	ASO: SCK					

Sample product shipping label (not actual product label)





(1P) \$N74L\$07N\$R (Q) 2000 (D) 0336 (31T)LOT: 3959047MLA (4W) TKY(1T) 7523483\$I2 (P) (2P) REV: (V) 0033317

LBL: 5A (L)TO:1750

(2P) REV: (V) 0033317 (20L) CSO: SHE (21L) CCO:USA (22L) ASO: MLA (23L) ACO: MYS

Product Affected:

IWR1443FQAGABL	IWR1843ABGABL	IWR6443ABGABL	IWR6843ABSABLR
IWR1443FQAGABLR	IWR1843ABGABLR	IWR6443AQGABL	IWR6843AQGABL
IWR1642AQAGABL	IWR1843AQGABL	IWR6443AQGABLR	IWR6843AQGABLR
IWR1642AQAGABLR	IWR1843AQGABLR	IWR6843ABGABL	IWR6843AQSABL
IWR1642AQASABL	IWR2243APBGABL	IWR6843ABGABLR	IWR6843AQSABLR
IWR1642AQASABLR	IWR2243APBGABLR	IWR6843ABSABL	XI1843AQGABL

STATS ChipPac Korea Qualification Report

Automotive New Product Qualification Summary (As per AEC-Q100 and JEDEC Guidelines)

Approve Date 17-May-2022

Product Attributes

	1 Todact Attributes			
Attributes	Qualification Device: xWR1843 ES2.0	Qualification Device: xWR2243 ES2.0		
Automotive Grade Level*	Grade 2	Grade 2		
Product Function	-Radar processor	-Radar Processor		
Wafer Fab Supplier	UMC F12	UMC F12		
Assembly Site	SCK	SCK		
Package Type	-FCBGA	FCBGA		
Package Designator	161 ABL	161 ABL		
Ball/Lead Count	205	205		

^{*}All currently qualified product niches in Radar business with ABL package are covered by this qualification: xWR1xxx, xWR2xxx, and xWR6xxx.

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	Test Spec	Lot QTY	SS/Lot	Test Name / Condition	Duration	Results (lots/sample size/fails) xWR1843 ES2.0	Results (lots/sample size/fails) <u>xWR2243 ES2.0</u>
HTOL	JEDEC JESD22- A108	3	77	High Temp Op Life, 140C Tj	1000 Hours	3/231/0	QBS xWR1843
EFR	AEC Q100-	3	800	Early Life Failure Rate,	48 Hours	3/2400/0	QBS xWR1843

	800			140C Tj			
PC	JEDEC J-STD- 020 JESD22- A113	3	26	Preconditioning	Level 3- 260C	3/1677/0**	1/170/0
THB	JEDEC JESD22- A101	3	77	Biased Temperature & Humidity, 85C/85%RH	1000 Hours	3/231/0	QBS xWR1843
TC	JEDEC JESD22- A104 and Appendix 3	3	26	Temperature Cycle, -55/150C	1000 Cycles	3/231/0	1/77/0**
PTC	JEDEC JESD22- A105	1	45	Power Temperature Cycle, -40/125C	1000 Cycles	3/45/0	QBS xWR1843
HTSL	JEDEC JESD22- A103	1	45	High Temp. Storage Bake, 150C	1000 Hours	3/77/0	QBS xWR1843
UHAST	JEDEC JESD22- A118	3	77	Unbiased HAST, 110C/85%RH	264 Hours	3/231/0	QBS xWR1843
PD	JEDEC JESD22- B100 and B108	3	10	Physical Dimensions (Cpk>1.67)	NA	Complete/Pass	QBS xWR1843
SBS	AEC Q100- 010	3	50	Solder Ball Shear (Cpk>1.67)	3 lots / 5 balls per device/10 devices per lot	Complete/Pass	QBS xWR1843
BLR Temp Cycle	JEDEC JESD22- A104	-	-	-40/125C,60 min cyc JESD22-A104, Condition G, Soak Mode 4	1000+ cyc	1/32/0 at 1kcyc (Weibull plot available upon request)	QBS xWR1843

QBS = Qualification By Similarity.

Tests D1 through D5 were completed at silicon technology level. Summary is available upon request.

A1 (PC): Preconditioning:

Performed completed on TC, PTC, UHAST, HTSL, and THB samples.

Green/Pb-free Status:

Qualified Pb-Free(SMT) and Green

^{**15} Post-precondition electrical endpoint test rejects on xWR1843 and 8 Temperature Cycle electrical endpoint test rejects on xWR2243 were discounted due to an Electrically-Induced Physical Damage (EIPD) failure mechanism, common across all units, which is unrelated to the changes under qualification. These do not affect the required AEC-Q100 test quantities shown in the results table, as extra units were populated.

Amkor K4 Qualification Report

Automotive New Product Qualification Summary (As per AEC-Q100 and JEDEC Guidelines)

Approve Date 17-May-2022

Product Attributes

Attributes	Attributes Qualification Device: <u>xWR1843 ES2.0</u>		Qualification Device: xWR1243 ES 3.0	Qualification Device: AWR1642 ES 2.0	Qualification Device: AWR6843 ES 2.0
Automotive Grade Level*	Grade 2	Grade 2	Grade 2	Grade 2	Grade 2
Product Function	Product Function -Radar processor		-Radar Processor	-Radar processor	-Radar processor
Wafer Fab Supplier	Wafer Fab Supplier UMC F12		UMC F12	UMC F12	UMC F12
Assembly Site	Amkor	Amkor	Amkor	Amkor	Amkor
Package Type	Package Type -FCBGA		FCBGA	-FCBGA	-FCBGA
Package Designator 161 ABL		161 ABL	161 ABL	161 ABL	161 ABL
Ball/Lead Count	ead Count 205 205		205	205	205

^{*}Change = From - capillary flip-chip underfill and baseline mold compound, To - new molded underfill (mold and flip-chip underfill are same material). Bump and assembly site is Amkor K4, which was previously qualified and is not changing.

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	Test Spec	Lot QTY	SS/Lot	Test Name / Condition	Duration	Results (lots/sample size/fails) xWR1843 ES2.0	Results (lots/sample size/fails) xWR2243 ES2.0	Results (lots/sample size/fails) xWR1243 ES 3.0	Results (lots/sample size/fails) AWR1642 ES 2.0	Results (lots/sample size/fails) AWR6843 ES 2.0
HTOL	JEDEC JESD22- A108	3	77	High Temp Op Life, 140C Tj	1000 Hours	3/231/0 (SCK lots – same G355S material)	QBS xWR1843	QBS xWR1843	QBS xWR1843	QBS xWR1843
EFR	AEC Q100- 008	3	800	Early Life Failure Rate, 140C Tj	48 Hours	3/2400/0 (SCK lots – same G355S material)	QBS xWR1843	QBS xWR1843	QBS xWR1843	QBS xWR1843
PC	JEDEC J-STD- 020 JESD22- A113	3	26	Preconditioning	Level 3- 260C	3/1600/0	1/100/0	QBS xWR1843+xWR2243	QBS xWR1843	QBS xWR1843
THB	JEDEC JESD22- A101	3	77	Biased Temperature & Humidity, 85C/85%RH	1000 Hours	3/231/0	QBS xWR1843	QBS xWR1843	QBS xWR1843	QBS xWR1843
тс	JEDEC JESD22- A104 and Appendix 3	3	26	Temperature Cycle, - 55/150C	1000 Cycles	3/231/0	1/90/0	QBS xWR1843+xWR2243	QBS xWR1843	QBS xWR1843
PTC	JEDEC JESD22- A105	1	45	Power Temperature Cycle, - 40/125C	1000 Cycles	3/54/0	QBS xWR1843	QBS xWR1843	QBS xWR1843	QBS xWR1843
HTSL	JEDEC JESD22- A103	1	45	High Temp. Storage Bake, 150C	1000 Hours	3/87/0	QBS xWR1843	QBS xWR1843	QBS xWR1843	QBS xWR1843
UHAST	JEDEC JESD22- A118	3	77	Unbiased HAST, 110C/85%RH	264 Hours	3/231/0	QBS xWR1843	QBS xWR1843	QBS xWR1843	QBS xWR1843
PD	JEDEC JESD22- B100 and B108	3	10	Physical Dimensions (Cpk>1.67)	NA	Complete/Pass	QBS xWR1843	QBS xWR1843	QBS xWR1843	QBS xWR1843
SBS	AEC Q100- 010	3	50	Solder Ball Shear (Cpk>1.67)	3 lots / 5 balls per device/10 devices	Complete/Pass	QBS xWR1843	QBS xWR1843	QBS xWR1843	QBS xWR1843

					per lot					
BL Ten Cyc	p JESD22-	-	-	-40/125C,60 min cyc JESD22-A104, Condition G, Soak Mode 4	1000+ cyc	1/32/0 at 1kcyc (Weibull plot available upon request)	1/32/0 at 1kcyc (Weibull plot available upon request)	QBS xWR1243	QBS xWR1843	QBS xWR1843

QBS = Qualification By Similarity.

Tests D1 through D5 were completed at silicon technology level. Summary is available upon request.

A1 (PC): Preconditioning:

Performed completed on TC, PTC, UHAST, HTSL, and THB samples.

Qualification By Similarity Information / Justification:

Green/Pb-free Status:

Qualified Pb-Free(SMT) and Green

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Location	E-Mail
WW PCN Team	PCN www admin_team@list.ti.com

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