| Not | ification Number: | 2022080 |)90 | 000.0 Notification D | ate: | Α | ugust 09, 2022 |
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| Title: Datasheet for TMS320F2833x, TMS320F2823x | | | | | | | |
| Cus | | N Manager | | ., | De | nt: | Quality Services |
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| | Assembly Site | | \neg | Design | | Wafo | r Bump Site |
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| | Assembly Materials | 0 n | 井 | Part number change | H | | r Bump Process |
| | Mechanical Specification | _ | ╣ | Test Site | H | | r Fab Site |
| ☐ Packing/Shipping/Labeling | | | _ | Test Process | H | | r Fab Materials |
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| | | | | ouncing an information only | not / | ificatio | n. |
| | | | | ited as summarized below. | | | |
| The | following change histor | ry provides | s f | | | | |
| | | | | TMS320F28335, TMS320F283 | | | |
| - 4 | TEXAS | | | TMS320F2833 TMS320F28234, TMS320F28234-0 | | | 8235, TMS320F28235-Q1 |
| ~ | INSTRUMENTS | | | | | | 2007 – REVISED AUGUST 2022 |
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| • | | | | S320F2833x, TMS320F2823x L me Microcontrollers | | | |
| | | | | " to "real-time microcontroller". | | | |
| | | | | notice from our substrate supp | | | |
| | | | | | | | |
| | MicroStar BGA™ packaging devices. These devices have now been converted to a New Fine Pitch Ball Grid Array (nFBGA) package. For more information, see the Package Redesign Details section | | | | | | |
| • | | | | tch Ball Grid Array (nFBGA) | | | |
| • | | | | 320F2833x, TMS320F2823x D3 | | | |
| | | | | me MCUs Silicon Errata | | | |
| • | | | | al reference guides with reference | | | |
| | | | | rs Technical Reference Manual | | | |
| • | | | | debug probe" | | | |
| • | | | | d emulation features" to "Advan | | | |
| • | | | | h Ball Grid Array (nFBGA) [ZAY | | | |
| • | | • | | ption "A" | | | |
| | Section 2 (Applications): Updated section. Section 3 (Description): Updated section. Changed Device Information table to Package Information table. | | | | | | |
| Section 3 (Description): Opdated section. Changed Device information table to Package Information table. Added ZAY nFBGA to Package Information table | | | | | | | |
| • | Table 5-1 (F2833x Device (| Comparison |): / | Appended "(UART-compatible)" | to "Se | erial Cor | nmunications Interface |
| | | | | | | | |
| • | <u></u> | | | | | | |
| • | Table 5-2 (F2823x Device 0 | Comparison |): / | Appended "(UART-compatible)" | to "Se | erial Cor | nmunications Interface |
| | | | | | | | |
| • | | | | nging section. Added ZAY to "A" | | | |
| • | (related reducte). Opadiod couldness | | | | | | |
| • | Couldn't in Diagramo, radou in o bail 2711 now into piton bail gira array (in Dorty | | | | | | |
| | Table 6-1 (Signal Descriptions): Added ZAY package | | | | | | |
| | Table 6-1: Updated DESCRIPTION of EMU0, EMU1, and XRS | | | | | | |
| | Section 7.3 (ESD Ratings – Commercial): Add data for ZAY package | | | | | | |
| | Section 7.5.3 (Reducing Current Consumption): Updated list of methods to reduce power consumption38 Section 7.7.4 (ZAY Package): Added table | | | | | | |
| | Section 7.9.2 (Power Sequ | iencina). Hin | dai | ed "No requirements are placed | on th | ne powe | r-up and power-down |
| | | | | | | | |
| sequences" paragraph | | | | | | | |
| | Section 7.9.5: Changed se | ction title fro | ш | Emulator Connection Without | signai | Bullerin | ng for the DSP" to |

| • | Figure 7-27: Changed figure title from "Emulator Connection Without Signal Buffering for the DSP" to "JTAG |
|---|--|
| | Debug Probe Connection Without Signal Buffering for the MCU" |
| • | Figure 7-27 (Emulator Connection Without Signal Buffering for the MCU): Changed "DSC" to "MCU"79 |
| • | Section 7.9.6.8.2 (Synchronous XREADY Timing Requirements (Ready-on-Write, One Wait State)): Restored |
| | footnote92 |
| • | Table 8-14 (SCI-C Registers): Restored footnotes |
| • | Figure 8-15 (Serial Communications Interface (SCI) Module Block Diagram): Updated figure141 |
| • | Figure 8-34 (Watchdog Module): Updated figure |
| • | Section 9.1: Changed title from "TI Design or Reference Design" to "TI Reference Design" |
| • | Section 9.1 (TI Reference Design): Updated section |
| • | Section 10 (Device and Documentation Support): Updated section |
| • | Section 10.1: Changed title from "Getting Started" to "Getting Started and Next Steps". Updated section 182 |
| • | Figure 10-1 (Example of F2833x, F2823x Device Nomenclature): Added 179-ball ZAY package under |
| | PACKAGE TYPE |
| • | Section 10.3 (Tools and Software): Updated section. Updated Design Kits and Evaluation Modules section. Updated Models section. Added Training section |
| • | Section 10.4 (Documentation Support): Added nFBGA Packaging Application Report |
| • | Section 10.4: Added Technical Reference Manual section |
| • | Section 10.4: Updated Peripheral Guides section. Removed most peripheral reference guides as they are now replaced by the <i>TMS320x2833x</i> , <i>TMS320x2823x Real-Time Microcontrollers Technical Reference Manual</i> |
| | |

The datasheet number will be changing.

| Device Family | Change From: | Change To: |
|----------------------------|--------------|------------|
| TMS320F2833x, TMS320F2823x | SPRS439P | SPRS439Q |

These changes may be reviewed at the datasheet links provided.

https://www.ti.com/product/TMS320F28335

Error! Bookmark not defined.

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this notification:

None.

Product Affected:

| SA320F28335ZJZQ | SP320F28335ZJZQR | TMS320F28232PTPQ | TMS320F28234PTPQ |
|------------------|------------------|--------------------|-------------------|
| TMS320F28234ZJZQ | TMS320F28235PTPQ | TMS320F28235ZJZQ | TMS320F28235ZJZQR |
| TMS320F28335PTPQ | TMS320F28335ZJZQ | TMS320F28335ZJZQCR | TMS320F28335ZJZQR |

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| Location | E-Mail | | | |
|-------------|--------------------------------|--|--|--|
| WW PCN Team | PCN www admin_team@list.ti.com | | | |

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