

Product Discontinuation Notice for Development Systems Products

XCN16007 (v1.0) January 25, 2016

Product Discontinuation Notice

Overview

The purpose of this notification is to communicate that Xilinx[®] is discontinuing Development Systems Virtex UltraScale VCU110 ES Development Kit part numbers and replacing them with equivalent VCU110 ES3 part numbers.

Description

As part of Xilinx regular product pruning activities, the following products have been identified for obsolescence. This notice should be used in conjunction with previous discontinuance notices which can be found at Xilinx.com.

Products Affected

Virtex UltraScale FPGA VCU110 Development Kit

Table 1: Virtex UltraScale FPGA VCU110 Development Kit

Part Number	Description	Replacement Part
DK-U1-VCU110-ES-G	Xilinx Virtex UltraScale FPGA VCU110 Development Kit	DK-U1-VCU110-ES3-G
DK-U1-VCU110-ES-G-J	Xilinx Virtex UltraScale FPGA VCU110 Development Kit, Japan Specific	DK-U1-VCU110-ES3-G-J

NOTE: The last version of tools supporting ES DK-U1-VCU110-ES-G and DK-U1-VCU110-ES-G-J is Vivado version 2016.1.

Key Dates and Ordering Information

Virtex UltraScale FPGA VCU110 Development Kit

Final orders accepted until February 15, 2016 or until inventory is exhausted whichever comes first. Final shipments will continue until February 29, 2016

Technical Support

For Technical Support on these products refer to Solution Center Xilinx Answer Record 40174. Support for these products will be limited to tools usage and existing documentation.

Response

Customers are requested to place last-time-buy orders in a timely manner prior to the key dates listed above to avoid product availability issues. If you anticipate that your last time buy requirements will exceed the current rate, please provide your Xilinx Sales Representative with a forecast of the requirements. All orders will be supported on a first-come, first-served basis. All orders placed within 60 days of the last time buy date will be non-cancellable and non-returnable (NCNR). For additional information or questions, contact your local Xilinx Sales representative.

Important Notice: Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the Support website (http://www.xilinx.com/support). Register today and personalize your "Documentation and Design Advisory Alerts" area to include Customer Notifications. Xilinx Support provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as

© Copyright 2016 Xilinx, Inc. Xilinx, the Xilinx logo, Artix, ISE, Kintex, Spartan, Virtex, Vivado, Zynq, and other designated brands included herein are trademarks of Xilinx in the United States and other countries. All other trademarks are the property of their respective owners.



alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to Xilinx Answer Record 18683.

Revision History

The following table shows the revision history for this document.

Date	Version	Revision
01/25/2016	1.0	Initial release

Notice of Disclaimer

The information disclosed to you hereunder (the "Materials") is provided solely for the selection and use of Xilinx products. To the maximum extent permitted by applicable law: (1) Materials are made available "AS IS" and with all faults, Xilinx hereby DISCLAIMS ALL WARRANTIES AND CONDITIONS, EXPRESS, IMPLIED, OR STATUTORY, INCLUDING BUT NOT LIMITED TO WARRANTIES OF MERCHANTABILITY, NON-INFRINGEMENT, OR FITNESS FOR ANY PARTICULAR PURPOSE; and (2) Xilinx shall not be liable (whether in contract or tort, including negligence, or under any other theory of liability) for any loss or damage of any kind or nature related to, arising under, or in connection with, the Materials (including your use of the Materials), including for any direct, indirect, special, incidental, or consequential loss or damage (including loss of data, profits, goodwill, or any type of loss or damage suffered as a result of any action brought by a third party) even if such damage or loss was reasonably foreseeable or Xilinx had been advised of the possibility of the same. Xilinx assumes no obligation to correct any errors contained in the Materials or to notify you of updates to the Materials or to product specifications. You may not reproduce, modify, distribute, or publicly display the Materials without prior written consent. Certain products are subject to the terms and conditions of Xilinx's limited warranty, please refer to Xilinx's Terms of Sale which can be viewed at http://www.xilinx.com/legal.htm#tos; IP cores may be subject to warranty and support terms contained in a license issued to you by Xilinx. Xilinx products are not designed or intended to be fail-safe or for use in any application requiring fail-safe performance; you assume sole risk and liability for use of Xilinx products in such critical applications, please refer to Xilinx's Terms of Sale which can be viewed at http://www.xilinx.com/legal.htm#tos.