

SPC5-UDESTK JTAG debugger for SPC5 MCUs



Features

- SPC5-UDESTK USB/JTAG adapter working with PLS UDE (universal debug engine) starter kit version
- Supports SPC5x automotive microcontroller product families
- · Supports multicore eTPU and GTM debugging
- Provides FLASH programming of on-chip FLASH featuring erase/program/verify
- · Status LED's indicate the target's IO voltage, connection state and running state
- Supports service provided through STMicroelectronics

Description

The SPC5-UDESTK is a USB/JTAG interface to enable the debugging and programming at budget price of SPC5x MCUs.

SPC5-UDESTK is fully compliant with IEEE1149.1 JTAG protocol.

SPC5-UDESTK supports you while building applications and you can run and test your software in a convenient and cost-efficient way.

It offers a collection of tools including source file management, project building and powerful HLL debugger with high-speed communication paths to the customer's hardware target system with target monitor.

There are three kit bundle versions available including the SPC5-UDESTK USB/ JTAG adapter and the debugger SW.

It includes a perpetual, full-feature, limited code-size (256 kBytes) license.

 ${\tt SPC5-UDEDEBG-TL}\ is\ a\ one-year,\ full-feature,\ unlimited\ code-size\ license.$

SPC5-UDEDEBG is a perpetual, full-feature, unlimited code-size license.

SPC5-UDESTK can be ordered directly from ST or ST franchised distributors.

To download the debugger software and to activate license go to pls-mc.com web site.

The SPC5-UDESTK debugger is accessible via ST's free integrated development environment, SPC5Studio, which can be downloaded from ST WEB (free download).

ST Community is available on ST.COM.

Product status link

SPC5-UDESTK

Product summary		
Order code	Reference	
SPC5-UDESTK- EVAL	SPC-UDESTK HW adapter plus perpetual, full-feature free of charge license, but with code size limitation to 256 kB	
SPC5- UDEDEBG-TL ⁽¹⁾	One-year, full-feature, unlimited code-size licence	
SPC5- UDEDEBG ⁽¹⁾	Perpetual, full-feature, unlimited code-size licence	

1. Require SPC5-UDESTK-EVAL



1 System requirements, HW and SW resources

1.1 System requirements

- Windows PC (Windows 10, 7, or 8)
- USB type A to mini-B cable

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2 Device features

Some features may be available only for specific devices and software version. Multicore debug is available only for low and mid end devices.

Please refer to relevant release notes.

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Revision history

Table 1. Document revision history

Date	Version	Changes
22-May-2013	1	Initial release.
		Updated Features list
10-Jun-2013	2	Section 1.1: "SPC5-UDESTK starter kit details":
		- Removed GTM debugging
02-Sep-2013	3	Updated all document.
17-Sep-2013	4	Updated Disclaimer.
01-Oct-2013	5	Updated Description.
08-May2015	6	Updated Cover page and added Section 1: System requirements, HW and SW resources.
06-Aug-2015	7	Typos.
23-Nov-2016	8	Changed SPC56 and SPC57 in SPC5x.
		Updated Features and Description.
01-Aug-2018	9	Updated Section 1.1: System requirements.
		Minor text changes.
12-Jul-2019	10	Updated Description.
05-May-2020	11	Updated Description and Product summary table.
12-May-2020	12	Updated Section Description
22-Oct-2020	13	Updated description and in cover page.
22-06(-2020		Minor text changes.

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