

Data brief

Evaluation board based on the ST1PS01EJR 400 mA nano-quiescent synchronous step-down converter





Features

- 94% typical efficiency at 1 mA load (V_{IN}=3.6 V, V_{OUT}=3.3 V)
- 1.8 V to 5.5 V input operating range
- · Up to 400 mA output current capability
- Tiny external components: L=2.2 µH typ.
- Selectable output voltages: 1.8 V to 3.3 V
- Output voltage Power Good
- Dynamic output voltage selection (D0, D1)
- Suitable for the following applications:
 - Wearable applications
 - Personal tracking monitors
 - Smart watches, sport bands
 - Energy harvesting, wireless sensors
 - Wearable and fitness accessories
 - Industrial sensors, portable low power devices
 - Single cell Li-lon battery applications
 - Bluetooth[®] low energy
 - ZigBee[®]
- · Hardware is WEEE and RoHS compliant

Product summary		
Evaluation board for ST1PS01 step-down converter	STEVAL-1PS01EJR	
400 mA nano- quiescent synchronous step- down converter with digital voltage selection and power good	ST1PS01	

Description

The STEVAL-1PS01EJR evaluation board features the ST1PS01 nano-quiescent miniaturized synchronous step-down converter that is designed for applications where high efficiency, PCB size and thickness are key factors.

The converter can provide up to 400 mA output current with an input voltage ranging from 1.8 V to 5.5 V. The output voltage can be dynamically adjusted from 1.8 V to 3.3 V using two digital control inputs.

Thanks to the enhanced peak current control (PCC), the ST1PS01 can achieve very high conversion efficiency using only a 2.2 µH inductor and two small capacitors.

The advanced design circuitry minimizes quiescent current.



1 Block diagram

□PGOOD VIN 🗖 - UVLO Comp. **□**∨out EN 🗖 Peak and Valley CURRENT LIMIT Driver/ Min. ON Time CTRL Anti-cross 🗖 sw D1 📮 conduction Logic Min. OFF Time circuitry D0 🗖 Pulse Freq. Detection 100% Mode Comp. VFB TON Compensation \pm 1 GND Vth100% → +

Figure 1. STEVAL-1PS01EJR block diagram

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2 Schematic diagrams

CN2 VIN_F 1 ST1PS01EJR O TP2 CN5 R1 Header 3 B2 EN PGOOD E1 2 CN1 JP1 VIN F VIN S GND S GND F sw А3 Header 2 VIN CN6 3 VOUT F VOUT S GND_S GND_F C1_ C2_ VOUT 4 3 2 1 C4_ C3_ Header 4 GND D2 CN4 Header 4 D0D1 VIN_F 3 A1C1 2 Header 3 CN3 VIN_F 2 Header 3 =

Figure 2. STEVAL-1PS01EJR schematic

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Revision history

Table 1. Document revision history

Date	Version	Changes
21-Dec-2018	1	Initial release.
17-Jun-2019	2	Updated features and photo in cover page.

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