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(619) 593-5050

Application:

Ultra Low Resistance

Portable Electronics: SMART PHONE, Tablet PC and Power Bank, etc.

USB 3.0

Product Features:

Lo-Rho internal resistance

Small surface mount, Solid State

Faster time to trip than standard SMD devices Lower resistance than standard SMD devices

Operation Current: 250mA ~ 1A **Maximum Voltage:** 6V - 9VDC **Temperature Range:** -40°C to 85°C.

Agency Standards and Listings:







Note: R0603LR-100-R UL and TUV pending

Electrical Characteristics (23°C)

	Hold Current	Trip Current	Rated Voltage	Max Current	Typical Power	Max Time to Trip		Resistance Tolerance	
Part Number						Current	Time	R _{MIN}	R1 _{MAX}
Number	I _H , A	I _T , A	V _{MAX} , Vdc	I _{MAX} , A	Pd, W	Amp	Sec	Ω	Ω
R0603LR-025-R	0.25	0.55	9	100	0.5	8.0	0.08	0.500	3.000
R0603LR-035-R	0.35	0.75	6	100	0.5	8.0	0.10	0.200	1.000
R0603LR-050-R	0.50	1.00	6	100	0.6	8.0	0.10	0.070	0.350
R0603LR-075-R	0.75	1.50	6	100	0.6	8.0	0.20	0.050	0.250
R0603LR-100-R	1.00	1.80	6	100	0.6	8.0	0.30	0.040	0.120

 I_H = Hold Current – Maximum current at which the device will not trip at 23°C still air.

 I_T = Trip Current – Minimum current at which the device will always trip at 23°C still air.

 V_{MAX} = Maximum voltage device can withstand without damage at it's rated current.

 I_{MAX} = Maximum fault current device can withstand without damage at rated voltage (V max).

Pd = Typical power dissipated from device when in the tripped state in 23°C still air environment.

 $\mathbf{R}_{\mathbf{MIN}}$ = Minimum device resistance at 23°C.

 $R1_{MAX}$ = Maximum device resistance at 23°C, 1 hour after tripping.

Warning:

-Operation beyond the specified maximum ratings or improper use may result in damage and possible electrical arcing and/or flame.



-PPTC device are intended for occasional overcurrent protection. Application for repeated overcurrent condition and/or prolonged trip are not anticipated.

-Avoid contact of PPTC device with chemical solvent. Prolonged contact may damage the device performance.

Note: All specifications subject to change without notice.

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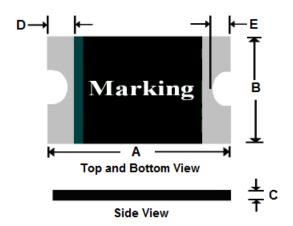
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Physical Specifications:

Termination Pad Characteristics: Pure Tin

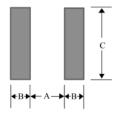
R0603LR: Product Dimensions (millimeters)



Part	A		В		C		D		E	
Number	Min	Max								
R0603LR-025-R	1.40	1.80	0.45	1.00	0.35	0.75	0.10	0.50	0.08	0.40
R0603LR-035-R	1.40	1.80	0.45	1.00	0.35	0.75	0.10	0.50	0.08	0.40
R0603LR-050-R	1.40	1.80	0.45	1.00	0.35	0.75	0.10	0.50	0.08	0.40
R0603LR-075-R	1.40	1.80	0.45	1.00	0.35	0.75	0.10	0.50	0.08	0.40
R0603LR-100-R	1.40	1.80	0.45	1.00	0.35	0.75	0.10	0.50	0.08	0.40

Pad Layouts - Solder Reflow and Rework Recommendations

The dimensions in the table below provide the recommended pad layout for each R0603LR device.



Pad Dimensions (millimeters)					
A – Nominal – 0.80 mm					
B – Nominal – 0.60 mm					
C – Nominal – 0.80 mm					

Standard Package

4K Reel/Tape

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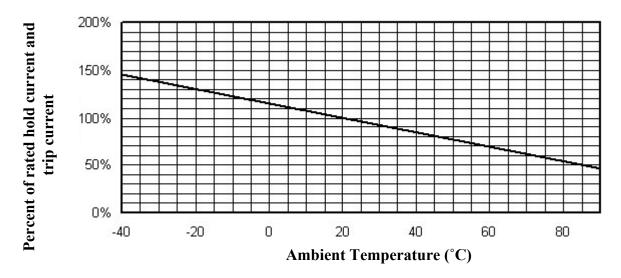
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Thermal Derating Curve – Type R0603LR



Typical Time-To-Trip at 23°C

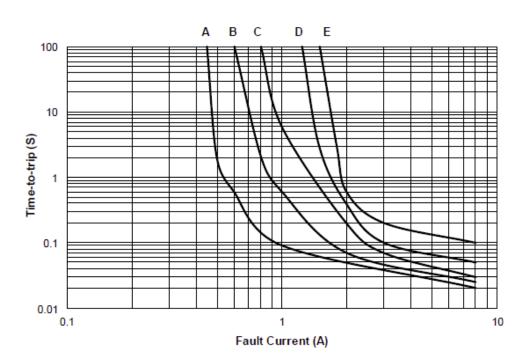


B = R0603LR-035

C = R0603LR-050

D = R0603LR-075

E = R0603LR-100



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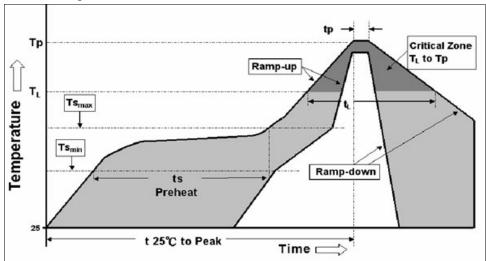
Profile Features	Pb-Free Assembly			
Average Ramp-Up Rate (Tsmax to Tp)	3 °C/second max.			
Preheat:				
Temperature Min (Tsmin)	150 °C			
Temperature Max (Tsmax)	200 °C			
Time (Tsmin to Tsmax)	60-180 seconds			
Time maintained above:				
Temperature (T _L)	217 °C			
Time (t _L)	60-150 seconds			
Peak/Classification Temperature (Tp):	260 °C			
Time within 5 °C of actual Peak:				
Temperature (tp)	20-40 seconds			
Ramp-Down Rate:	6 °C/second max.			
Time 25 °C to Peak Temperature:	8 minute max.			

Solder reflow

- * Due to "Lead Free" nature, Temperature and Dwelling time for the soldering zone is higher than those for Regular. This may cause damage to other components.
 - 1. Recommended maximum paste thickness > 0.25mm.
 - 2. Devices can be cleaned using standard industry methods and aqueous solvents.
 - 3. Rework use standard industry practices.
 - 4. Storage Environment: < 30°C / 60%RH

Caution:

- 1. If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements.
- 2. Devices are not designed to be wave soldered to the bottom side of the board.



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