

### Features

- Trench Power LV MOSFET Technology
- Excellent Package for Heat Dissipation
- High Density Cell Design for Low  $R_{\text{DS}(\text{on})}$
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- Moisture Sensitivity Level 1

#### **Maximum Ratings**

- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 41.5°C/W Junction to Ambient<sup>(2)</sup>

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	40	V
Gate-Source Volltage	V <sub>GS</sub>	±20	V
Continuous Drain Current	I <sub>D</sub>	25	Α
Pulsed Drain Current <sup>(3)</sup>	I <sub>DM</sub>	100	Α
Total Power Dissipation	P <sub>D</sub>	3	W

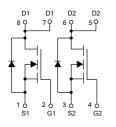
Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

2. Surface Mounted on FR4 Board, t  $\leq$  10 sec.

3. Repetitive Rating: Pulse width limited by maximum junction temperature.

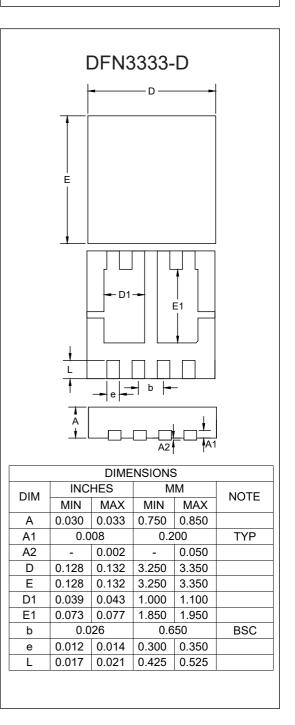
# Internal Structure and Marking Code





pin1

# Dual N-CHANNEL MOSFET



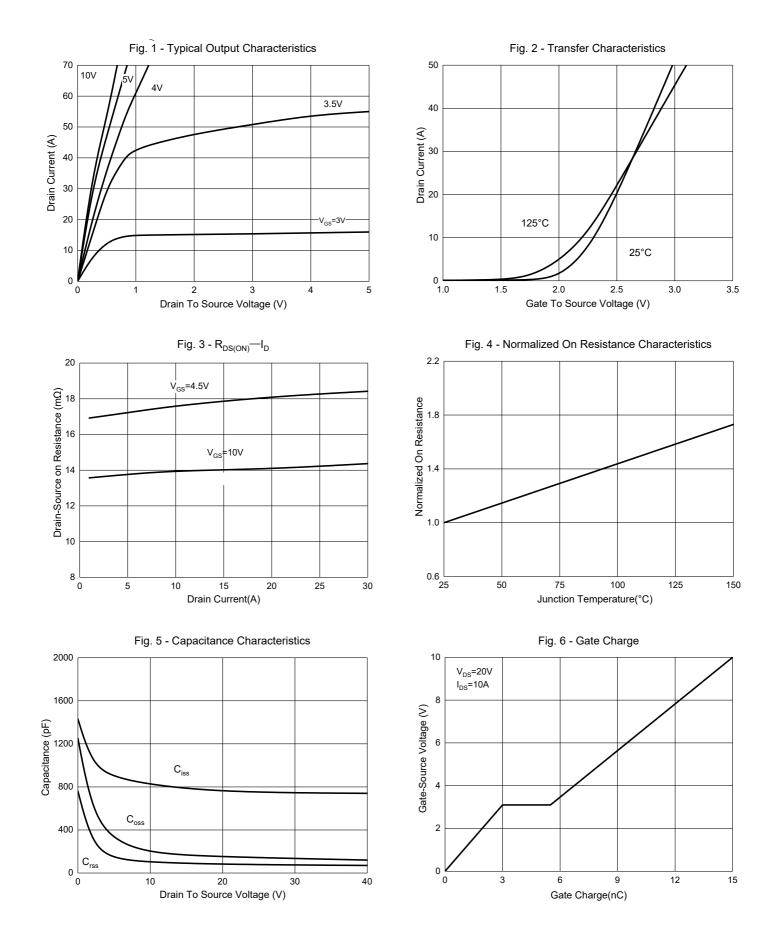


## Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics			<b>I</b>	1	1	I	
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250µA	40			V	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V			1	μA	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1	1.5	2.5	V	
Drain-Source On-Resistance		V <sub>GS</sub> =10V, I <sub>D</sub> =8A		14	18	mΩ	
	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A		18	24	mΩ	
Diode Characteristics							
Continuous Body Diode Current	I <sub>S</sub>				25	A	
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =10A			1.2	V	
Reverse Recovery Time	t <sub>rr</sub>	- I <sub>S</sub> =10A,di/dt=100A/μs		29		ns	
Reverse Recovery Charge	Q <sub>rr</sub>			26		nC	
Dynamic Characteristics			·				
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =20V,V <sub>GS</sub> =0V,f=1MHz		750			
Output Capacitance	C <sub>oss</sub>			150		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			80			
Total Gate Charge	Qg			15			
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =20V,V <sub>GS</sub> =10V,I <sub>D</sub> =10A		3		nC	
Gate-Drain Charge	Q <sub>gd</sub>			2.5			
Turn-On Delay Time	t <sub>d(on)</sub>			6			
Turn-On Rise Time	t <sub>r</sub>	$V_{DS}=20V, V_{GEN}=10V,$ - R <sub>G</sub> =3Ω, R <sub>L</sub> =1Ω, I <sub>DS</sub> =2A		17.5		ns	
Turn-Off Delay Time	t <sub>d(off)</sub>			31		113	
Turn-Off Fall Time	t <sub>f</sub>			17			

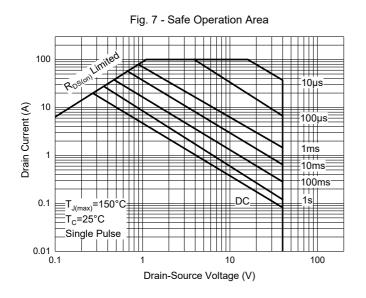


# **Curve Characteristics**





## **Curve Characteristics**





## **Ordering Information**

Device	Packing	
Part Number-TP	Tape&Reel: 5Kpcs/Reel	

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