Octal bus transceiver; 3-state Rev. 6 — 3 July 2020

### 1. General description

The 74AHC245; 74AHCT245 is an 8-bit transceiver with 3-state outputs. The device features an output enable (OE) and send/receive (DIR) for direction control. A HIGH on OE causes the outputs to assume a high-impedance OFF-state. Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

### 2. Features and benefits

- Wide supply voltage range from 2.0 V to 5.5 V
- Balanced propagation delays •
- All inputs have Schmitt-trigger action
- Overvoltage tolerant inputs to 5.5 V
- High noise immunity
- CMOS low power dissipation
- Input levels:
  - For 74AHC245: CMOS level
  - For 74AHCT245: TTL level
- ESD protection:
  - HBM JESD22-A114F exceeds 2000 V
  - MM JESD22-A115-A exceeds 200 V
  - CDM JESD22-C101E exceeds 1000 V
  - Latch-up performance exceeds 100 mA per JESD 78 Class II Level A
- Multiple package options
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

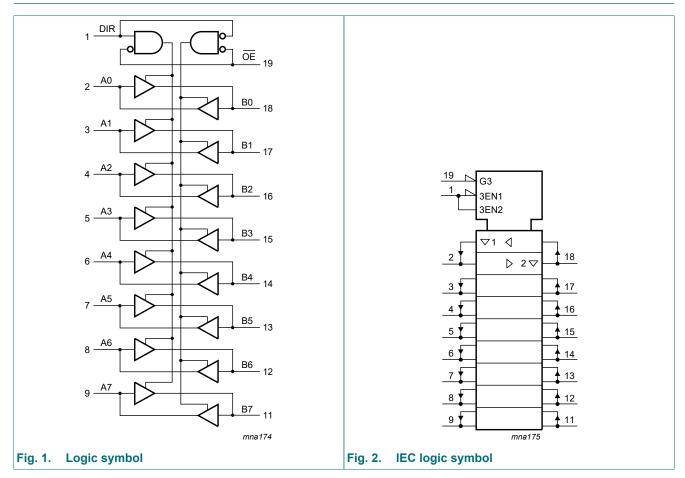
### 3. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
74AHC245D	-40 °C to +125 °C	SO20	plastic small outline package; 20 leads;	SOT163-1
74AHCT245D			body width 7.5 mm	
74AHC245PW	-40 °C to +125 °C	TSSOP20	plastic thin shrink small outline package;	SOT360-1
74AHCT245PW			20 leads; body width 4.4 mm	
74AHC245BQ	-40 °C to +125 °C	DHVQFN20	plastic dual in-line compatible thermal	SOT764-1
74AHCT245BQ			enhanced very thin quad flat package; no leads; 20 terminals; body 2.5 × 4.5 × 0.85 mm	

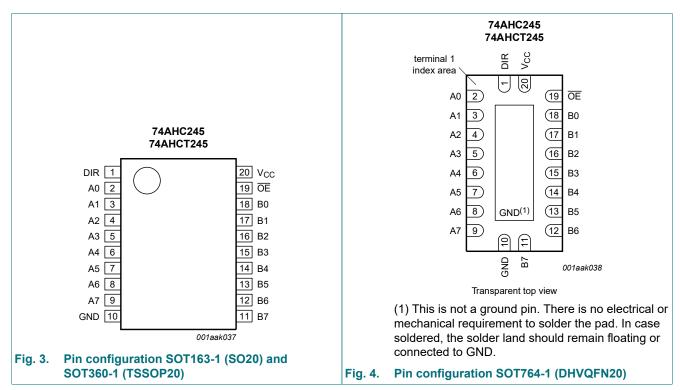
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### Octal bus transceiver; 3-state

# 4. Functional diagram



### 5. Pinning information



### 5.1. Pinning

### 5.2. Pin description

### Table 2. Pin description

Symbol	Pin	Description
DIR	1	direction control input
A0, A1, A2, A3, A4, A5, A6, A7	2, 3, 4, 5, 6, 7, 8, 9	data input/output
GND	10	ground (0 V)
B0, B1, B2, B3, B4, B5, B6, B7	18, 17, 16, 15, 14, 13, 12, 11	data input/output
ŌE	19	output enable input (active LOW)
V <sub>CC</sub>	20	supply voltage

### 6. Functional description

### Table 3. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

		Input/output				
OE	DIR	An	Bn			
L	L	A = B	inputs			
L	Н	inputs	B = A			
Н	Х	Z	Z			

# 7. Limiting values

#### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage			-0.5	+7.0	V
VI	input voltage			-0.5	+7.0	V
I <sub>IK</sub>	input clamping current	V <sub>1</sub> < -0.5 V	[1]	-20	-	mA
I <sub>OK</sub>	output clamping current	$V_{\rm O}$ < -0.5 V or $V_{\rm O}$ > $V_{\rm CC}$ + 0.5 V	[1]	-20	+20	mA
I <sub>O</sub>	output current	$V_{\rm O}$ = -0.5 V to (V <sub>CC</sub> + 0.5 V)		-25	+25	mA
I <sub>CC</sub>	supply current			-	+75	mA
I <sub>GND</sub>	ground current			-75	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = -40 °C to +125 °C	[2]	-	500	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT163-1 (SO20) package: P<sub>tot</sub> derates linearly with 12.3 mW/K above 109 °C.

For SOT360-1 (TSSOP20) package:  $P_{tot}$  derates linearly with 10.0 mW/K above 100  $^\circ\text{C}.$ 

For SOT764-1 (DHVQFN20) package: Ptot derates linearly with 12.9 mW/K above 111 °C.

### 8. Recommended operating conditions

### Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	7	4AHC24	5	7	4AHCT24	45	Unit
			Min	Тур	Max	Min	Тур	Мах	]
V <sub>CC</sub>	supply voltage		2.0	5.0	5.5	4.5	5.0	5.5	V
VI	input voltage		0	-	5.5	0	-	5.5	V
Vo	output voltage		0	-	V <sub>CC</sub>	0	-	V <sub>CC</sub>	V
T <sub>amb</sub>	ambient temperature		-40	+25	+125	-40	+25	+125	°C
Δt/ΔV	input transition rise and	$V_{CC} = 3.3 V \pm 0.3 V$	-	-	100	-	-	-	ns/V
	fall rate	$V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	-	-	20	-	-	20	ns/V

# 9. Static characteristics

### Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C t	o +85 °C	-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Мах	Min	Max	
74AHC2	45	I						1	1	1
V <sub>IH</sub>	HIGH-level	V <sub>CC</sub> = 2.0 V	1.5	-	-	1.5	-	1.5	-	V
	input voltage	V <sub>CC</sub> = 3.0 V	2.1	-	-	2.1	-	2.1	-	V
		V <sub>CC</sub> = 5.5 V	3.85	-	-	3.85	-	3.85	-	V
V <sub>IL</sub>	LOW-level	V <sub>CC</sub> = 2.0 V	-	-	0.5	-	0.5	-	0.5	V
	input voltage	V <sub>CC</sub> = 3.0 V	-	-	0.9	-	0.9	-	0.9	V
		V <sub>CC</sub> = 5.5 V	-	-	1.65	-	1.65	-	1.65	V
V <sub>OH</sub>	HIGH-level	$V_{I} = V_{IH} \text{ or } V_{IL}$								
	output voltage	I <sub>O</sub> = -50 μΑ; V <sub>CC</sub> = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I <sub>O</sub> = -50 μΑ; V <sub>CC</sub> = 3.0 V	2.9	3.0	-	2.9	-	2.9	-	V
		I <sub>O</sub> = -50 μΑ; V <sub>CC</sub> = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.58	-	-	2.48	-	2.40	-	V
		I <sub>O</sub> = -8.0 mA; V <sub>CC</sub> = 4.5 V	3.94	-	-	3.80	-	3.70	-	V
V <sub>OL</sub>	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}$								
	output voltage	I <sub>O</sub> = 50 μA; V <sub>CC</sub> = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 50 μA; V <sub>CC</sub> = 3.0 V	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 50 μA; V <sub>CC</sub> = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	-	0.44	-	0.55	V
		I <sub>O</sub> = 8.0 mA; V <sub>CC</sub> = 4.5 V	-	-	0.36	-	0.44	-	0.55	V
I	input leakage current	V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA
I <sub>OZ</sub>	OFF-state output current	$V_I = V_{IH} \text{ or } V_{IL};$ $V_O = V_{CC} \text{ or GND}; V_{CC} = 5.5 \text{ V}$	-	-	±0.25	-	±2.5	-	±10.0	μA
I <sub>CC</sub>	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	4.0	-	40	-	80	μA
CI	input capacitance	V <sub>I</sub> = V <sub>CC</sub> or GND	-	3	10	-	10	-	10	pF
Co	output capacitance		-	4	-	-	-	-	-	pF

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Symbol	Parameter	Conditions		25 °C		-40 °C t	o +85 °C	-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
74AHCT	245									
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 4.5 V to 5.5 V	2.0	-	-	2.0	-	2.0	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 4.5 V to 5.5 V	-	-	0.8	-	0.8	-	0.8	V
V <sub>OH</sub>	HIGH-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	Ι <sub>Ο</sub> = -50 μΑ	4.4	4.5	-	4.4	-	4.4	-	V
		l <sub>O</sub> = -8.0 mA	3.94	-	-	3.80	-	3.70	-	V
V <sub>OL</sub>	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	l <sub>O</sub> = 50 μA	-	0	0.1	-	0.1	-	0.1	V
		l <sub>O</sub> = 8.0 mA	-	-	0.36	-	0.44	-	0.55	V
l <sub>l</sub>	input leakage current	V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA
I <sub>OZ</sub>	OFF-state output current	$V_I = V_{IH} \text{ or } V_{IL};$ $V_O = V_{CC} \text{ or GND}; V_{CC} = 5.5 \text{ V}$	-	-	±0.25	-	±2.5	-	±10.0	μA
I <sub>CC</sub>	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	4.0	-	40	-	80	μA
ΔI <sub>CC</sub>	additional supply current	per input pin; $V_I = V_{CC} - 2.1 V$ ; other pins at $V_{CC}$ or GND; $I_O = 0 A$ ; $V_{CC} = 4.5 V$ to 5.5 V	-	-	1.35	-	1.5	-	1.5	mA
Cı	input capacitance	V <sub>I</sub> = V <sub>CC</sub> or GND	-	3	10	-	10	-	10	pF
Co	output capacitance		-	4	-	-	-	-	-	pF

# **10.** Dynamic characteristics

### Table 7. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 7.

Symbol	Parameter	Conditions		25 °C		-40 °C t	o +85 °C	-40 °C to	o +125 °C	Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	1
74AHC2	45		I						_	
t <sub>pd</sub>	propagation delay	An to Bn; Bn to An; [2] see Fig. 5								
		V <sub>CC</sub> = 3.0 V to 3.6 V								
		C <sub>L</sub> = 15 pF	-	5.0	8.4	1.0	10.0	1.0	10.5	ns
		C <sub>L</sub> = 50 pF	-	6.5	11.9	1.0	13.5	1.0	15.0	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V								
		C <sub>L</sub> = 15 pF	-	3.5	5.5	1.0	6.5	1.0	7.0	ns
		C <sub>L</sub> = 50 pF		5.0	7.5	1.0	8.5	1.0	9.5	ns
t <sub>en</sub>	enable time	OE to An; OE to Bn;[3]see Fig. 6								
		V <sub>CC</sub> = 3.0 V to 3.6 V								
		C <sub>L</sub> = 15 pF	-	6.5	13.2	1.0	15.5	1.0	16.5	ns
		C <sub>L</sub> = 50 pF	-	9.0	16.7	1.0	19.0	1.0	21.0	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V								
		C <sub>L</sub> = 15 pF	-	4.0	8.5	1.0	10.0	1.0	11.0	ns
		C <sub>L</sub> = 50 pF	-	5.0	10.6	1.0	12.0	1.0	13.5	ns
t <sub>dis</sub>	disable time	OE to An; OE to Bn;[4]see Fig. 6								
		V <sub>CC</sub> = 3.0 V to 3.6 V								
		C <sub>L</sub> = 15 pF	-	7.5	12.5	1.0	15.5	1.0	16.0	ns
		C <sub>L</sub> = 50 pF	-	10.0	15.8	1.0	18.0	1.0	20.0	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V								
		C <sub>L</sub> = 15 pF	-	4.5	7.8	1.0	9.2	1.0	10.0	ns
		C <sub>L</sub> = 50 pF	-	6.0	9.7	1.0	11.0	1.0	12.5	ns
C <sub>PD</sub>	power dissipation capacitance	$f_i = 1 \text{ MHz}; V_I = \text{GND to } V_{\text{CC}}$ [5]	-	12	-	-	-	-	-	pF

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Symbol	Parameter	Conditions		25 °C		-40 °C t	o +85 °C	-40 °C to	o +125 ℃	Unit
			Min	Typ[1]	Мах	Min	Max	Min	Мах	
74AHCT	245; V <sub>CC</sub> = 4.5	V to 5.5 V								
t <sub>pd</sub>	propagation delay	An to Bn; Bn to An; [2] see Fig. 5								
		C <sub>L</sub> = 15 pF	-	3.5	7.7	1.0	8.5	1.0	10.0	ns
		C <sub>L</sub> = 50 pF	-	4.5	8.7	1.0	9.5	1.0	11.0	ns
t <sub>en</sub>	enable time	OE to An; OE to Bn;       [3]         see Fig. 6								
		C <sub>L</sub> = 15 pF	-	5.0	13.8	1.0	15.0	1.0	17.5	ns
		C <sub>L</sub> = 50 pF	-	6.0	14.8	1.0	16.0	1.0	18.5	ns
t <sub>dis</sub>	disable time	OE to An; OE to Bn;[4]see Fig. 6								
		C <sub>L</sub> = 15 pF	-	5.0	14.4	1.0	15.5	1.0	18.0	ns
		C <sub>L</sub> = 50 pF	-	6.0	15.4	1.0	16.5	1.0	19.5	ns
C <sub>PD</sub>	power dissipation capacitance	$f_i = 1 \text{ MHz}; V_1 = \text{GND to } V_{\text{CC}}$ [5]	-	15	-	-	-	-	-	pF

Typical values are measured at nominal supply voltage ( $V_{CC}$  = 3.3 V and  $V_{CC}$  = 5.0 V). [1]

[2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .

[2]  $t_{pd}$  is the same as  $t_{PL}$  and  $t_{PL}$ . [3]  $t_{en}$  is the same as  $t_{PZL}$  and  $t_{PZH}$ . [4]  $t_{dis}$  is the same as  $t_{PLZ}$  and  $t_{PHZ}$ . [5]  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu$ W).  $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$  where:

 $f_i$  = input frequency in MHz;

 $f_o$  = output frequency in MHz;

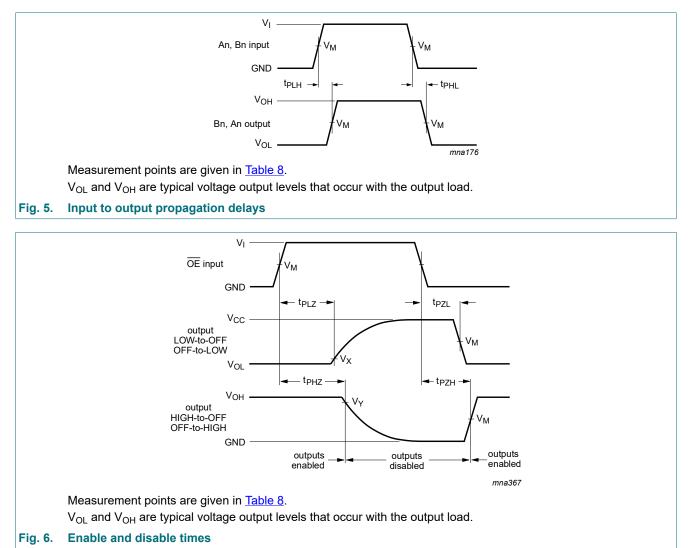
 $C_L$  = output load capacitance in pF;

 $V_{CC}$  = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$  = sum of the outputs.

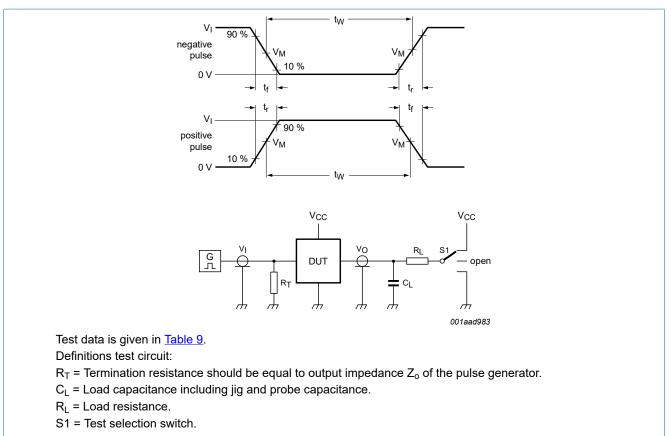
### 10.1. Waveforms



### Table 8. Measurement points

Туре	Input	Output	Output					
	V <sub>M</sub>	V <sub>M</sub>	V <sub>X</sub>	V <sub>Y</sub>				
74AHC245	0.5 x V <sub>CC</sub>	0.5 x V <sub>CC</sub>	V <sub>OL</sub> + 0.3 V	V <sub>OH</sub> - 0.3 V				
74AHCT245	1.5 V	0.5 x V <sub>CC</sub>	V <sub>OL</sub> + 0.3 V	V <sub>OH</sub> - 0.3 V				

### Octal bus transceiver; 3-state

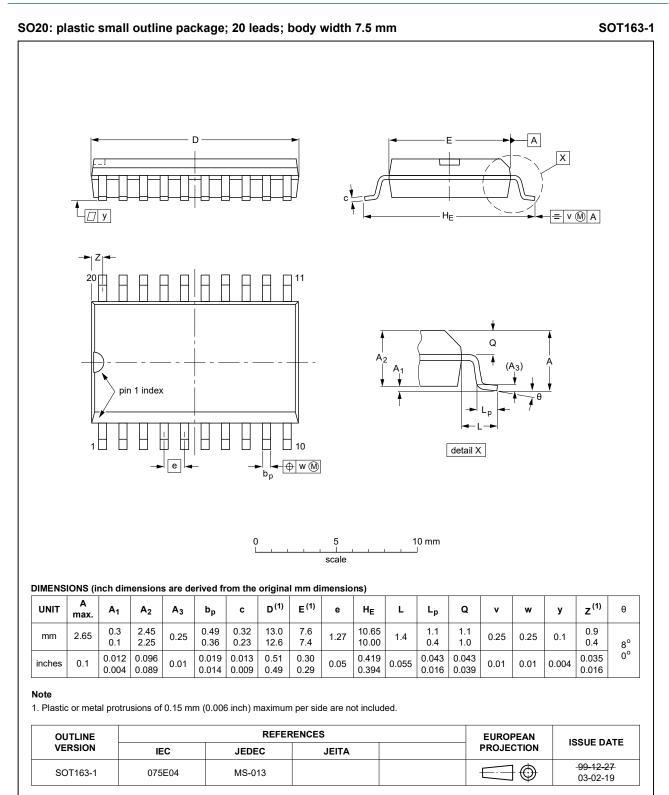


### Fig. 7. Test circuit for measuring switching times

#### Table 9. Test data

Туре	Input		Load		S1 position		
	VI	t <sub>r</sub> , t <sub>f</sub>	CL	R <sub>L</sub>	t <sub>PHL</sub> , t <sub>PLH</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>
74AHC245	V <sub>CC</sub>	≤ 3.0 ns	15 pF, 50 pF	1 kΩ	open	GND	V <sub>CC</sub>
74AHCT245	3.0 V	≤ 3.0 ns	15 pF, 50 pF	1 kΩ	open	GND	V <sub>CC</sub>

# **11. Package outline**



### Fig. 8. Package outline SOT163-1 (SO20)

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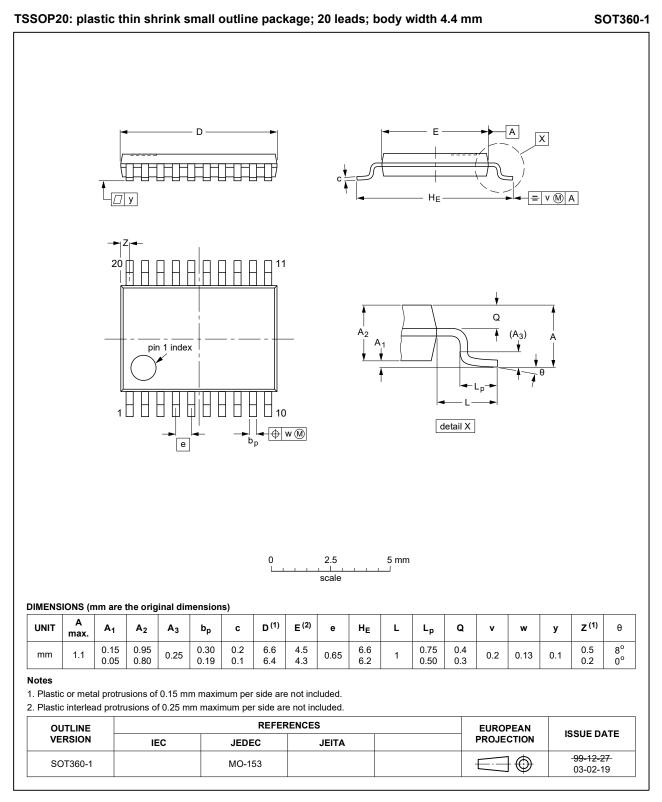


Fig. 9. Package outline SOT360-1 (TSSOP20)

### Octal bus transceiver; 3-state

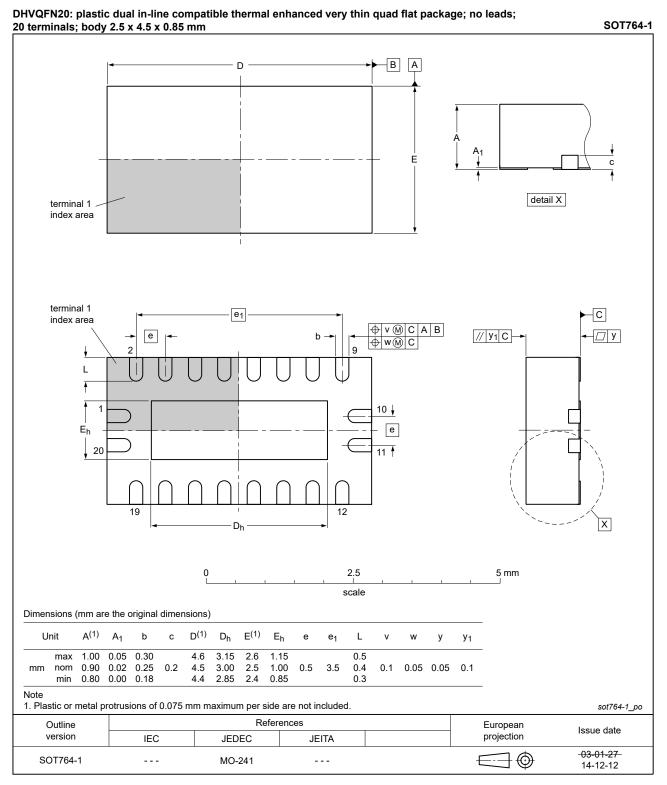


Fig. 10. Package outline SOT764-1 (DHVQFN20)

# **12. Abbreviations**

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

# 13. Revision history

### Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74AHC_AHCT245 v.6	20200703	Product data sheet	-	74AHC_AHCT245 v.5	
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li><u>Section 1</u> and <u>Section 2</u> updated.</li> <li><u>Table 4</u>: Derating values for P<sub>tot</sub> total power dissipation have been updated.</li> <li><u>Table 6</u>: Conditions for I<sub>OZ</sub> corrected.</li> <li>Package outline drawing of SOT764-1 (Fig. 10) updated.</li> </ul>				
74AHC_AHCT245 v.5	20090428	Product data sheet	-	74AHC_AHCT245 v.4	
Modifications:	<ul> <li><u>Section 3</u>: DHVQFN20 package added.</li> <li><u>Section 7</u>: derating values added for DHVQFN20 package.</li> <li><u>Section 11</u>: outline drawing added for DHVQFN20 package.</li> </ul>				
74AHC_AHCT245 v.4	20080425	Product data sheet	-	74AHC_AHCT245 v.3	
74AHC_AHCT245 v.3	20070925	Product data sheet	-	74AHC_AHCT245 v.2	
74AHC_AHCT245 v.2	19990928	Product specification	-	74AHC_AHCT245 v.1	
74AHC_AHCT245 v.1	19980921	Product specification	-	-	

# 14. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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