74AUP1G386

Low-power 3-input EXCLUSIVE-OR gate

Rev. 8 — 21 January 2022

Product data sheet

1. General description

The 74AUP1G386 is a single 3-input EXCLUSIVE-OR gate. Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times. This device ensures very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V. This device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- · CMOS low power dissipation
- · High noise immunity
- · Overvoltage tolerant inputs to 3.6 V
- Low static power consumption; I_{CC} = 0.9 μA (maximum)
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- · Complies with JEDEC standards:
 - JESD8-12 (0.8 V to 1.3 V)
 - JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.2 V to 1.95 V)
 - JESD8-5 (1.8 V to 2.7 V)
 - JESD8-B (2.7 V to 3.6 V)
- ESD protection:
 - HBM JESD22-A114F Class 3A exceeds 5000 V
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101E exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

Type number	Package									
	Temperature range	Name	Description	Version						
74AUP1G386GW	-40 °C to +125 °C	TSSOP6	plastic thin shrink small outline package; 6 leads; body width 1.25 mm	SOT363-2						
74AUP1G386GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886						
74AUP1G386GN	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm	SOT1115						
74AUP1G386GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	SOT1202						



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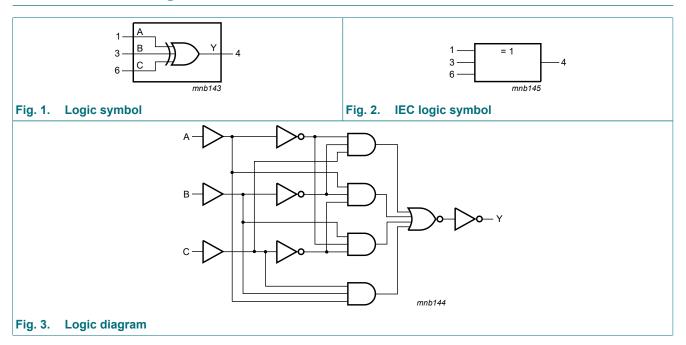
4. Marking

Table 2. Marking

Type number	Marking code [1]
74AUP1G386GW	аН
74AUP1G386GM	аН
74AUP1G386GN	аН
74AUP1G386GS	аН

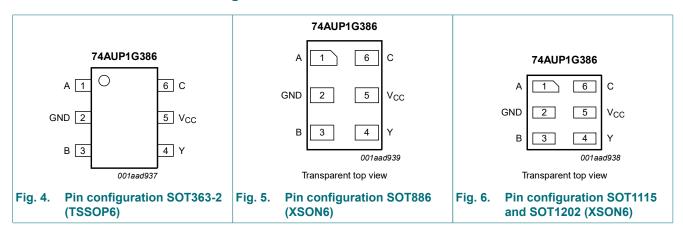
^[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram



6. Pinning information

6.1. Pinning



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6.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
A	1	data input A
GND	2	ground (0 V)
В	3	data input B
Υ	4	data output Y
V _{CC}	5	supply voltage
С	6	data input C

7. Functional description

Table 4. Function table

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$

Input	Input			
A	В	С	Υ	
L	L	L	L	
L	L	Н	Н	
L	Н	L	Н	
L	Н	Н	L	
Н	L	L	Н	
Н	L	Н	L	
Н	Н	L	L	
Н	Н	Н	Н	

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+4.6	V
I _{IK}	input clamping current	V _I < 0 V		-50	-	mA
VI	input voltage		[1]	-0.5	+4.6	V
I _{OK}	output clamping current	V _O < 0 V		-50	-	mA
Vo	output voltage	Active mode and Power-down mode	[1]	-0.5	+4.6	V
Io	output current	$V_O = 0 \text{ V to } V_{CC}$		-	±20	mA
I _{CC}	supply current			-	50	mA
I _{GND}	ground current			-50	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C	[2]	-	250	mW

^[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

For SOT886 (XSON6) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C.

For SOT1115 (XSON6) package: Ptot derates linearly with 3.2 mW/K above 71 °C.

For SOT1202 (XSON6) package: P $_{tot}$ derates linearly with 3.3 mW/K above 74 °C.

^[2] For SOT363-2 (TSSOP6) package: P_{tot} derates linearly with 3.7 mW/K above 83 °C.

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9. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V _{CC}	V
		Power-down mode; V _{CC} = 0 V	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 0.8 V to 3.6 V	0	200	ns/V

10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 2	5°C					
V_{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V_{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.75 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.11	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.32	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	2.05	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.9	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.72	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.6	-	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.31	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.44	V
l _l	input leakage current	V_{I} = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.1	μA
I _{OFF}	power-off leakage current	V_{I} or $V_{O} = 0 \text{ V}$ to 3.6 V; $V_{CC} = 0 \text{ V}$	-	-	±0.2	μΑ

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Δl _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.2	μA
I _{CC}	supply current	V_{I} = GND or V_{CC} ; I_{O} = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	0.5	μA
ΔI _{CC}	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	40	μA
Cı	input capacitance	V_{CC} = 0 V to 3.6 V; V_I = GND or V_{CC}			-	pF
Co	output capacitance	$V_O = GND; V_{CC} = 0 V$	-	1.7	-	pF
T _{amb} = -4	40 °C to +85 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V_{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}				
		I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.7 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.03	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	V
		I_{O} = -2.3 mA; V_{CC} = 2.3 V	1.97	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.85	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.67	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.55	-	-	V
V_{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.33	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.45	٧
I _I	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.5	μA
I _{OFF}	power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.5	μA
Δl _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.6	μΑ
I _{CC}	supply current	$V_I = GND \text{ or } V_{CC}; I_O = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.9	μΑ
ΔI_{CC}	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	50	μA

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -	40 °C to +125 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.75 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.25 × V _{CC}	٧
		V _{CC} = 0.9 V to 1.95 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	٧
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	٧
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I _O = -20 μA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.11	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.6 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	0.93	-	-	٧
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	٧
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.77	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.67	-	-	٧
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.40	-	-	٧
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.30	-	-	٧
√ _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_O = 20 \mu A; V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.11	٧
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.33 × V _{CC}	٧
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	٧
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	٧
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	٧
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.50	V
I	input leakage current	$V_I = GND \text{ to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.75	μΑ
OFF	power-off leakage current	V_{I} or $V_{O} = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.75	μΑ
∆I _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.75	μA
CC	supply current	V_I = GND or V_{CC} ; I_O = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	1.4	μA
ΔI _{CC}	additional supply current	$V_1 = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	75	μΑ

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11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 8.

Symbol	Parameter	Conditions		25 °C		-40 °C to	o +85 °C	-40 °C to	+125 °C	Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
C _L = 5 p	F			·			'		·	
t _{pd}		A, B and C to Y; see Fig. 7 [2]								
	delay	V _{CC} = 0.8 V	-	23.4	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.7	6.5	14.2	2.4	14.6	2.4	14.7	ns
		V _{CC} = 1.4 V to 1.6 V	2.0	4.4	8.1	2.1	8.8	2.1	9.1	ns
		V _{CC} = 1.65 V to 1.95 V	1.8	3.5	6.1	1.6	7.0	1.6	7.3	ns
		V _{CC} = 2.3 V to 2.7 V	1.5	2.7	4.3	1.2	4.6	1.2	4.8	ns
		V _{CC} = 3.0 V to 3.6 V	1.3	2.4	3.6	1.0	4.0	1.0	4.2	ns
C _L = 10	pF				•					
t _{pd}		A, B and C to Y; see Fig. 7 [2]								
	delay	V _{CC} = 0.8 V	-	26.8	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.2	7.3	15.8	2.7	16.2	2.7	16.3	ns
		V _{CC} = 1.4 V to 1.6 V	2.3	5.0	9.0	2.5	9.8	2.5	10.2	ns
		V _{CC} = 1.65 V to 1.95 V	2.2	4.1	6.9	1.9	7.8	1.9	8.2	ns
		V _{CC} = 2.3 V to 2.7 V	1.9	3.2	5.0	1.6	5.3	1.6	5.5	ns
		V _{CC} = 3.0 V to 3.6 V	1.7	2.9	4.3	1.4	4.7	1.4	4.9	ns
C _L = 15	pF									
t _{pd}		A, B and C to Y; see Fig. 7 [2]								
	delay	V _{CC} = 0.8 V	-	30.1	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.5	8.1	17.3	3.0	17.7	3.0	17.8	ns
		V _{CC} = 1.4 V to 1.6 V	2.6	5.6	9.8	2.8	10.7	2.8	11.1	ns
		V _{CC} = 1.65 V to 1.95 V	2.4	4.6	7.5	2.2	8.6	2.2	9.0	ns
		V _{CC} = 2.3 V to 2.7 V	2.2	3.7	5.5	1.9	5.9	1.9	6.2	ns
		V _{CC} = 3.0 V to 3.6 V	2.0	3.4	4.8	1.7	5.2	1.7	5.5	ns
C _L = 30	pF									
t _{pd}		A, B and C to Y; see Fig. 7 [2]								
	delay	V _{CC} = 0.8 V	-	37.9	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.5	10.3	21.6	3.9	22.0	3.9	22.1	ns
		V _{CC} = 1.4 V to 1.6 V	3.5	7.1	12.1	3.5	13.2	3.5	13.8	ns
		V _{CC} = 1.65 V to 1.95 V	3.1	5.8	9.5	2.8	10.7	2.8	11.3	ns
		V _{CC} = 2.3 V to 2.7 V	2.9	4.8	6.9	2.6	7.8	2.6	8.2	ns
		V _{CC} = 3.0 V to 3.6 V	2.7	4.5	6.1	2.3	6.6	2.3	6.9	ns

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Symbol	Parameter	Conditions		25 °C		-40 °C to	+85 °C	-40 °C to	+125 °C	Unit	
			Min	Typ [1]	Max	Min	Max	Min	Max		
C _L = 5 pF, 10 pF, 15 pF and 30 pF											
C _{PD}	dissipation	f_i = 1 MHz; [3][4] V_I = GND to V_{CC}									
	capacitance	capacitance	V _{CC} = 0.8 V	-	2.9	-	-	-	-	-	рF
		V _{CC} = 1.1 V to 1.3 V	-	3.0	-	-	-	-	-	pF	
		V _{CC} = 1.4 V to 1.6 V	-	3.1	-	-	-	-	-	рF	
		V _{CC} = 1.65 V to 1.95 V	-	3.3	-	-	-	-	-	рF	
		V _{CC} = 2.3 V to 2.7 V	-	3.9	-	-	-	-	-	pF	
		V _{CC} = 3.0 V to 3.6 V	-	4.4	-	-	-	-	-	pF	

- All typical values are measured at nominal V_{CC}.
- t_{pd} is the same as t_{PLH} and t_{PHL} . [2]
- All specified values are the average typical values over all stated loads.
- C_{PD} is used to determine the dynamic power dissipation (P_D in μ W). $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$$
 where:

f_i = input frequency in MHz;

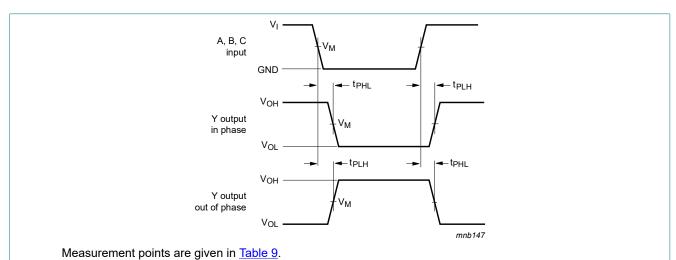
fo = output frequency in MHz;

C_L = load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching; $\Sigma (C_L \times V_{CC}^{\ 2} \times f_o) = \text{sum of the outputs}.$

11.1. Waveform and test circuit



Logic levels: V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

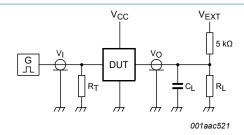
Input A, B and C to output Y propagation delay times Fig. 7.

Table 9. Measurement points

Supply voltage	Output	Input				
V _{CC}	V_{M}	V_{M} V_{I} $t_{r} = t_{f}$				
0.8 V to 3.6 V	0.5 × V _{CC}	0.5 × V _{CC}	V _{CC}	≤ 3.0 ns		

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Test data is given in Table 10.

Definitions for test circuit:

R_L = Load resistance;

C_L = Load capacitance including jig and probe capacitance;

 R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator;

 V_{EXT} = External voltage for measuring switching times.

Fig. 8. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Load		V _{EXT}		
V _{CC}	CL	R _L [1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 × V _{CC}

[1] For measuring enable and disable times R_L = 5 k Ω . For measuring propagation delays, setup and hold times and pulse width R_L = 1 M Ω .

Low-power 3-input EXCLUSIVE-OR gate

12. Package outline

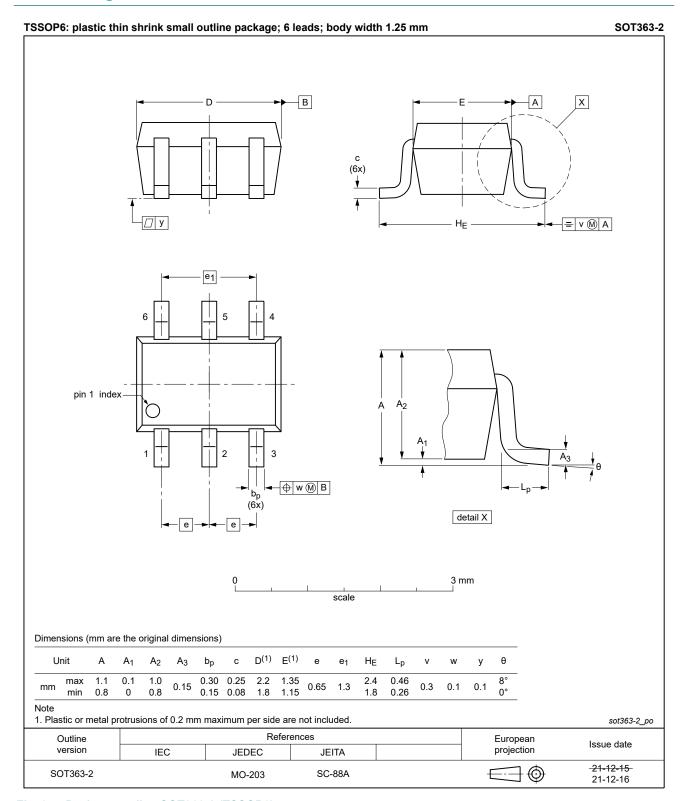


Fig. 9. Package outline SOT363-2 (TSSOP6)

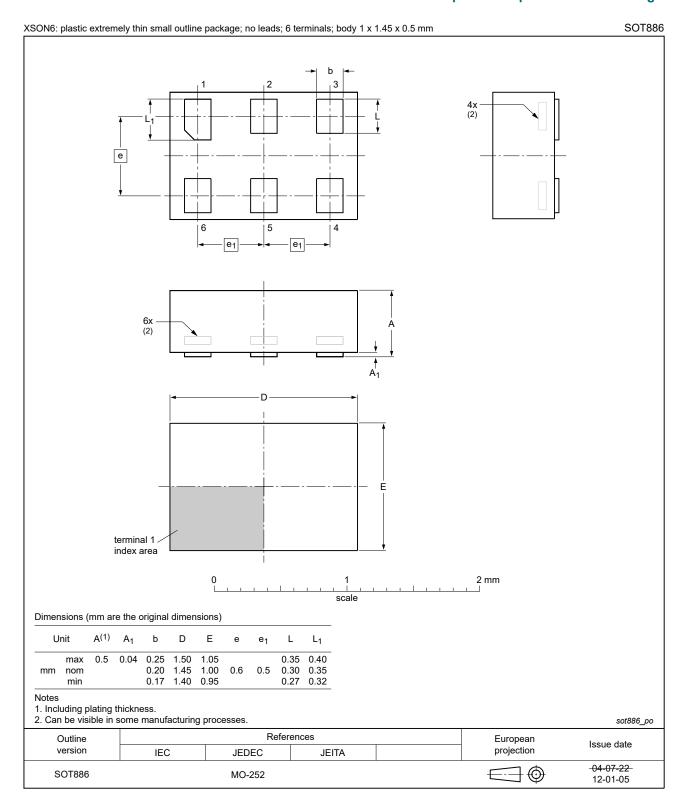


Fig. 10. Package outline SOT886 (XSON6)

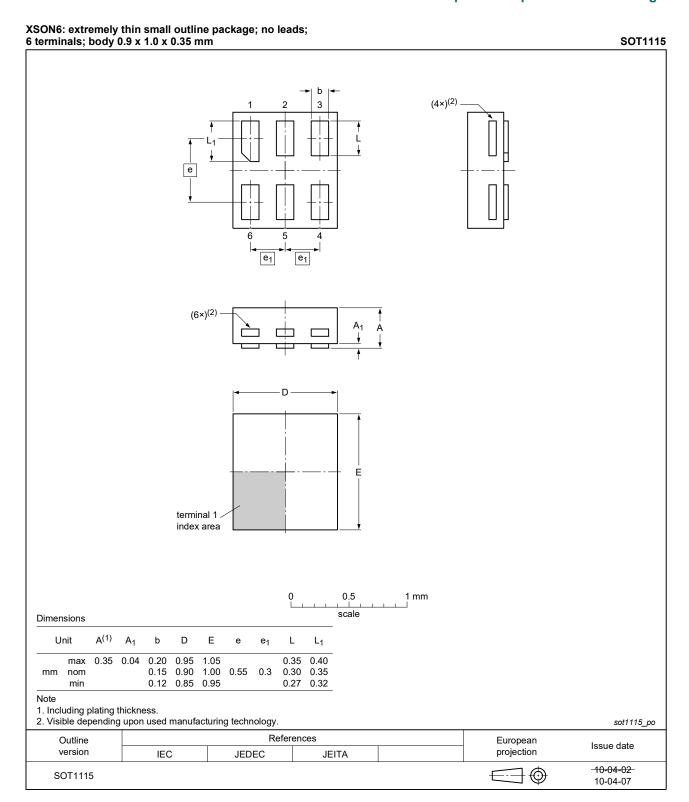


Fig. 11. Package outline SOT1115 (XSON6)

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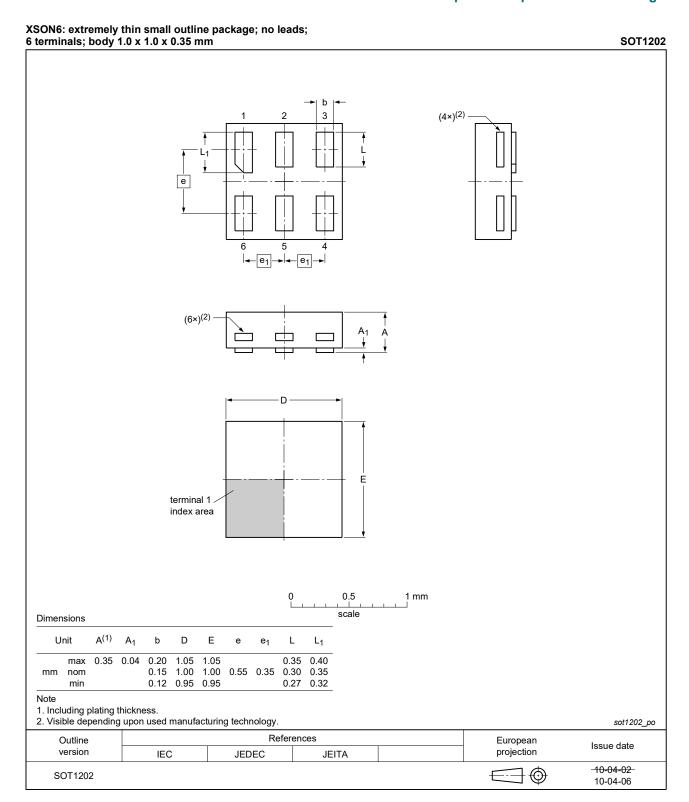


Fig. 12. Package outline SOT1202 (XSON6)

Product data sheet

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13. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

14. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1G386 v.8	20220121	Product data sheet	-	74AUP1G386 v.7
Modifications:	Package S0	DT363 (SC-88) changed to	SOT363-2 (TSS0	DP6).
74AUP1G386 v.7	20201208	Product data sheet	-	74AUP1G386 v.6
Modifications:	guidelines of Legal texts Type number Section 1 ar	of this data sheet has beer of Nexperia. have been adapted to the reer 74AUP1G386GF (SOT8 and Section 2 updated. rating values for P _{tot} total p	new company nar 91 / XSON6) rem	ne where appropriate. oved.
74AUP1G386 v.6	20120731	Product data sheet	-	74AUP1G386 v.5
Modifications:	Package out	tline drawing of SOT886 (ig. 10) modified.	
74AUP1G386 v.5	20111128	Product data sheet	-	74AUP1G386 v.4
Modifications:	Legal pages	updated.		
74AUP1G386 v.4	20100805	Product data sheet	-	74AUP1G386 v.3
74AUP1G386 v.3	20090702	Product data sheet	-	74AUP1G386 v.2
74AUP1G386 v.2	20080110	Product data sheet	-	74AUP1G386 v.1
74AUP1G386 v.1	20061129	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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