74AUP2G16

Low-power dual buffer

Rev. 4 — 31 January 2022

Product data sheet

1. General description

The 74AUP2G16 provides two low-power, low-voltage buffers.

Schmitt trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire V_{CC} range from 0.8 V to 3.6 V.

This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial power-down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
 - JESD8-12 (0.8 V to 1.3 V)
 - JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.2 V to 1.95 V)
 - JESD8-5 (1.8 V to 2.7 V)
 - JESD8-B (2.7 V to 3.6 V)
- ESD protection:
 - HBM JESD22-A114F Class 3A exceeds 5000 V
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101E exceeds 1000 V
- Low static power consumption; I_{CC} = 0.9 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3. Ordering information

Table 1 Ordering information

Tubic 1. Ordering in	Table 1. Ordering information											
Type number	Package											
	Temperature range	Name	Description	Version								
74AUP2G16GW	-40 °C to +125 °C	TSSOP6	plastic thin shrink small outline package; 6 leads; body width 1.25 mm	SOT363-2								
74AUP2G16GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886								



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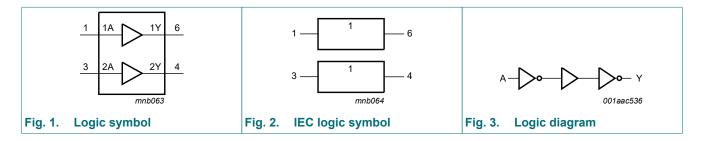
4. Marking

Table 2. Marking

Type number	Marking code[1]
74AUP2G16GW	5A
74AUP2G16GM	5A

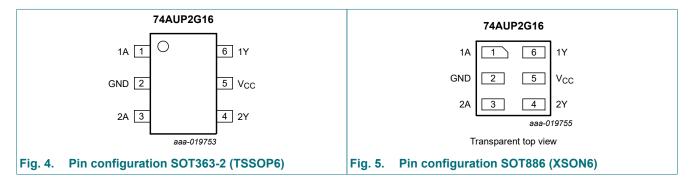
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram



6. Pinning information

6.1. Pinning



6.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
1A	1	data input
GND	2	ground (0 V)
2A	3	data input
2Y	4	data output
Vcc	5	supply voltage
1Y	6	data output

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7. Functional description

Table 4. Function table

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$

Input	Output
nA	nY
L	L
Н	Н

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+4.6	V
I _{IK}	input clamping current	V _I < 0 V		-50	-	mA
VI	input voltage]	[1]	-0.5	+4.6	V
I _{OK}	output clamping current	V _O < 0 V		-50	-	mA
Vo	output voltage	Active mode and Power-down mode [[1]	-0.5	+4.6	V
Io	output current	$V_O = 0 V \text{ to } V_{CC}$		-	±20	mA
I _{CC}	supply current			-	50	mA
I _{GND}	ground current			-50	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T_{amb} = -40 °C to +125 °C	[2]	-	250	mW

^[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V _{CC}	V
		Power-down mode; V _{CC} = 0 V	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 0.8 V to 3.6 V	-	200	ns/V

^[2] For SOT363-2 (TSSOP6) package: P_{tot} derates linearly with 3.7 mW/K above 83 °C. For SOT886 (XSON6) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C.

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10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 2	5 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}				
		I _O = -20 μA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.75 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.11	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.32	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	2.05	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.9	-	-	V
		I_{O} = -2.7 mA; V_{CC} = 3.0 V	2.72	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.6	-	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I_{O} = 20 μ A; V_{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.31	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.44	V
l _l	input leakage current	V_I = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.1	μΑ
l _{OFF}	power-off leakage current	V_{I} or $V_{O} = 0 V$ to 3.6 V; $V_{CC} = 0 V$	-	-	±0.2	μΑ
ΔI_{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.2	μΑ
I _{CC}	supply current	$V_I = GND \text{ or } V_{CC}; I_O = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μA
ΔI _{CC}	additional supply current	$V_1 = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$	-	-	40	μΑ
C _I	input capacitance	V_{CC} = 0 V to 3.6 V; V_I = GND or V_{CC}	-	8.0	-	pF
Co	output capacitance	V _O = GND; V _{CC} = 0 V	-	1.7	-	pF

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -4	10 °C to +85 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	٧
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	٧
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.7 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.03	-	-	٧
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	٧
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.97	-	-	٧
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.85	-	-	V
		I_{O} = -2.7 mA; V_{CC} = 3.0 V	2.67	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.55	-	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	٧
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.33	٧
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	٧
		I_{O} = 2.7 mA; V_{CC} = 3.0 V	-	-	0.33	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.45	٧
ı	input leakage current	$V_I = GND \text{ to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.5	μΑ
OFF	power-off leakage current	V_{I} or $V_{O} = 0 V$ to 3.6 V; $V_{CC} = 0 V$	-	-	±0.5	μΑ
Δl _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.6	μA
СС	supply current	V_{I} = GND or V_{CC} ; I_{O} = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	0.9	μA
ΔI _{CC}	additional supply current	$V_1 = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$	-	-	50	μA

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -4	40 °C to +125 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.75 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	٧
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	٧
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.25 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	٧
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.11	-	-	٧
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.6 × V _{CC}	-	-	٧
		I _O = -1.7 mA; V _{CC} = 1.4 V	0.93	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	٧
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.77	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.67	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.40	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.30	-	-	٧
V _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.33 × V _{CC}	٧
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	٧
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.36	٧
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	٧
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	٧
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.50	٧
l _l	input leakage current	$V_I = GND \text{ to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.75	μΑ
l _{OFF}	power-off leakage current	V_{I} or $V_{O} = 0 V$ to 3.6 V; $V_{CC} = 0 V$	-	-	±0.75	μΑ
Δl _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.75	μA
I _{CC}	supply current	$V_I = GND \text{ or } V_{CC}; I_O = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	1.4	μA
Δl _{CC}	additional supply current	$V_1 = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$	-	-	75	μA

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11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 7.

Symbol	Parameter	Conditions		25 °C		-40 °C to	+85 °C	-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
C _L = 5 p	F									
t _{pd}	propagation	nA to nY; see Fig. 6 [2]								
	delay	V _{CC} = 0.8 V	-	14.9	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.6	4.7	9.2	2.0	10.0	2.0	11.0	ns
		V _{CC} = 1.4 V to 1.6 V	2.1	3.4	5.7	1.6	6.5	1.6	7.2	ns
		V _{CC} = 1.65 V to 1.95 V	1.8	2.9	4.5	1.4	5.2	1.4	5.8	ns
		V _{CC} = 2.3 V to 2.7 V	1.5	2.3	3.5	1.2	4.2	1.2	4.6	ns
		V _{CC} = 3.0 V to 3.6 V	1.4	2.1	3.2	1.0	3.8	1.0	4.2	ns
C _L = 10	pF									,
t _{pd}	propagation	nA to nY; see Fig. 6 [2]								
	delay	V _{CC} = 0.8 V	-	18.4	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.2	5.6	10.9	2.3	11.8	2.3	13.1	ns
		V _{CC} = 1.4 V to 1.6 V	2.6	4.1	6.7	1.9	7.7	1.9	8.5	ns
		V _{CC} = 1.65 V to 1.95 V	2.3	3.4	5.3	1.7	6.2	1.7	6.9	ns
		V _{CC} = 2.3 V to 2.7 V	2.0	2.9	4.2	1.5	5.0	1.5	5.5	ns
		V _{CC} = 3.0 V to 3.6 V	1.7	2.6	3.8	1.4	4.6	1.4	5.1	ns
C _L = 15	pF									
t _{pd}	propagation	nA to nY; see Fig. 6 [2]								
	delay	V _{CC} = 0.8 V	-	21.9	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.6	6.4	12.6	2.6	13.8	2.6	15.2	ns
		V _{CC} = 1.4 V to 1.6 V	3.0	4.6	7.6	2.2	8.9	2.2	9.8	ns
		V _{CC} = 1.65 V to 1.95 V	2.6	3.9	6.0	2.0	7.2	2.0	7.9	ns
		V _{CC} = 2.3 V to 2.7 V	2.3	3.3	4.8	1.8	5.7	1.8	6.3	ns
		V _{CC} = 3.0 V to 3.6 V	2.1	3.1	4.2	1.6	5.0	1.6	5.5	ns
C _L = 30	pF									
t _{pd}	propagation	nA to nY; see Fig. 6 [2]								
	delay	V _{CC} = 0.8 V	-	32.1	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.8	8.7	16.3	3.6	18.9	3.6	20.8	ns
		V _{CC} = 1.4 V to 1.6 V	4.0	6.2	10.3	3.4	12.2	3.4	13.4	ns
		V _{CC} = 1.65 V to 1.95 V	3.6	5.2	8.1	3.2	9.8	3.2	10.8	ns
		V _{CC} = 2.3 V to 2.7 V	3.0	4.4	6.4	2.7	7.7	2.7	8.5	ns
		V _{CC} = 3.0 V to 3.6 V	2.9	4.2	5.6	2.5	6.5	2.5	7.2	ns

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Symbol	Parameter	Conditions	25 °C		-40 °C to	+85 °C	-40 °C to	+125 °C	Unit		
			Min	Typ[1]	Max	Min	Max	Min	Max		
C _L = 5 p	F, 10 pF, 15 pl	F and 30 pF									
C _{PD}	power dissipation	f_i = 1 MHz; [3][4] V _I = GND to V _{CC}									
	capacitance	V _{CC} = 0.8 V	-	2.5	-	-	-	-	-	pF	
		V _{CC} = 1.1 V to 1.3 V	-	2.6	-	-	-	-	-	pF	
		V _{CC} = 1.4 V to 1.6 V	-	2.7	-	-	-	-	-	pF	
			V _{CC} = 1.65 V to 1.95 V	-	2.9	-	-	-	-	-	pF
			V _{CC} = 2.3 V to 2.7 V	-	3.4	-	-	-	-	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	4.0	-	-	-	-	-	pF	

- All typical values are measured at nominal V_{CC}.
- t_{pd} is the same as t_{PLH} and t_{PHL} .
- All specified values are the average typical values over all stated loads.
- C_{PD} is used to determine the dynamic power dissipation (P_D in μ W). $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$$
 where:

f_i = input frequency in MHz;

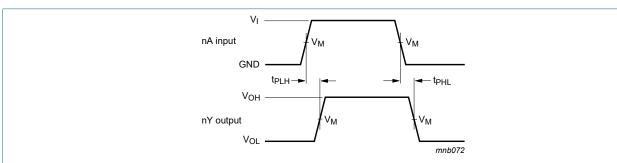
fo = output frequency in MHz;

C_L = load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching; $\Sigma (C_L \times V_{CC}^{\ 2} \times f_o) = sum \ of \ the \ outputs.$

11.1. Waveform and test circuit



Measurement points are given in Table 9.

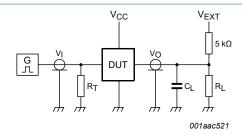
Logic levels: V_{OL} and V_{OH} are typical output voltage drop that occur with the output load.

The data input (nA) to output (nY) propagation delays Fig. 6.

Table 9. Measurement points

Supply voltage	Output	Input					
V _{CC}	V _M	V _M	V _I	$t_r = t_f$			
0.8 V to 3.6 V	0.5 × V _{CC}	0.5 × V _{CC}	V _{CC}	≤ 3.0 ns			

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Test data is given in Table 10.

Definitions for test circuit:

R_L = Load resistance;

C_L = Load capacitance including jig and probe capacitance;

 R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator;

 V_{EXT} = External voltage for measuring switching times.

Fig. 7. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Load		V _{EXT}		
V _{CC}	CL	R _L [1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 × V _{CC}

[1] For measuring enable and disable times R_L = 5 k Ω . For measuring propagation delays, setup and hold times and pulse width R_L = 1 M Ω .

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12. Package outline

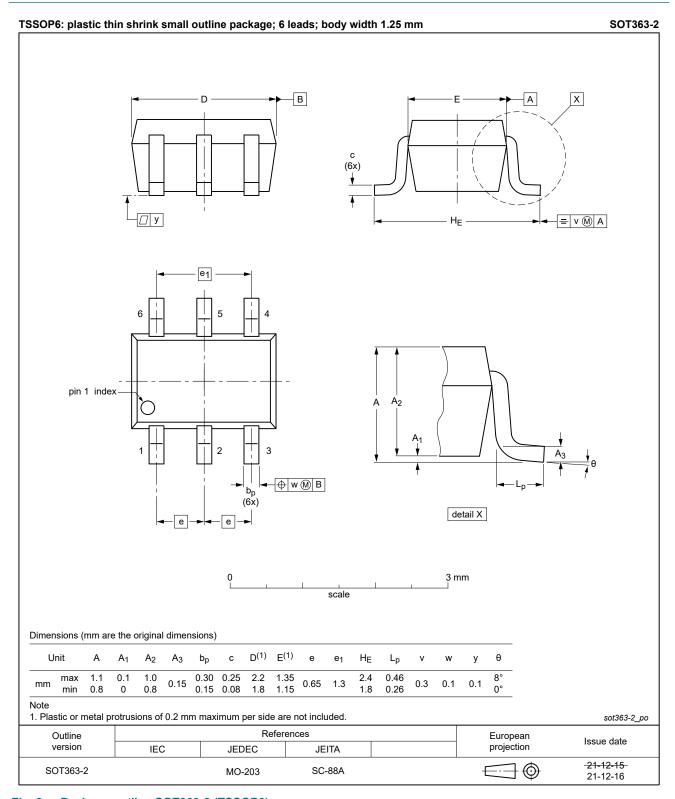


Fig. 8. Package outline SOT363-2 (TSSOP6)

Product data sheet

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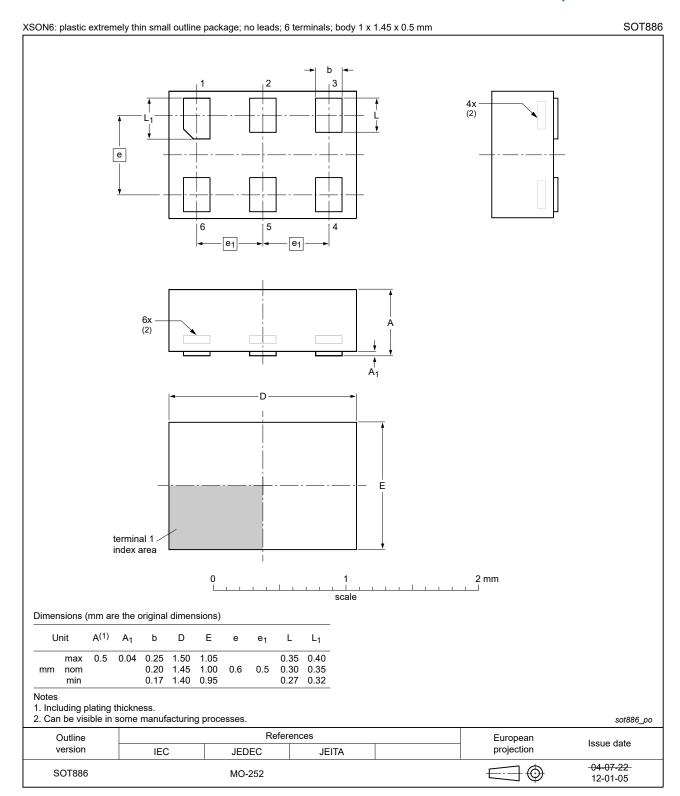


Fig. 9. Package outline SOT886 (XSON6)

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13. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

14. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP2G16 v.4	20220131	Product data sheet	-	74AUP2G16 v.3
Modifications:	Package SOT363 (SC-88) changed to SOT363-2 (TSSOP6).			
74AUP2G16 v.3	20201201	Product data sheet	-	74AUP2G16 v.2
Modifications:		Perating values for P _{tot} total per 74AUP2G16GF (SOT891/	•	en updated.
74AUP2G16 v.2	20161007	Product data sheet	-	74AUP2G16 v.1
Modifications:	Type numbers 74AUP2G16GN, 74AUP2G16GS and 74AUP2G16GX removed.			
74AUP2G16 v.1	20151104	Product data sheet	_	_

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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