# 74LV1T02

2-input single supply translating NOR gate Rev. 4 — 15 August 2022

**Product data sheet** 

### 1. General description

The 74LV1T02 is a single, level translating 2-input NOR gate. The low threshold inputs support 1.8 V input logic at V<sub>CC</sub> = 3.3 V and can be used in 1.8 V to 3.3 V level up translation. In addition, the 5 V tolerant input pins enable level down translation (3.3 V to 2.5 V output at V<sub>CC</sub> = 2.5 V). The output level is referenced to the supply voltage and supports 1.8 V, 2.5 V, 3.3 V and 5.0 V CMOS levels. The wide V<sub>CC</sub> range permits the generation of output levels to connect to controllers or processors.

### 2. Features and benefits

- Single supply voltage translator at 1.8 V, 2.5 V, 3.3 V and 5.0 V
- Up translation
  - 1.2 V to 1.8 V at  $V_{CC}$  = 1.8 V
  - 1.5 V to 2.5 V at V<sub>CC</sub> = 2.5 V
  - 1.8 V to 3.3 V at V<sub>CC</sub> = 3.3 V
  - 3.3 V to 5.0 V at V<sub>CC</sub> = 5.0 V
- Down translation
  - 3.3 V to 1.8 V at V<sub>CC</sub> = 1.8 V
  - 3.3 V to 2.5 V at V<sub>CC</sub> = 2.5 V
  - 5.0 V to 3.3 V at V<sub>CC</sub> = 3.3 V
- 5 V tolerant inputs
- Latch-up performance exceeds 250 mA per JESD 78 Class II
- ESD protection:
  - HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 2 kV
  - CDM JESD22-C101 exceeds 1 kV
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

### 3. Applications

- Portable applications
- PC and notebooks
- Industrial controller
- Telecom



## 4. Ordering information

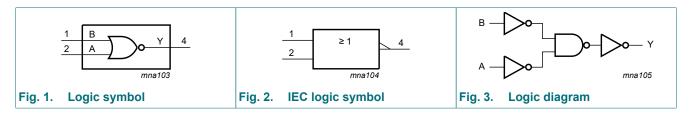
Type number	Package			
	Temperature range	Name	Description	Version
74LV1T02GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	<u>SOT353-1</u>
74LV1T02GV	-40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	<u>SOT753</u>
74LV1T02GX	-40 °C to +125 °C	X2SON5	plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 × 0.8 × 0.32 mm	<u>SOT1226-3</u>

## 5. Marking

Table 2. Marking	
Type number	Marking code[1]
74LV1T02GW	SF
74LV1T02GV	SF
74LV1T02GX	SF

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

## 6. Functional diagram



## 7. Pinning information

7.1. Pinning



### 7.2. Pin description

Symbol	Pin	Description
В	1	data input
A	2	data input
GND	3	ground (0 V)
Y	4	data output
V <sub>CC</sub>	5	supply voltage

## 8. Functional description

#### Table 4. Function table

*H* = *HIGH* voltage level; *L* = *LOW* voltage level.

Input	Output	
Α	В	Y
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

## 9. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage			-0.5	+7.0	V
VI	input voltage		[1]	-0.5	+7.0	V
Vo	output voltage	output HIGH or LOW state	[2][3]	-0.5	V <sub>CC</sub> + 0.5	V
		output in power-off state	[2]	-0.5	4.6	V
I <sub>IK</sub>	input clamping current	V <sub>1</sub> < 0 V		-20	-	mA
Ι <sub>ΟΚ</sub>	output clamping current	$V_{O} < 0 V \text{ or } V_{O} > V_{CC}$		-	±20	mA
lo	output current	$V_{O} = 0 V \text{ to } V_{CC}$		-	±25	mA
I <sub>CC</sub>	supply current			-	50	mA
I <sub>GND</sub>	ground current			-50	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = -40 °C to +125 °C	[4]	-	250	mW

[1] If the input current ratings are observed, the minimum input voltage ratings may be exceeded.

[2] If the output current ratings are observed, the output voltage ratings may be exceeded.

[3] This value is limited to 7 V maximum.

For SOT353-1 (TSSOP5) package: P<sub>tot</sub> derates linearly with 3.3 mW/K above 74 °C.
 For SOT753 (SC-74A) package: P<sub>tot</sub> derates linearly with 3.8 mW/K above 85 °C.
 For SOT1226-3 (X2SON5) package: P<sub>tot</sub> derates linearly with 3.0 mW/K above 67 °C.

## **10. Recommended operating conditions**

#### Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	supply voltage		1.6	5.0	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage	output HIGH or LOW state	0	-	V <sub>CC</sub>	V
T <sub>amb</sub>	ambient temperature		-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 1.8 V to 5.0 V	-	-	20	ns/V

## **11. Static characteristics**

#### **Table 7. Static characteristics**

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	25 °	C	-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Мах	Min	Max	Min	Max	1
VIH	HIGH-level	V <sub>CC</sub> = 1.65 V to 1.8 V	0.94	-	1.0	-	1.0	-	V
	input voltage	V <sub>CC</sub> = 2.0 V	0.99	-	1.03	-	1.03	-	V
		V <sub>CC</sub> = 2.25 V to 2.5 V	1.135	-	1.18	-	1.18	-	V
		V <sub>CC</sub> = 2.75 V	1.21	-	1.23	-	1.23	-	V
		V <sub>CC</sub> = 3.0 V to 3.3 V	1.35	-	1.37	-	1.37	-	V
		V <sub>CC</sub> = 3.6 V	1.47	-	1.48	-	1.48	-	V
		V <sub>CC</sub> = 4.5 V to 5.0 V	2.02	-	2.03	-	2.03	-	V
		V <sub>CC</sub> = 5.5 V	2.10	-	2.11	-	2.11	-	V
V <sub>IL</sub>	LOW-level	V <sub>CC</sub> = 1.65 V to 2.0 V	-	0.58	-	0.55	-	0.55	V
	input voltage	V <sub>CC</sub> = 2.25 V to 2.75 V	-	0.75	-	0.71	-	0.71	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	0.80	-	0.65	-	0.65	V
		V <sub>CC</sub> = 4.5 V to 5.5 V	-	0.80	-	0.80	-	0.80	V
V <sub>OH</sub>	HIGH-level	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> ;							
	output voltage	$V_{CC}$ = 1.65 V to 5.5 V; I <sub>O</sub> = -20 µA	V <sub>CC</sub> - 0.1	-	V <sub>CC</sub> - 0.1	-	V <sub>CC</sub> - 0.1	-	V
		V <sub>CC</sub> = 1.65 V; I <sub>O</sub> = -2 mA	1.28	-	1.21	-	1.21	-	V
		V <sub>CC</sub> = 1.8 V; I <sub>O</sub> = -2 mA	1.5	-	1.45	-	1.45	-	V
		V <sub>CC</sub> = 2.3 V; I <sub>O</sub> = -2.3 mA	2.0	-	2.0	-	2.0	-	V
		V <sub>CC</sub> = 2.3 V; I <sub>O</sub> = -3 mA	2.0	-	1.93	-	1.93	-	V
		V <sub>CC</sub> = 2.5 V; I <sub>O</sub> = -3 mA	2.25	-	2.15	-	2.15	-	V
		V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = -3 mA	2.78	-	2.7	-	2.7	-	V
		V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = -5.5 mA	2.6	-	2.49	-	2.49	-	V
		V <sub>CC</sub> = 3.3 V; I <sub>O</sub> = -5.5 mA	2.9	-	2.8	-	2.8	-	V
		V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = -4 mA	4.2	-	4.1	-	4.1	-	V
		V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = -8 mA	4.1	-	3.95	-	3.95	-	V
		V <sub>CC</sub> = 5.0 V; I <sub>O</sub> = -8 mA	4.6	-	4.5	-	4.5	-	V
V <sub>OL</sub>	LOW-level	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>							
	output voltage	$V_{CC}$ = 1.65 V to 5.5 V; I <sub>O</sub> = 20 µA	-	0.1	-	0.1	-	0.1	V
		V <sub>CC</sub> = 1.65 V; I <sub>O</sub> = 2 mA	-	0.2	-	0.25	-	0.25	V
		V <sub>CC</sub> = 2.3 V; I <sub>O</sub> = 2.3 mA	-	0.1	-	0.15	-	0.15	V
		V <sub>CC</sub> = 2.3 V; I <sub>O</sub> = 3 mA	-	0.15	-	0.2	-	0.2	V
		V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = 3 mA	-	0.1	-	0.15	-	0.15	V
		V <sub>CC</sub> = 3.0 V; I <sub>O</sub> = 5.5 mA	-	0.2	-	0.252	-	0.252	V
		V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = 4 mA	-	0.15	-	0.2	-	0.2	V
		V <sub>CC</sub> = 4.5 V; I <sub>O</sub> = 8 mA	-	0.3	-	0.35	-	0.35	V

Symbol Parameter		Conditions	25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Max	Min	Max	Min	Мах	
lı	input leakage current	V <sub>I</sub> = V <sub>CC</sub> or GND; V <sub>CC</sub> = 0 V to 5.5 V	-	±0.1	-	±1	-	±1	μA
I <sub>CC</sub>	supply current	V <sub>I</sub> = V <sub>CC</sub> or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 1.8 V, 2.5 V, 3.3 V, 5.0 V	-	1	-	10	-	10	μA
ΔI <sub>CC</sub>	additional supply current	per input pin; $V_{CC}$ = 1.8 V; V <sub>I</sub> = 0.3 V or 1.1 V; I <sub>O</sub> = 0 A; other pins at V <sub>CC</sub> or GND	-	10	-	10	-	10	μA
		per input pin; V <sub>CC</sub> = 5.5 V; V <sub>I</sub> = 0.3 V or 3.4 V; I <sub>O</sub> = 0 A; other pins at V <sub>CC</sub> or GND	-	1.35	-	1.5	-	1.5	mA

## 12. Dynamic characteristics

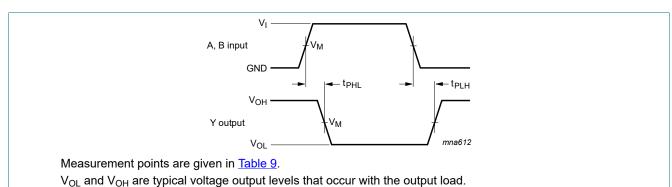
#### Table 8. Dynamic characteristics

GND = 0 V. For test circuit, see Fig. 7.

Symbol Parameter		Conditions		25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Тур	Мах	Min	Мах	Min	Мах	
t <sub>pd</sub>	propagation	A, B to Y; see Fig. 6 [1]								
	delay	V <sub>CC</sub> = 1.8 V; C <sub>L</sub> = 15 pF	-	6.6	10.1	-	11.4	-	12.2	ns
		V <sub>CC</sub> = 1.8 V; C <sub>L</sub> = 30 pF	-	7.6	11.9	-	13.4	-	14.3	ns
		V <sub>CC</sub> = 2.5 V; C <sub>L</sub> = 15 pF	-	4.6	6.8	-	7.7	-	8.2	ns
		V <sub>CC</sub> = 2.5 V; C <sub>L</sub> = 30 pF	-	5.4	7.9	-	9.0	-	9.6	ns
		V <sub>CC</sub> = 3.3 V; C <sub>L</sub> = 15 pF	-	3.8	5.5	-	6.1	-	6.5	ns
		V <sub>CC</sub> = 3.3 V; C <sub>L</sub> = 30 pF	-	4.4	6.3	-	7.0	-	7.5	ns
		V <sub>CC</sub> = 5.0 V; C <sub>L</sub> = 15 pF	-	3.2	4.1	-	4.5	-	4.7	ns
		V <sub>CC</sub> = 5.0 V; C <sub>L</sub> = 30 pF	-	3.7	4.7	-	5.1	-	5.4	ns
CI	input capacitance	$V_{I} = V_{CC}$ or GND; $V_{CC} = 3.3 V$	-	1.5	10	-	10	-	10	pF
C <sub>O</sub>	output capacitance	$V_{O} = V_{CC}$ or GND; $V_{CC} = 3.3 V$	-	2.5	-	-	-	-	-	pF
C <sub>PD</sub>	power dissipation	per buffer; $V_I$ = GND to $V_{CC}$ ; [2] $C_L$ = 30 pF; f = 10 MHz								
	capacitance	V <sub>CC</sub> = 1.8 V	-	4.2	-	-	-	-	-	pF
		V <sub>CC</sub> = 2.5 V	-	5.7	-	-	-	-	-	pF
		V <sub>CC</sub> = 3.3 V	-	7.7	-	-	-	-	-	pF
		V <sub>CC</sub> = 5.0 V	-	12.0	-	-	-	-	-	pF

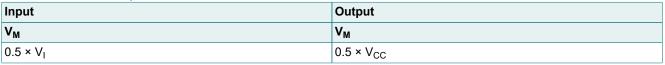
f<sub>i</sub> = input frequency in MHz; f<sub>o</sub> = output frequency in MHz; C<sub>L</sub> = output load capacitance in pF; V<sub>CC</sub> = supply voltage in V; N = number of inputs switching;  $\sum (C_L \times V_{CC}^2 \times f_o) = \text{sum of the outputs.}$ 

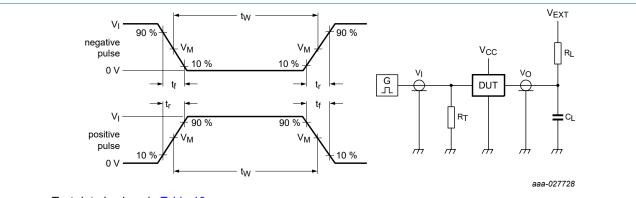
### 12.1. Waveforms and test circuit



### Fig. 6. The input A, B to output Y propagation delays

#### Table 9. Measurement points





Test data is given in <u>Table 10</u>.

Definitions test circuit:

 $R_T$  = Termination resistance should be equal to output impedance  $Z_o$  of the pulse generator;

C<sub>L</sub> = Load capacitance including jig and probe capacitance;

R<sub>L</sub> = Load resistance;

V<sub>EXT</sub> = External voltage for measuring switching times.

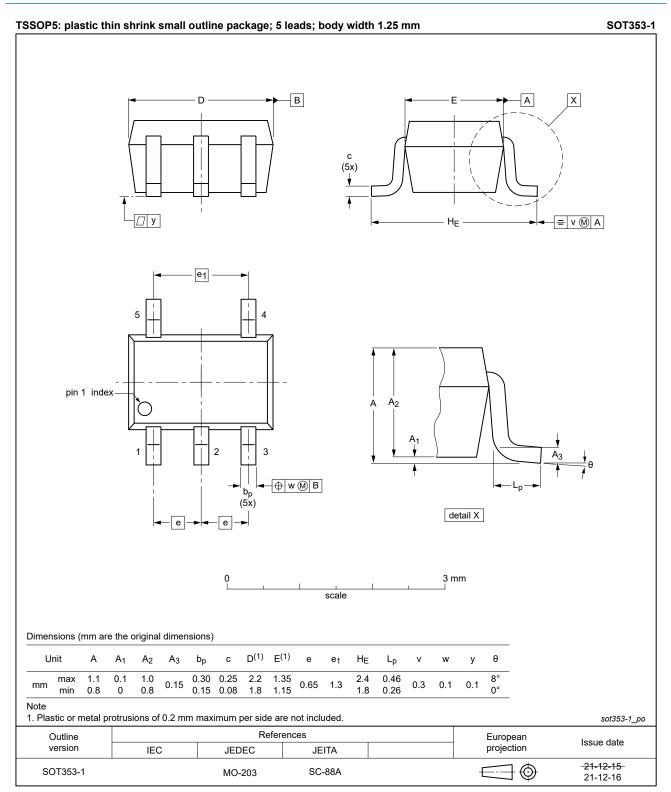
#### Fig. 7. Test circuit for measuring switching times

Supply voltage Input			Load		V <sub>EXT</sub>	V <sub>EXT</sub>		
V <sub>cc</sub>	Vi	Δt/ΔV [1]	f <sub>max</sub>	CL	RL	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>
1.8 V	V <sub>CC</sub>	≤ 1.0 ns/V	15 MHz	15 pF, 30 pF	1 kΩ	open	GND	V <sub>CC</sub>
2.5 V	V <sub>CC</sub>	≤ 1.0 ns/V	25 MHz	15 pF, 30 pF	1 kΩ	open	GND	V <sub>CC</sub>
3.3 V	3 V	≤ 1.0 ns/V	50 MHz	15 pF, 30 pF	1 kΩ	open	GND	V <sub>CC</sub>
5.0 V	3 V	≤ 1.0 ns/V	50 MHz	15 pF, 30 pF	1 kΩ	open	GND	V <sub>CC</sub>

[1] dV/dt ≥ 1.0 V/ns

74LV1T02

## 13. Package outline



#### Fig. 8. Package outline SOT353-1 (TSSOP5)

## 74LV1T02

### 2-input single supply translating NOR gate

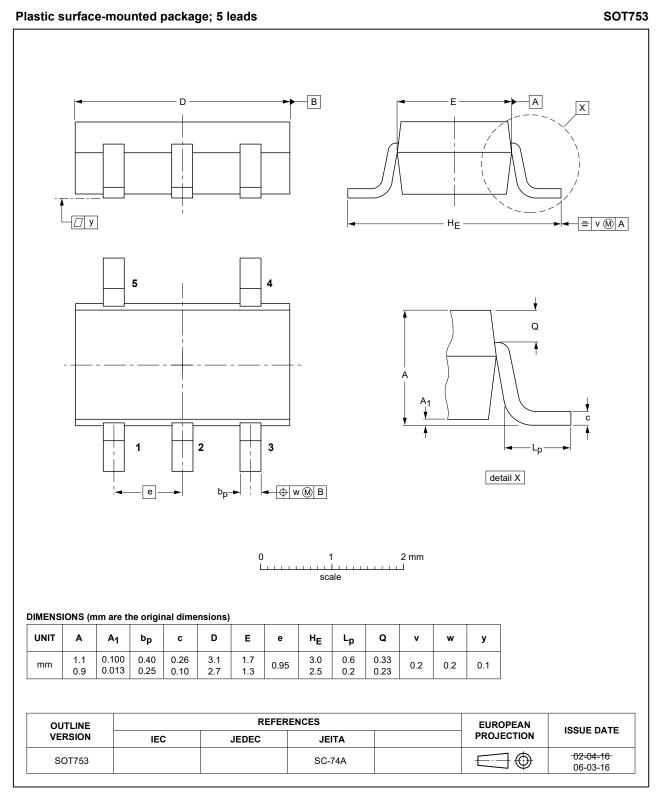


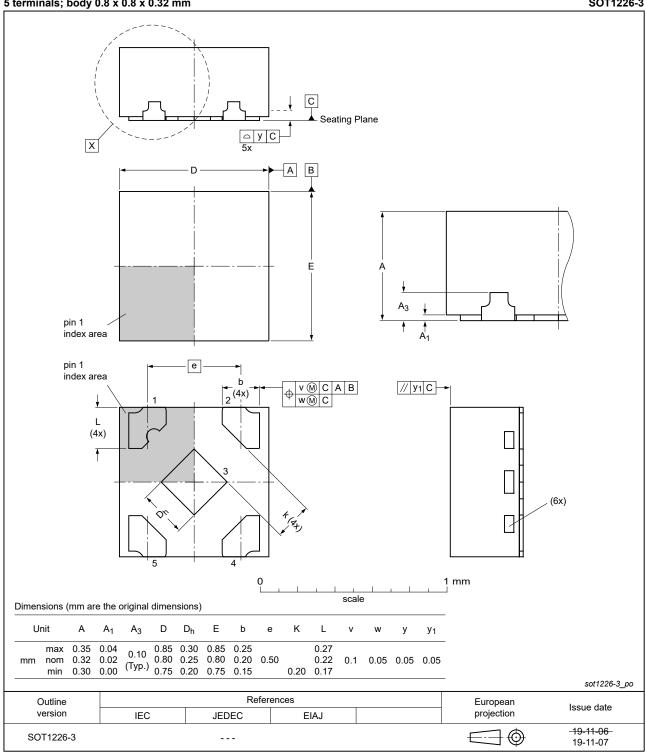
Fig. 9. Package outline SOT753 (SC-74A)

## 74LV1T02

### 2-input single supply translating NOR gate

#### X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 x 0.8 x 0.32 mm

SOT1226-3





**Product data sheet** 

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## 14. Abbreviations

Table 11. Abbreviations						
Acronym	Description					
CDM	Charge Device Model					
CMOS	Complementary Metal Oxide Semiconductor					
DUT	Device Under Test					
ESD	ElectroStatic Discharge					
HBM	Human Body Model					

## 15. Revision history

#### Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74LV1T02 v.4	20220815	Product data sheet	-	74LV1T02 v.3	
Modifications:	<ul> <li>Package SOT1226 (X2SON5) changed to SOT1226-3 (X2SON5).</li> </ul>				
74LV1T02 v.3	20220204	Product data sheet	-	74LV1T02 v.2	
Modifications:	• Fig. 8: Package outline drawing for SOT353-1 (TSSOP5) has changed.				
74LV1T02 v.2	20191203	Product data sheet	-	74LV1T02 v.1	
Modifications:	<ul> <li>Type number 74LV1T02GV (SOT753/SC-74A) added.</li> <li><u>Table 5</u>: Derating values for P<sub>tot</sub> total power dissipation updated.</li> </ul>				
74LV1T02 v.1	20171128	Product data sheet	-	-	

## 16. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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## Contents

1. General description	1
2. Features and benefits	1
3. Applications	1
4. Ordering information	2
5. Marking	2
6. Functional diagram	2
7. Pinning information	3
7.1. Pinning	3
7.2. Pin description	3
8. Functional description	3
9. Limiting values	4
10. Recommended operating conditions	4
11. Static characteristics	5
12. Dynamic characteristics	6
12.1. Waveforms and test circuit	7
13. Package outline	8
14. Abbreviations	11
15. Revision history	11
16. Legal information	12

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