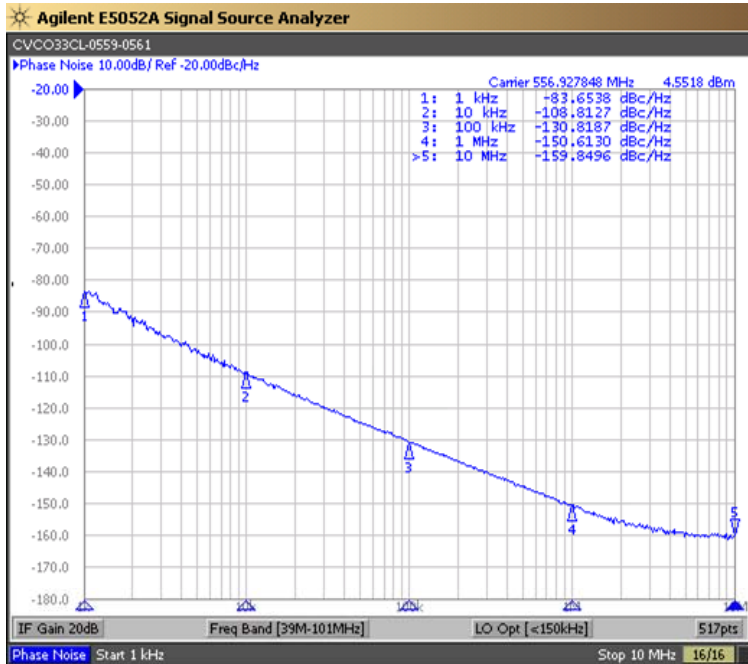


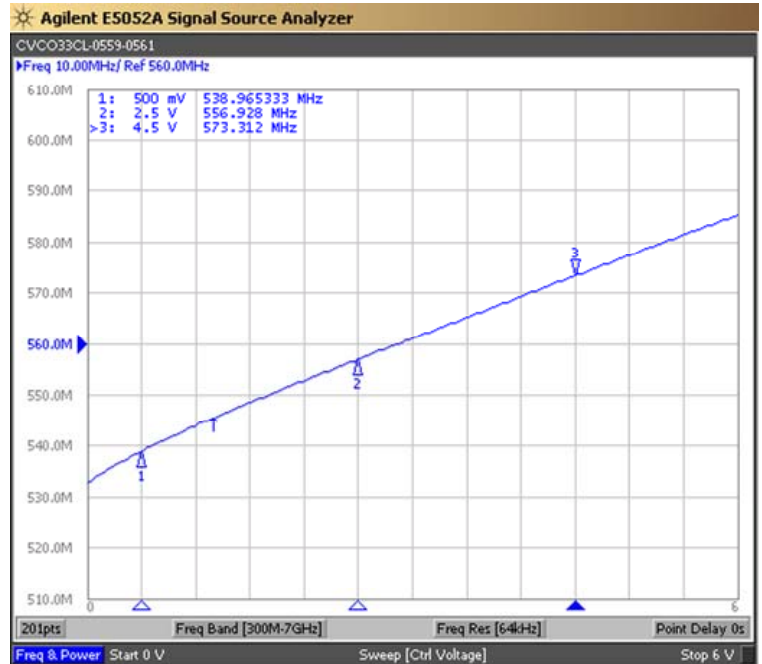


PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			559	MHz
Upper Frequency:	561			MHz
Tuning Voltage:	0.5		4.5	VDC
Supply Voltage:	4.75	5.0	5.25	VDC
Output Power:	-1.0	+1.0	+3.0	dBm
Supply Current:		25		mA
Harmonic Suppression (2 nd Harmonic):		-15		dBc
Pushing:		1.0		MHz/V
Pulling, all Phases:		3.0		MHz pk-pk
Tuning Sensitivity:		12		MHz/V
Phase Noise @ 10kHz offset:		-105		dBc/Hz
Phase Noise @ 100kHz offset:		-130		dBc/Hz
Load Impedance:		50		Ω
Input Capacitance:			100	pF
Operating Temperature Range:	-40		+85	$^{\circ}$ C
Storage Temperature Range:	-45		+90	$^{\circ}$ C

Phase Noise (1 Hz BW, Typical)



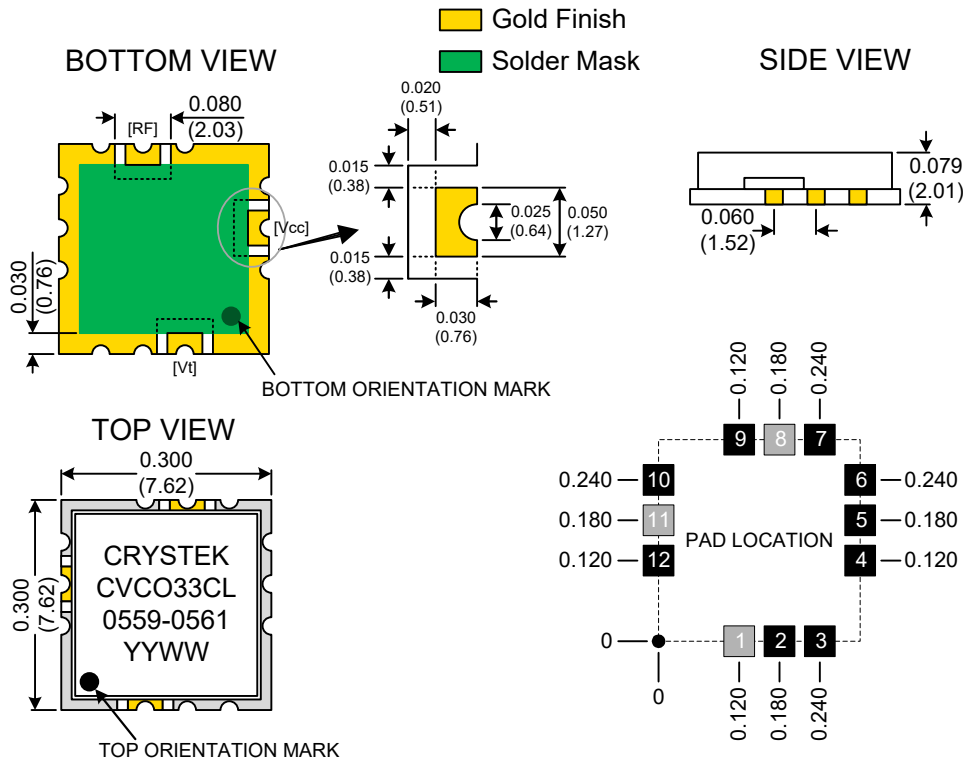
Tuning Curve (Typical)



Product Control:

Crystek Part Number:	CVCO33CL-0591-0561	Release Date:	08-May-2017
Revision Level:	F	Responsible:	C. Vales

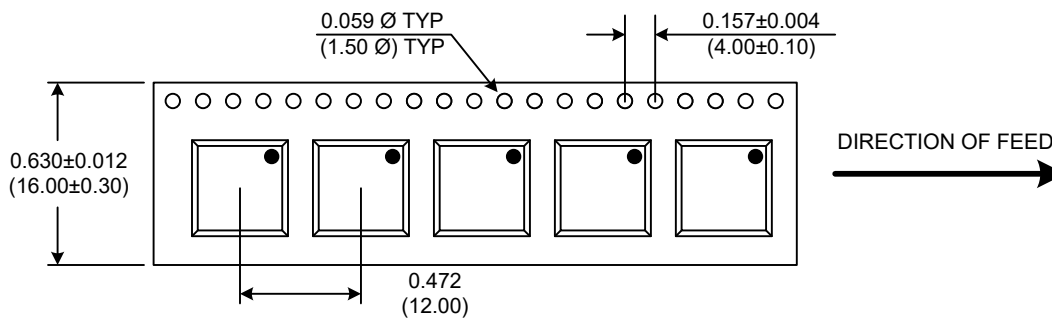




Pad	Connection
1	Vt
8	RF-OUTPUT
11	Vcc
Others	GROUND

- Unless otherwise specified, Dimensions are in: $\frac{IN}{(mm)}$
- Pad Location Dimensions are in: Inches

TAPE AND REEL



Drawing not to scale

Product Control:

Crystek Part Number:	CVCO33CL-0591-0561	Release Date:	08-May-2017
Revision Level:	F	Responsible:	C. Vales

Specification is subject to change without notice

