

SPECIFICATIONS:

ELECTRICAL:

INSULATION RESISTANCE: $5 G\Omega MIN.$

DIELECTRIC WITHSTANDING VOLTAGE: 1000 VAC FOR 1 MIN

WORKING VOLTAGE: 250V

OPERATING TEMPERATURE: -55° C TO +125°C POWER CONTACT RESISTANCE: 2.0 m Ω MAX. SIGNAL CONTACT RESISTANCE: 10 m Ω MAX. POWER PINS: 40 AMPS SIGNAL PINS: 5 AMPS

MECHANICAL:

CONTACT INSERTION FORCE: 4.5 Kg MAX.
CONTACT SEPARATION FORCE: 0.3 Kg MIN.

MATERIALS:

CONTACTS: COPPER ALLOY, GOLD FLASH

INSULATOR:

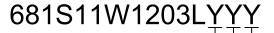
PBT UL94V-0 RATED
230°C PROCESS TEMP.

SEAL:

F04 FLEXIBLE ADHESIVE
SHELL:

NICKEL PLATED STEEL
HARDWARE:

NICKEL PLATED BRASS



0 = 40 AMP

0 = NO HARDWARE -

4 = 0.237" BOARDLOCK PCB SIDE

1 = GOLD FLASH

MEETS IP67 STANDARDS

(SEE PAGE 2 FOR SEALING DETAILS)

ROHS COMPLIANT

M.

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DRAWN:

M. SIGMON

DATE:

06/11/2013

SEE SHEET 2 FOR DETAILS

UNITS = INCHES [MM] DO NOT SCALE FROM DRAWING



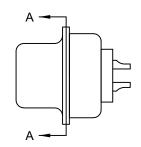
SCALE: SHEET OF 1 4

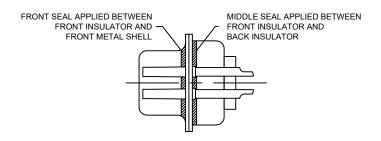
DWG NO. 681S11W1203LYYY

REV

3

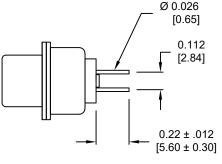
DESCRIPTION: 681 SERIES CONTACTS



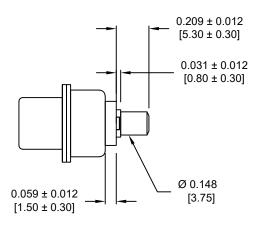


DETAIL A-A
SHOWN FOR REFERENCE ONLY

NOTE: SIGNAL AND POWER CONTACTS COME PRE-LOADED INTO INSULATORS



SIGNAL CONTACTS



POWER CONTACTS

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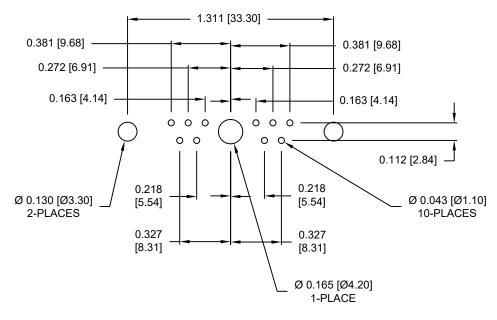
06/11/2013

UNITS = INCHES [MM] DO NOT SCALE FROM DRAWING



SCALE: NTS	SHEET 2	OF	4	REV 3
DWG NO.	681911	۱۸/1	2U3I VVV	

DESCRIPTION: PCB LAYOUT



RECOMMENDED PCB LAYOUT

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M. SIGMON

DATE: 06/11/2013

UNITS = INCHES [MM] DO NOT SCALE FROM DRAWING

SCALE: NTS	SHEET	3	OF	4	REV 3
DWG NO.	681S	11\	N12	203LYYY	

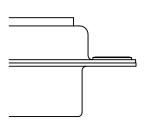
DESCRIPTION: CLINCH NUT HARDWARE

ALL CLINCH-NUTS ARE 4-40 INTERNAL THREADS

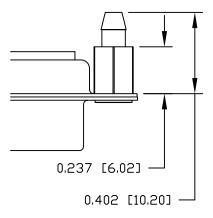
TOLERANCE:

ALL DIMENSIONS ARE ±0.010

0 NO HARDWARE



4 0.237" PCB SIDE



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M. SIGMON

DATE:

06/11/2013

UNITS = INCHES [MM]	DO	NOT	SCALE	FROM	DRAWING
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SCALE: NTS
DWG NO

OWG NO. 681S11W1203LYYY