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Vishay Dale

IHLP[®] Commercial Inductors, Low DCR Series



LINKS TO ADDITIONAL RESOURCES

3	D				
3D Models					

] <u>Design T</u>

STANDARD ELECTRICAL SPECIFICATIONS							
L ₀ INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (μH)	DCR TYP. 25 °C (mΩ)	DCR MAX. 25 °C (mΩ)	HEAT RATING CURRENT DC TYP. (A) ⁽¹⁾	SATURATION CURRENT DC TYP. (A) ⁽²⁾			
0.19	0.70	0.80	40	46			
0.22	0.85	0.95	33	44			
0.24	0.85	0.95	33	44			
0.36	1.05	1.15	32	30			
0.47	1.53	1.68	30	30			
0.56	1.61	1.80	32	22			
0.78	1.80	1.90	27	22			
1.0	2.30	2.50	25	20			
1.8	4.50	5.00	17	16			
2.0	5.20	5.80	16	14			
4.7	12.9	14.2	9.5	7.6			
6.8	17.5	19.3	9.0	7.5			
10	27.8	30.5	7.5	7.1			
15	40.9	45.0	6.25	6.0			
18	46.40	51.90	5.6	4.6			
22	60.4	66.0	5.0	4.5			
33	87.5	94.5	4.4	4.0			
47	132.0	145.0	3.3	3.0			
100	249.0	270.0	2.5	2.25			

Notes

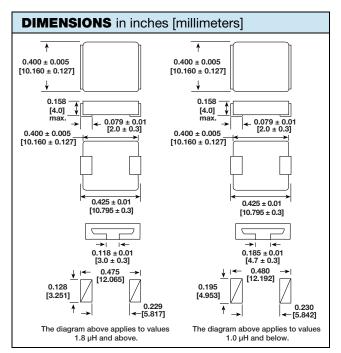
- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +125 °C
- The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- Rated operating voltage (across inductor) = 50 V $^{(1)}$ DC current (A) that will cause an approximate ΔT of 40 °C
- ⁽⁷⁾ DC current (A) that will cause an approximate $\Delta 1$ of 40 °C ⁽²⁾ DC current (A) that will cause L₀ to drop approximately 20 %

FEATURES

- Shielded construction
- Frequency range up to 1.0 MHz
- Lowest DCR/µH, in this package size
- Lowest DCh/µn, in this package size
 Handles high transient current spikes without saturation
 Ultra low buzz noise due to composite
- Ultra low buzz noise, due to composite construction
- Excellent temperature stability for inductance and saturation
- IHLP design; PATENT(S): <u>www.vishay.com/patents</u>
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- PDA/notebook/desktop/server applications
- High current POL converters
- · Low profile, high current power supplies
- · Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for field programmable gate array (FPGA)



DESCRIPTION						
IHLP-4040DZ-11	2.0 µH	± 20 %	ER	e3		
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERAN	ICE PACKAGE CODE	JEDEC [®] LEAD (Pb)-FREE STANDARD		
GLOBAL PART NUMBER						
		4 0 D SIZE	Z E R 2 PACKAGE	R 0 M 1 1 INDUCTANCE TOL. SERIES		
	vishav.com/patents	-	CODE	VALUE		

This Vishay product is protected by one or more United States and international patents.

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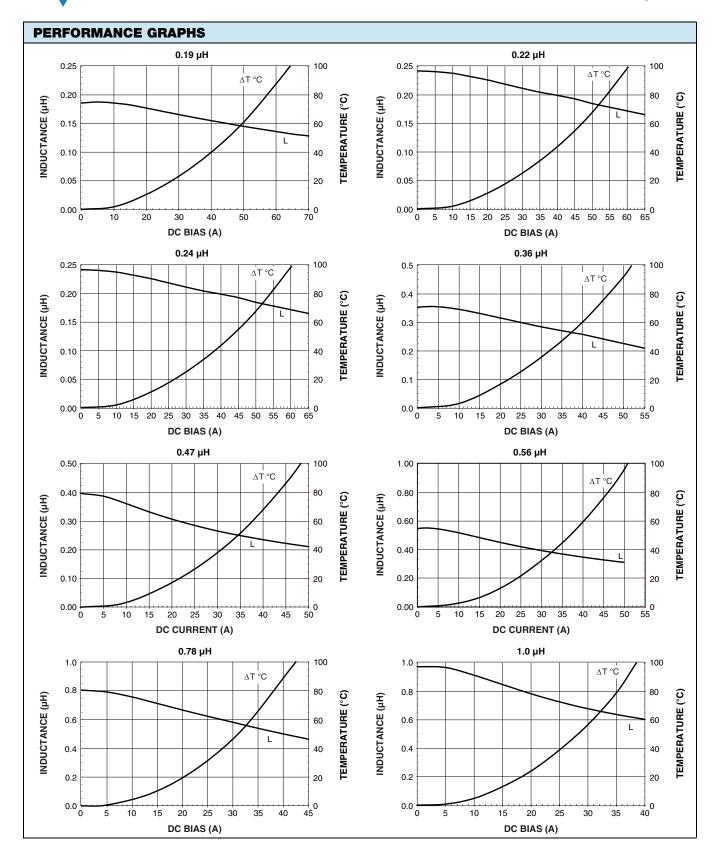
Pb-free

¹





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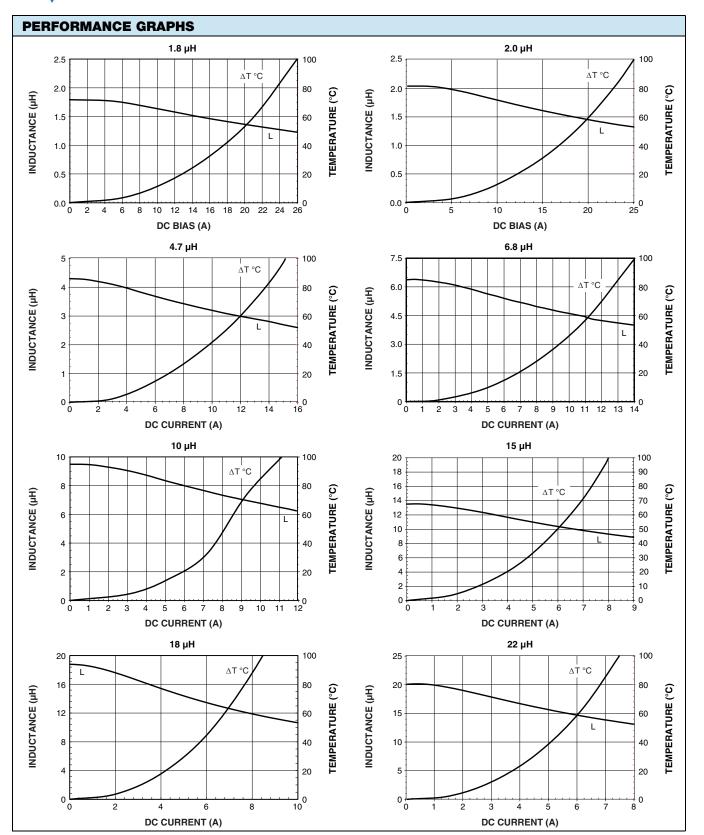
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IHLP-4040DZ-11

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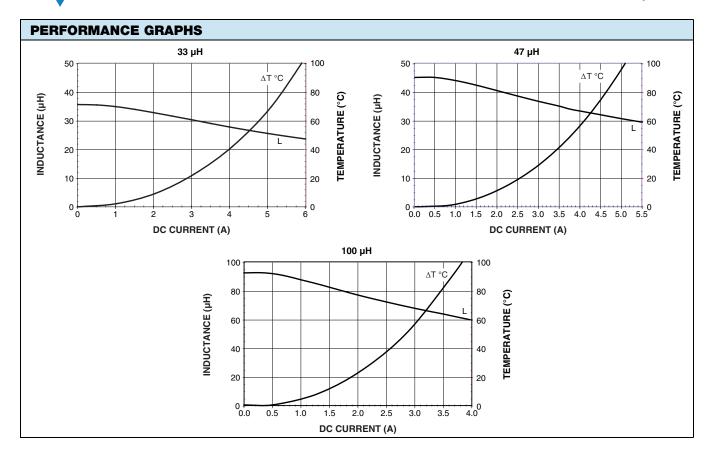
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