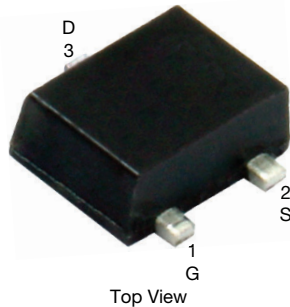


P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)	Q _g (TYP.) (nC)
-20	0.760 at V _{GS} = -4.5 V	-0.45	1
	1.040 at V _{GS} = -2.5 V	-0.40	
	1.500 at V _{GS} = -1.8 V	-0.32	

SC-89 (3 leads)


Top View

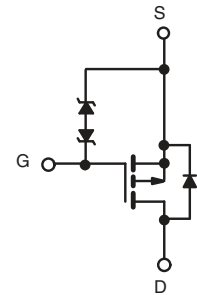
FEATURES

- TrenchFET® power MOSFET
- 100 % R_g tested
- Typical ESD protection: 1000 V (HBM)
- Fast switching speed
- Material categorization:
for definitions of compliance please see www.vishay.com/doc?99912


RoHS
 COMPLIANT
 HALOGEN
FREE

APPLICATIONS

- Load / power switch for portable devices
- Drivers: relays, solenoids, displays
- Battery operated systems



P-Channel MOSFET

Marking Code: 6

Ordering Information:

Si1013CX-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)				
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	V _{DS}	-20	V	
Gate-Source Voltage	V _{GS}	± 8		
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	-0.45 ^{b, c}	A	
	T _A = 70 °C	-0.36 ^{b, c}		
Pulsed Drain Current (t = 300 μs)	I _{DM}	-1.5		
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S		-0.16 ^{b, c}
Maximum Power Dissipation	T _A = 25 °C	P _D	0.19 ^{b, c}	W
	T _A = 70 °C		0.12 ^{b, c}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum Junction-to-Ambient ^{a, b}	t ≤ 5 s	440	530	°C/W	
	Steady State	540	650		

Notes

- Maximum under steady state conditions is 650 °C/W.
- Surface mounted on 1" x 1" FR4 board.
- t = 5 s.



SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0, I _D = -250 μA	-20	-	-	V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = -250 μA	-	-12	-	mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J		-	1.8	-	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250 μA	-0.4	-	-1	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 8 V	-	-	± 30	μA
		V _{DS} = 0 V, V _{GS} = ± 4.5 V	-	-	± 1	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -20 V, V _{GS} = 0 V	-	-	-1	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = -20 V, V _{GS} = 0 V, T _J = 85 °C	-	-	-10	A
		V _{DS} = ≥ 5 V, V _{GS} = -4.5 V	-1.5	-	-	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = -4.5 V, I _D = -0.4 A	-	0.630	0.760	Ω
		V _{GS} = -2.5 V, I _D = -0.2 A	-	0.865	1.040	
		V _{GS} = -1.8 V, I _D = -0.1 A	-	1.200	1.500	
Forward Transconductance	g _{fs}	V _{DS} = -10 V, I _D = 0.4 A	-	1	-	S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	-	45	-	pF
Output Capacitance	C _{oss}		-	15	-	
Reverse Transfer Capacitance	C _{rss}		-	10	-	
Total Gate Charge	Q _g	V _{DS} = -10 V, V _{GS} = -4.5 V, I _D = -0.4 A	-	1.65	2.50	nC
		V _{DS} = -0 V, V _{GS} = -2.5 V, I _D = -0.4	-	1	2	
Gate-Source Charge	Q _{gs}	-	0.2	-		
Gate-Drain Charge	Q _{gd}	-	0.26	-		
Gate Resistance	R _g	f = 1 MHz	2.4	12	24	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = -10 V, R _L = 33.3 Ω I _D ≅ -0.3 A, V _{GEN} = -4.5 V, R _g = 1 Ω	-	9	18	ns
Rise Time	t _r		-	10	20	
Turn-Off Delay Time	t _{d(off)}		-	10	20	
Fall Time	t _f		-	8	16	
Turn-On Delay Time	t _{d(on)}	V _{DD} = -10 V, R _L = 33.3 Ω I _D ≅ -0.3 A, V _{GEN} = -8 V, R _g = 1 Ω	-	1	2	
Rise Time	t _r		-	8	16	
Turn-Off Delay Time	t _{d(off)}		-	9	18	
Fall Time	t _f		-	5	10	
Drain-Source Body Diode Characteristics						
Pulse Diode Forward Current ^a	I _{SM}		-	-	-1.5	A
Body Diode Voltage	V _{SD}	I _S = -0.3 A	-	-0.8	-1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = -0.3 A, di/dt = 100 A/μs	-	16	24	ns
Body Diode Reverse Recovery Charge	Q _{rr}		-	8	16	nC
Reverse Recovery Fall Time	t _a		-	11	-	ns
Reverse Recovery Rise Time	t _b		-	5	-	

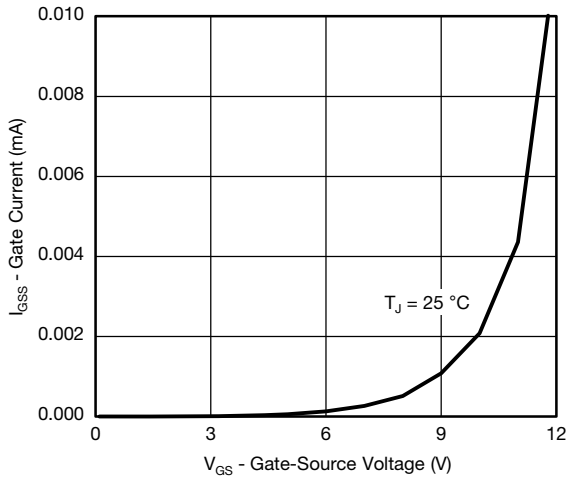
Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- b. Guaranteed by design, not subject to production testing.

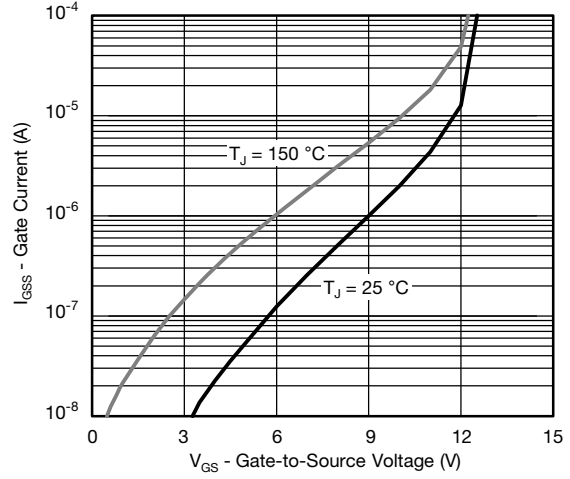
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



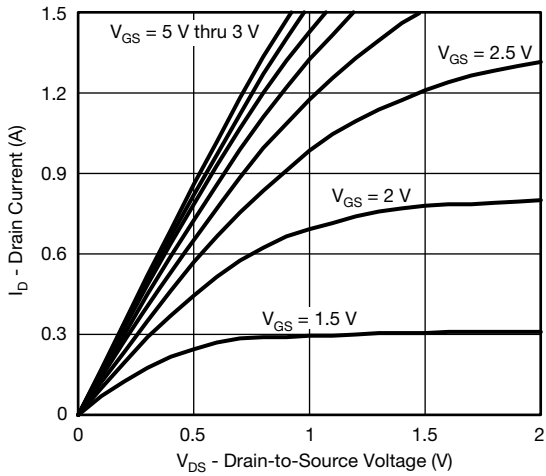
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



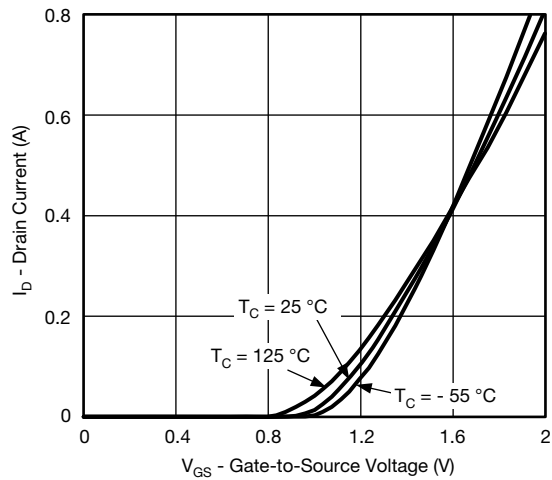
Gate Current vs. Gate-Source Voltage



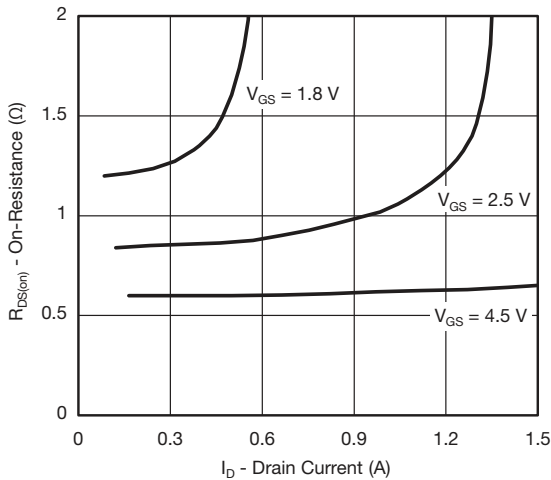
Gate Current vs. Gate-Source Voltage



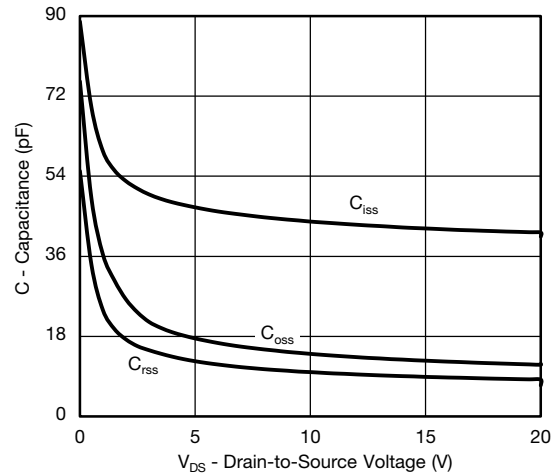
Output Characteristics



Transfer Characteristics



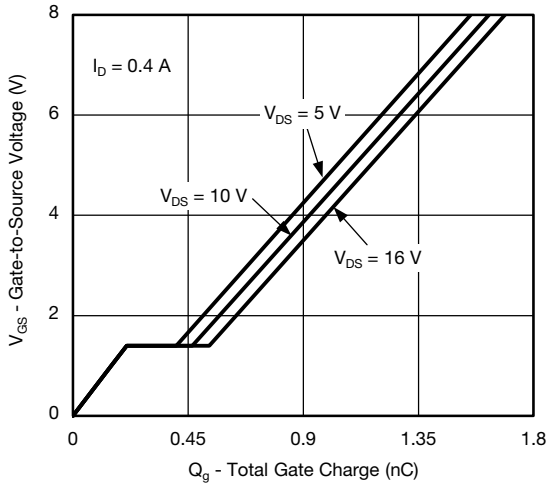
On-Resistance vs. Drain Current



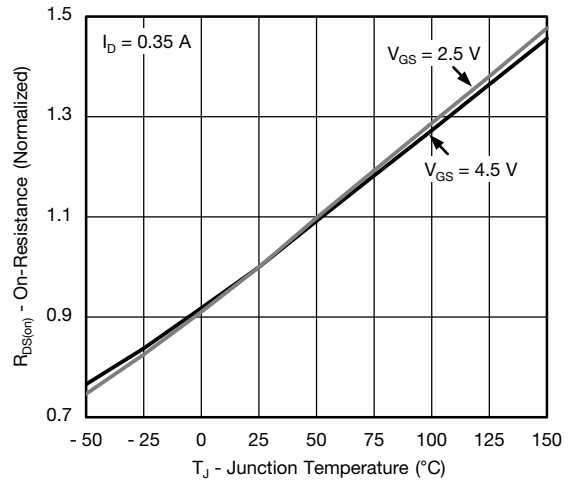
Capacitance



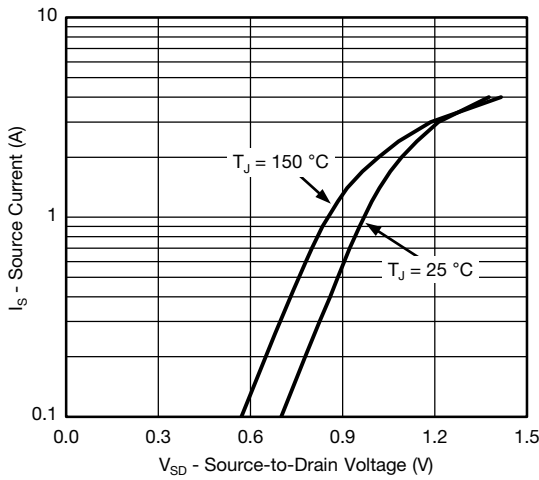
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



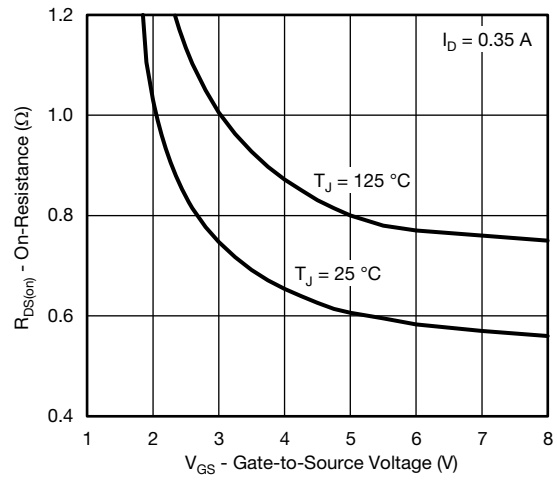
Gate Charge



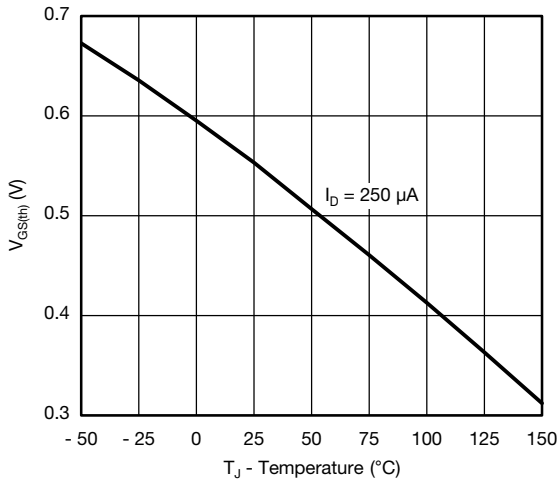
On-Resistance vs. Junction Temperature



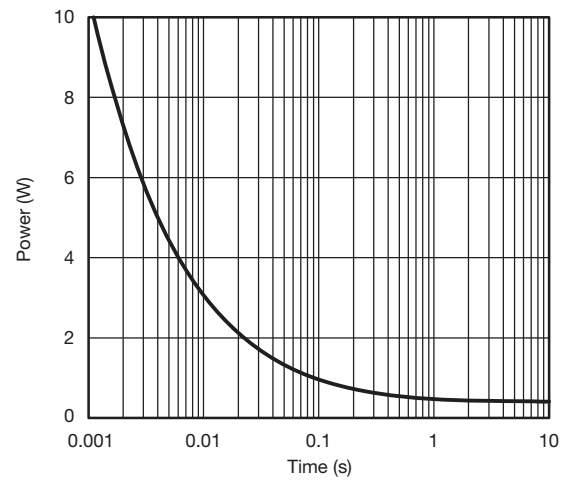
Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



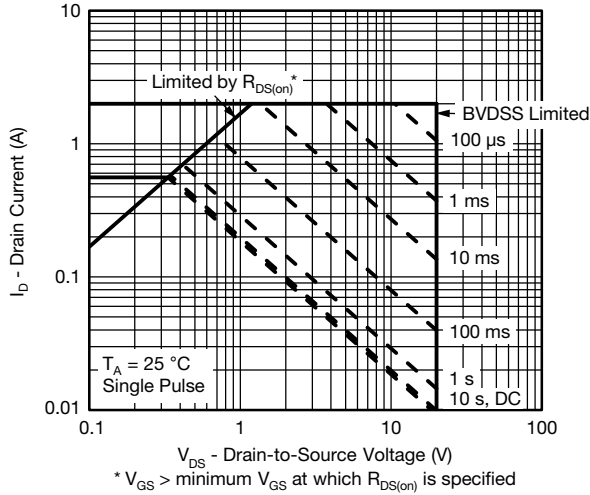
Threshold Voltage



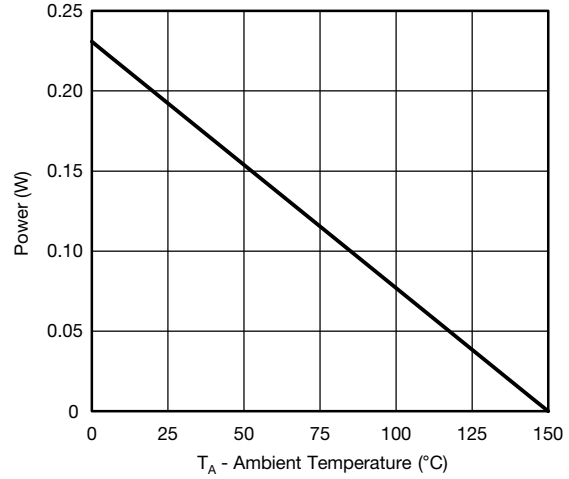
Single Pulse Power, Junction-to-Ambient



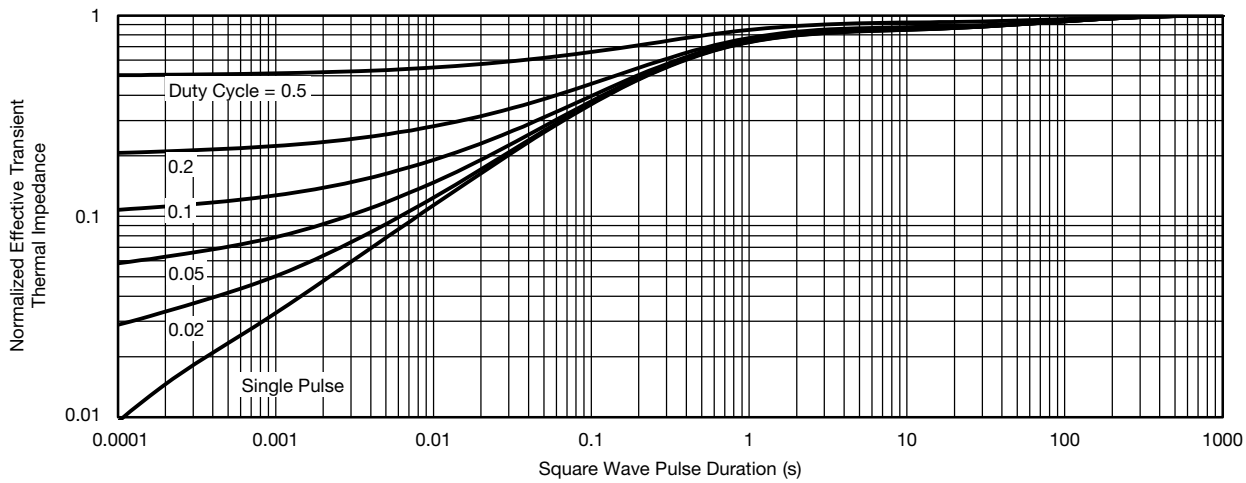
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Safe Operating Area, Junction-to-Ambient



Power Derating, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient

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SC89-3



DETAIL X

Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	0.60	0.80	0.024	0.031
b	0.23	0.33	0.009	0.013
C	0.10	0.20	0.004	0.008
D	1.50	1.70	0.059	0.067
E	0.75	0.95	0.030	0.037
e	1.00 BSC		0.040 BSC	
e₁	0.50 BSC		0.020 BSC	
H	1.50	1.70	0.059	0.067
L	0.30	0.50	0.012	0.020

ECN: S-03946—Rev. B, 09-Jul-01
DWG: 5869

RECOMMENDED MINIMUM PADS FOR SC-89: 3-Lead



Recommended Minimum Pads
Dimensions in Inches/(mm)

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