

Vishay Siliconix

Automotive N-Channel 250 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY				
V _{DS} (V)	250			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.0300			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 7.5 \text{ V}$	0.0320			
I _D (A)	53			
Configuration	Single			
Package	TO-220			

FEATURES

- TrenchFET® power MOSFET
- Package with low thermal resistance
- AEC-Q101 qualified
- 100 % R_a and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



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N-Channel MOSFET	o S

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-source voltage		V _{DS}	250	V	
Gate-source voltage		V _{GS}	± 20	V	
Continuous drain current	T _C = 25 °C		53		
Continuous drain current	T _C = 125 °C	l _D	30		
Continuous source current (diode conduction) ^a	Is	120	Α		
Pulsed drain current ^b		I _{DM}	180		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	41		
Single pulse avalanche energy	L = 0.1 IIII1	E _{AS}	84	mJ	
Maximum power dissipation ^b	T _C = 25 °C	В	250	W	
	T _C = 125 °C	P_D	83	VV	
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient	PCB mount c	R_{thJA}	40	°C/W	
Junction-to-case (drain)		R_{thJC}	0.6	C/VV	

Notes

- a. Package limited
- b. Pulse test; pulse width $\leq 300~\mu\text{s},$ duty cycle $\leq 2~\%$
- c. When mounted on 1" square PCB (FR4 material)



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static						l	
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		250	-	-	.,
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$		3.0	3.5	V
Gate-source leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 100	nA
		$V_{GS} = 0 V$	V _{DS} = 250 V	-	-	1	
Zero gate voltage drain current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 250 V, T _J = 125 °C	-	-	50	μΑ
		V _{GS} = 0 V	V _{DS} = 250 V, T _J = 175 °C	-	-	600	
On-state drain current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	30	-	-	Α
		V _{GS} = 10 V	I _D = 15 A	-	0.0244	0.0300	
Due in account on state westerness 2	Б	V _{GS} = 7.5 V	I _D = 10 A	-	0.0260	0.0320	
Drain-source on-state resistance a	R _{DS(on)}	V _{GS} = 10 V	I _D = 15 A, T _J = 125 °C	-	-	0.0641	Ω
		V _{GS} = 10 V	I _D = 15 A, T _J = 175 °C	-	-	0.0853	
Forward transconductance b	9 _{fs}	V _{DS}	= 15 V, I _D = 15 A	-	50	-	S
Dynamic ^b							
Input capacitance	C _{iss}			-	2880	4050	
Output capacitance	C _{oss}	$V_{GS} = 0 V$	V _{DS} = 25 V, f = 1 MHz	-	1480	2100	pF
Reverse transfer capacitance	C _{rss}			-	58	85	
Total gate charge ^c	Qg			-	50	75	
Gate-source charge c	Q _{gs}	V _{GS} = 10 V	$V_{GS} = 10 \text{ V}$ $V_{DS} = 125 \text{ V}, I_D = 10 \text{ A}$		12	-	nC
Gate-drain charge ^c	Q _{gd}				15	-	
Gate Resistance	R_g	f = 1 MHz		1.40	2.84	4.40	Ω
Turn-on delay time ^c	t _{d(on)}			-	14	30	
Rise time ^c	t _r	V _{DD} =	125 V, R_L = 12.5 $Ω$	-	6	15	
Turn-off delay time ^c	t _{d(off)}	$I_D \cong 10 A$,	$I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		38	60	ns
Fall time ^c	t _f				10	20	
Source-Drain Diode Ratings and Chara	acteristics ^b						
Pulsed current ^a	I _{SM}			-	-	180	Α
Forward voltage	V _{SD}	I _F =	I _F = 20 A, V _{GS} = 0 V		0.82	1.5	V
Body diode reverse recovery time	t _{rr}	I _F = 10 A, di/dt = 100 A/μs		-	155	310	ns
Body diode reverse recovery charge	Q _{rr}			-	933	1900	nC
Reverse recovery fall time	t _a			-	122	-	ns
Reverse recovery rise time	t _b			-	33	-	
Body diode peak reverse recovery current	I _{RM(REC)}			-	-11.6	-	Α

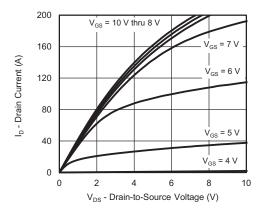
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

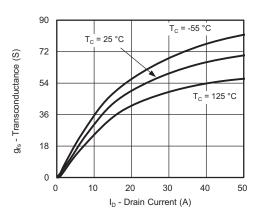
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



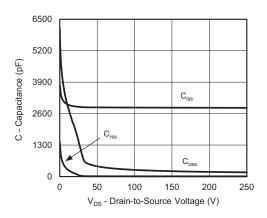
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



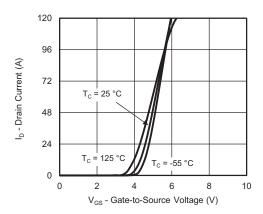
Output Characteristics



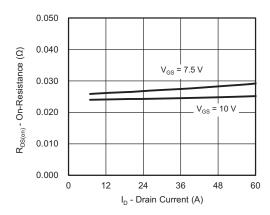
Transconductance



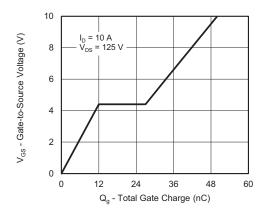
Capacitance



Transfer Characteristics



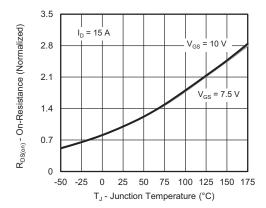
On-Resistance vs. Drain Current



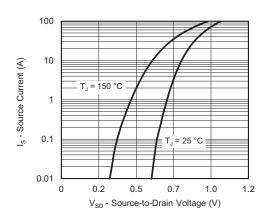
Gate Charge



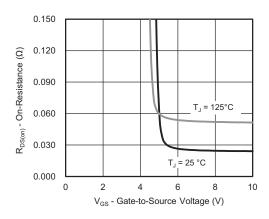
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



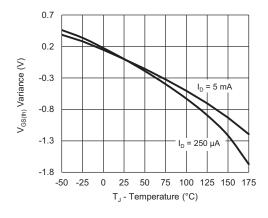
On-Resistance vs. Junction Temperature



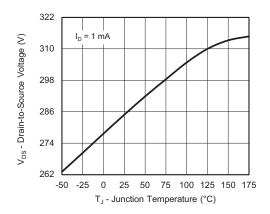
Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



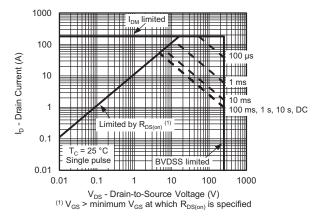
Threshold Voltage



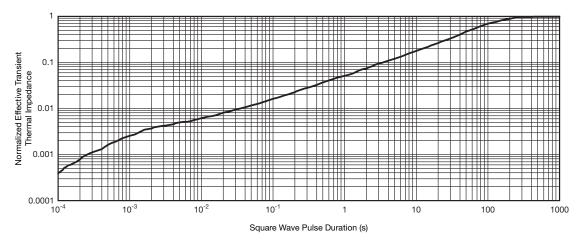
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



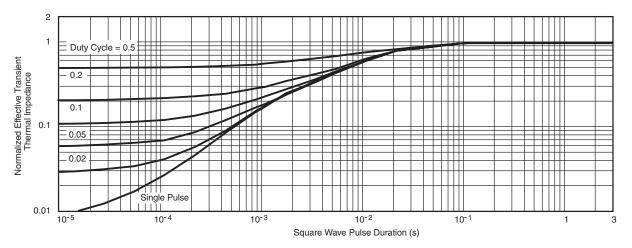
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction to Case (25 °C)

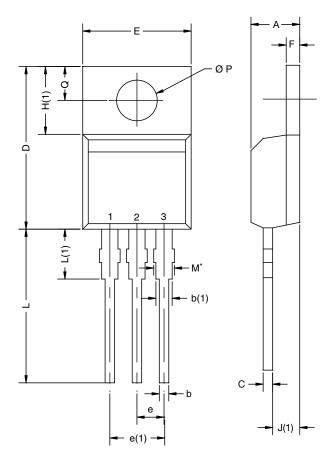
are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg276717



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TO-220AB



	D2

	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
D2	12.19	12.70	0.480	0.500
Е	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØΡ	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
ECN: T14-0413-Rev. P, 16-Jun-14 DWG: 5471				

Note

 $^{^{\}star}$ M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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