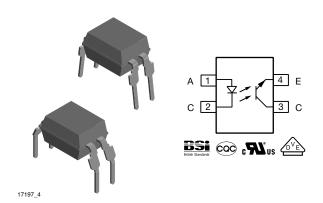
TCET1201, TCET1202, TCET1203, TCET1204

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Optocoupler, Phototransistor Output



LINKS TO ADDITIONAL RESOURCES



DESCRIPTION

The TCET1200 consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4 pin plastic dual inline package.

FEATURES

- High common mode rejection
- CTR offered in 5 groups
- · Low temperature coefficient of CTR
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912





RoHS COMPLIANT

APPLICATIONS

- Switch-mode power supplies
- · Line receiver
- Computer peripheral interface
- Microprocessor system interface
- Reinforced isolation provides circuit protection against electrical shock (safety class II)
- Circuits for safe protective separation against electrical shock according to safety class II (reinforced isolation):
 - for appl. class I IV at mains voltage \leq 300 V
 - for appl. class I III at mains voltage ≤ 600 V according to DIN EN 60747-5-5 (VDE 0884-5)

AGENCY APPROVALS

- <u>UL1577</u>
- cUL1577
- DIN EN 60747-5-5 (VDE 0884-5)
- BSI
- CQC GB8898-2011
- CQC GB4943.1-2011

ORDERING INFORMATION							
T C E	T 1	2 0	#	DIP			
_	PART NUMBER			7.62 mm			
AGENCY CERTIFIED / PACKAGE	CTR (%)						
AGENCY CENTIFIED / PACKAGE		10 mA					
UL, VDE, BSI, FIMKO	40 to 80	63 to 125	100 to 200	160 to 320			
DIP-4	TCET1201	TCET1202	TCET1203	TCET1204			

TCET1201, TCET1202, TCET1203, TCET1204

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PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Reverse voltage		V_{R}	6	V
Forward current		I _F	60	mA
Forward surge current	t _p ≤ 10 μs	I _{FSM}	1.5	Α
Power dissipation		P _{diss}	70	mW
Junction temperature		T _j	125	°C
OUTPUT				
Collector emitter voltage		V _{CEO}	70	V
Emitter collector voltage		V _{ECO}	7	V
Collector current		Ic	50	mA
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I _{CM}	100	mA
Power dissipation		P _{diss}	70	mW
Junction temperature		Tj	125	°C
COUPLER				
Isolation test voltage (RMS)		V _{ISO}	5000	V _{RMS}
Total power dissipation		P _{tot}	200	mW
Operating ambient temperature range		T _{amb}	-40 to +100	°C
Storage temperature range		T _{stg}	-55 to +125	°C
Soldering temperature (1)	2 mm from case, t ≤ 10 s	T _{sld}	260	°C

Notes

⁽¹⁾ Refer to wave profile for soldering conditions for through hole devices

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT								
Forward voltage	$I_F = 50 \text{ mA}$	V_{F}	-	1.25	1.6	V		
Junction capacitance	$V_R = 0 V$, $f = 1 MHz$	C _j	-	50	-	pF		
OUTPUT								
Collector emitter voltage	I _C = 1 mA	V_{CEO}	70	-	-	V		
Emitter collector voltage	I _E = 100 μA	V _{ECO}	7	-	-	V		
Collector emitter cut-off current	$V_{CE} = 20 \text{ V}, I_F = 0 \text{ A}, E = 0$	I _{CEO}	-	10	100	nA		
COUPLER								
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 1 \text{ mA}$	V _{CEsat}	-	-	0.3	V		
Cut-off frequency	V_{CE} = 5 V, I_F = 10 mA, R_L = 100 Ω	f _c	-	110	-	kHz		
Coupling capacitance	f = 1 MHz	C _k	-	0.6	-	pF		

Note

Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

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CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I _C /I _F	$V_{CE} = 5 \text{ V}, I_{F} = 5 \text{ mA}$	TCET1200	CTR	50	-	600	%
		TCET1201	CTR	40	-	80	%
	$V_{CF} = 5 \text{ V}, I_{F} = 10 \text{ mA}$	TCET1202	CTR	63	-	125	%
	V _{CE} = 5 V, I _F = 10 IIIA	TCET1203	CTR	100	-	200	%
		TCET1204	CTR	160	ı	320	%

SAFETY AND INSULATION RATED PARAMETERS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Partial discharge test voltage - routine test	100 %, t _{test} = 1 s	V _{pd}	1.6	-	-	kV	
Partial discharge test voltage - lot test (sample test)	t _{Tr} = 60 s, t _{test} = 10 s, (see Fig. 2)	V _{pd}	1.36	-	-	kV	
Insulation resistance	V _{IO} = 500 V	R _{IO}	10 ¹²	-	-	Ω	
	$V_{IO} = 500 \text{ V}, T_{amb} = 100 ^{\circ}\text{C}$	R _{IO}	10 ¹¹	-	-	Ω	
	V _{IO} = 500 V, T _{amb} = 150 °C (construction test only)	R _{IO}	10 ⁹	-	-	Ω	
Rated impulse voltage		V _{IOTM}	-	-	6	kV	
Max. working voltages	Recurring peak voltage	V _{IORM}	850	-	-	V	
Forward current		I _F	-	-	130	mA	
Power dissipation		P _{diss}	-	-	265	mW	
Safety temperature		T _{si}	-	-	150	°C	
Creepage distance			-	-	7.6	mm	

Note

 According to DIN EN 60747-5-2 (VDE 0884) (see figure 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits

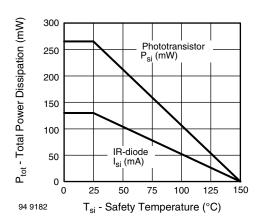


Fig. 1 - Derating Diagram

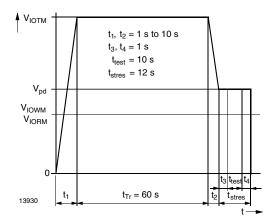


Fig. 2 - Test Pulse Diagram for Sample Test According to DIN EN 60747-5-5



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SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Delay time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega, (\text{see Fig. 3})$	t _d	-	3	-	μs	
Rise time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega, (see Fig. 3)$	t _r	=	3	-	μs	
Fall time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega, \text{ (see Fig. 3)}$	t _f	=	4.7	-	μs	
Storage time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega, (\text{see Fig. 3})$	t _s	-	0.3	-	μs	
Turn-on time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega, (see Fig. 3)$	t _{on}	=	6	-	μs	
Turn-off time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega, \text{ (see Fig. 3)}$	t _{off}	=	5	-	μs	
Turn-on time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega, \text{ (see Fig. 4)}$	t _{on}	-	9	-	μs	
Turn-off time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega, \text{ (see Fig. 4)}$	t _{off}	-	10	-	μs	

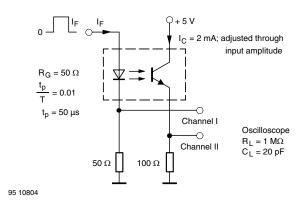


Fig. 3 - Test Circuit, Non-Saturated Operation

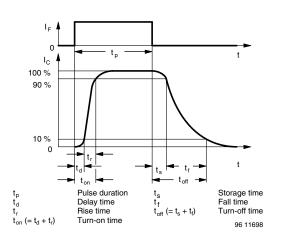


Fig. 5 - Switching Times

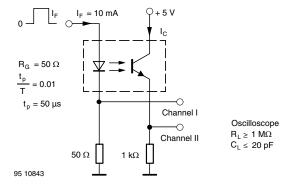


Fig. 4 - Test Circuit, Saturated Operation

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TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

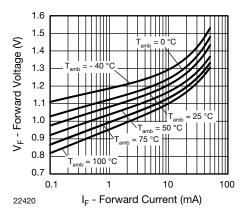


Fig. 6 - Forward Voltage vs. Forward Current

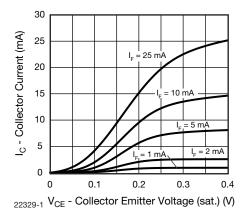


Fig. 9 - Collector Current vs. Collector Emitter Voltage (saturated)

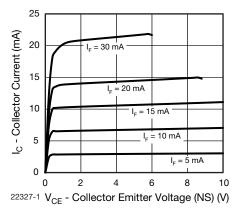


Fig. 7 - Collector Current vs. Collector Emitter Voltage (non-saturated)

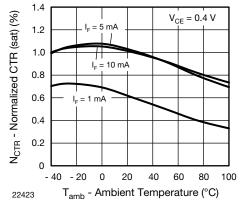


Fig. 10 - Normalized CTR (saturated) vs. Ambient Temperature

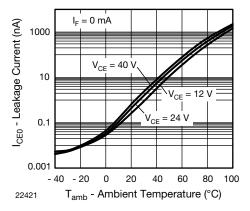


Fig. 8 - Leakage Current vs. Ambient Temperature

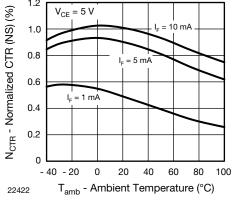


Fig. 11 - Normalized CTR (non-saturated) vs. Ambient Temperature

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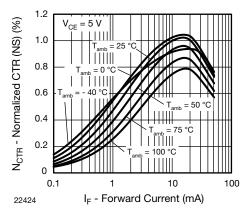


Fig. 12 - Normalized CTR (non-saturated) vs. Forward Current

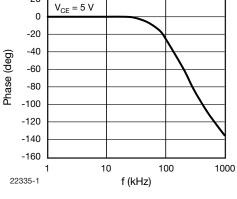


Fig. 15 - F_{CTR} vs. Phase Angle (kHz)

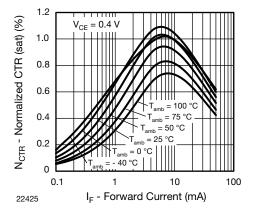


Fig. 13 - Normalized CTR (saturated) vs. Forward Current

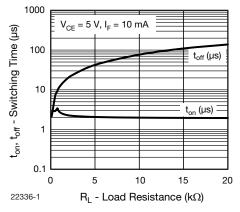


Fig. 16 - Switching Time vs. Load Resistance

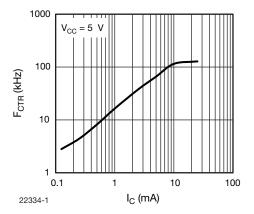
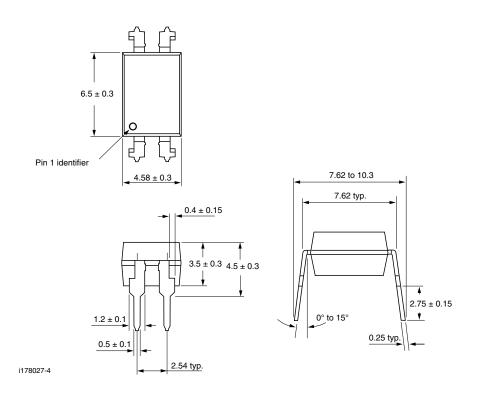


Fig. 14 - F_{CTR} vs. I_C (saturated) (mA)

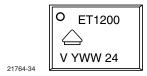
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PACKAGE DIMENSIONS in millimeters



PACKAGE MARKING



Note

• VDE logo is only printed on option 1 parts. Option information is not marked on the part



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