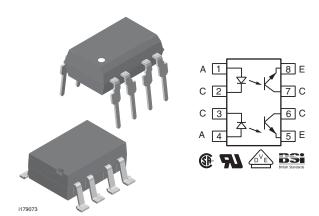


Vishay Semiconductors

Optocoupler, Phototransistor Output, Dual Channel



DESCRIPTION

The MCT6 is a two channel optocoupler for high density applications. Each channel consists of an optically coupled pair with a gallium arsenide infrared LED and a silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output.

The MCT6 is especially designed for driving medium-speed logic, where it may be used to eliminate troublesome ground loop and noise problems. It can also be used to replace relays and transformers in many digital interface applications, as well as analog applications such as CRT modulation.

FEATURES

- Current transfer ratio, 50 % typical
- Leakage current, 1.0 nA typical
- Two isolated channels per package
- Compliant to RoHS Directive and accordance to WEEE 2002/96/EC





AGENCY APPROVALS

- UL1577, file no. E52744 system code H, double protection
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 pending available with option 1
- CSA 93751
- BSI IEC 60950; IEC 60065

ORDERING INFORMATION						
M C T 6 PART NUMBER	PACKAGE OPTION TAPE AND REEL Option 7 Option 9					
AGENCY CERTIFIED/PACKAGE	CTR					
UL, CSA, BSI	≥ 20					
DIP-8	MCT6					
SMD-8, option 7	MCT6-X007T (1)					
SMD-8, option 9	MCT6-X009T (1)					
VDE, UL, CSA, BSI	≥ 20					
DIP-8	MCT6-X001					

- Additional options may be possible, please contact sales office.
 Also available in tubes, do not put "T" on the end.



Optocoupler, Phototransistor Output, Vishay Semiconductors **Dual Channel**

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Rated forward current, DC			60	mA
Peak forward current, DC	1.0 µs pulse, 300 pps	I _{FM}	3.0	Α
Power dissipation		P _{diss}	100	mW
Derate linearly from 25 °C			1.3	mW/°C
OUTPUT				
Collector current		I _C	30	mA
Collector emitter breakdown voltage		BV _{CEO}	30	V
Power dissipation		P _{diss}	150	mW
Derate linearly from 25 °C			2.0	mW/°C
COUPLER			· · · · ·	
Isolation test voltage		V _{ISO}	5300	V _{RMS}
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	M≥ 10 ¹²	Ω
isolation resistance	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω
Creepage distance			≥ 7.0	mm
Clearance distance			≥ 7.0	mm
Total package dissipation		P _{tot}	400	mW
Derate linearly from 25 °C			5.33	mW/°C
Storage temperature		T _{stg}	- 55 to + 150	°C
Operating temperature		T _{amb}	- 55 to + 100	°C
Lead soldering time at 260 °C			10	S

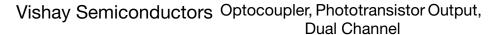
Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT								
Forward voltage	$I_F = 20 \text{ mA}$	V_{F}		1.25	1.50	V		
Reverse current	$V_{R} = 3.0 \text{ V}$	I _R		0.1	10	μΑ		
Junction capacitance	$V_F = 0 V$	C _j		25		pF		
OUTPUT								
Collector emitter breakdown voltage	$I_C = 1.0 \mu A, I_E = 10 \mu A$	BV _{CEO}	30	65		V		
Emitter collector breakdown voltage	$I_C = 10 \mu A, I_E = 10 \mu A$	BV _{ECO}	7.0	10		V		
Collector emitter leakage current	V _{CE} = 10 V	I _{CEO}		1.0	100	nA		
Collector emitter capacitance	V _{CE} = 0 V	C _{CE}		8.0		pF		
COUPLER								
Saturation voltage, collector emitter	$I_C = 2.0 \text{ mA}, I_F = 16 \text{ mA}$	V _{CEsat}			0.40	V		
Capacitance (input to output)	f = 1.0 MHz	C _{IO}		0.5		pF		
Capacitance between channels	f = 1.0 MHz			0.4		pF		
Bandwidth	I_C = 2.0 mA, V_{CC} = 10 V, R_L = 100 Ω			150		kHz		

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.





CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
DC current transfer ratio	I _F = 10 mA, V _{CE} = 10 V	CTR _{DC}	20	50		%

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Switching times, output transistor	$I_{C} = 2.0 \text{ mA}, R_{L} = 100 \Omega, V_{CE} = 5 \text{ V}$	t _{on} , t _{off}		3.0		μs

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

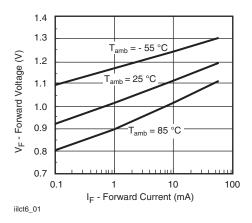


Fig. 1 - Forward Voltage vs. Forward Current

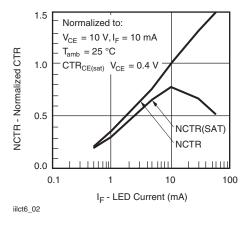


Fig. 2 - Normalized Non-Saturated and Saturated CTR vs. LED Current

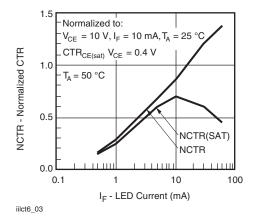


Fig. 3 - Normalized Non-Saturated and Saturated CTR vs. LED Current

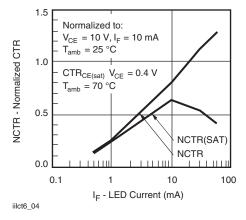


Fig. 4 - Normalized Non-Saturated and Saturated CTR vs. LED Current



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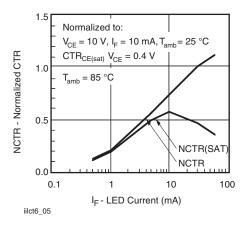


Fig. 5 - Normalized Non-Saturated and Saturated CTR vs. LED Current

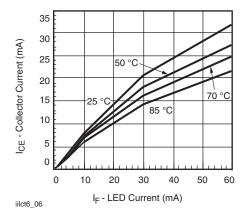


Fig. 6 - Collector Emitter Current vs. Temperature and LED Current

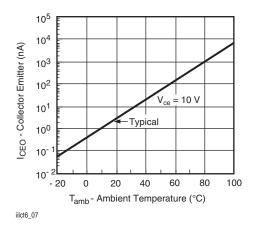


Fig. 7 - Collector Emitter Leakage Current vs.Temperature

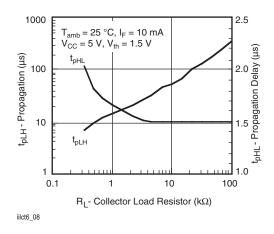


Fig. 8 - Propagation Delay vs. Collector Load Resistor

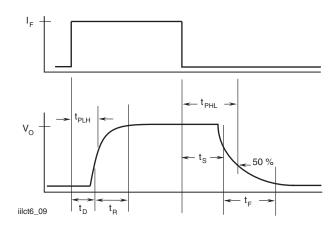


Fig. 9 - Switching Timing

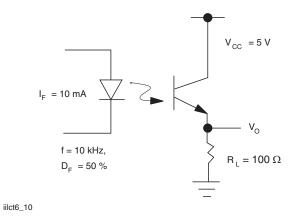
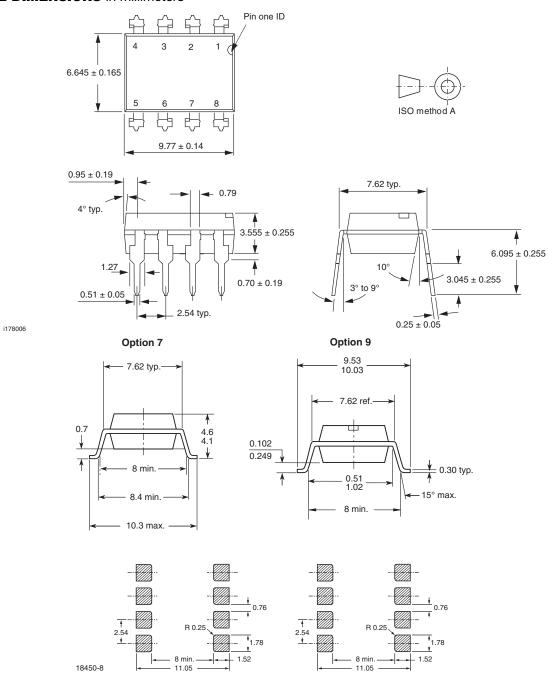


Fig. 10 - Switching Schematic

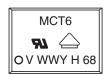
Vishay Semiconductors Optocoupler, Phototransistor Output, **Dual Channel**



PACKAGE DIMENSIONS in millimeters



PACKAGE MARKING



Notes

- Only options 1 and 7 reflected in the package marking
- The VDE logo is only marked on option 1 parts
- Tape and reel suffix (T) is not part of the package marking



Legal Disclaimer Notice

Vishay

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