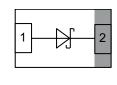


# Vishay Semiconductors

# **Small Signal Schottky Diode**





#### **LINKS TO ADDITIONAL RESOURCES**







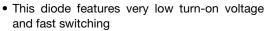
#### **MECHANICAL DATA**

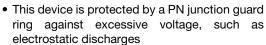
Case: DFN1006-2A Weight: 0.83 mg

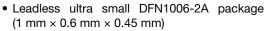
Molding compound flammability rating: UL 94 V-0 **Terminals:** high temperature soldering guaranteed:

Peak temperature max. 260 °C Packaging codes/options: 08/10K per 7" reel (8 mm tape)

#### **FEATURES**







Power dissipation better than SOT-23

 Surface-mounted device (SMD) plastic package with visible and sidewall plated / wettable flanks



AUTOMOTIVE GRADE

- Soldering can be checked by standard visual inspection. No X-ray inspection necessary to meet automotive AOI requirements
- AEC-Q101 qualified available
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

PARTS TABLE						
PART	ORDERING CODE	AEC-Q101 QUALIFIED	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS	
BAS40L	BAS40L-G3-08	no	Single	۸	Tape and reel	
	BAS40L-HG3-08	yes	Single	A.	rape and reel	

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Reverse voltage		V <sub>R</sub> 40		V		
Forward current	on FR-4 board with recommended soldering footprint	I <sub>F</sub>	200	mA		
	$T_j = 25  ^{\circ}\text{C},  t_p = 10  \text{ms}$		500	mA		
Non-repetitive peak forward current	$T_j = 100  ^{\circ}\text{C},  t_p = 10  \text{ms}$	I <sub>FSM</sub>	200			
	T <sub>j</sub> = 125 °C, t <sub>p</sub> = 20 μs	]	500			
Dower dissipation	on FR-4 board with recommended soldering footprint	В	300	mW		
Power dissipation	R <sub>thJL</sub> = 100 K/W	P <sub>tot</sub> 1250		mW		

<b>THERMAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air	according to JEDEC® 51-3 on FR-4 board with recommended soldering footprint	R <sub>thJA</sub> 420		K/W		
Thermal resistance junction to lead		R <sub>thJL</sub>	100	K/W		
Maximum junction temperature		T <sub>j max.</sub>	150	°C		
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C		
Operating temperature range		T <sub>op</sub>	-55 to +150	°C		



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
	V <sub>R</sub> = 40 V, T <sub>j</sub> = 25 °C	I <sub>R</sub>			10	μΑ
Leakage current	V <sub>R</sub> = 30 V, T <sub>j</sub> = 150 °C				200	μΑ
	V <sub>R</sub> = 40 V, T <sub>j</sub> = 150 °C				500	μΑ
	I <sub>F</sub> = 1 mA	V <sub>F</sub>			400	mV
Forward voltage	I <sub>F</sub> = 10 mA				560	mV
	I <sub>F</sub> = 40 mA				1000	mV
Diode capacitance	V <sub>R</sub> = 0 V, f = 1 MHz	C <sub>D</sub>		2.9		pF

# TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

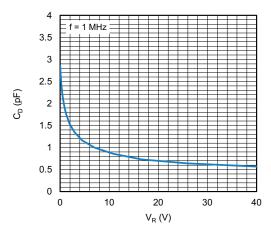


Fig. 1 - Typical Capacitance vs. Reverse Voltage

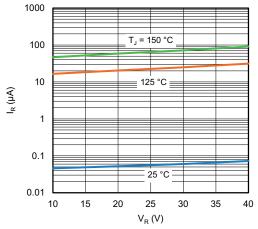


Fig. 3 - Typical Reverse Leakage Current vs. Reverse Voltage

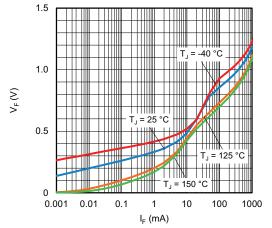
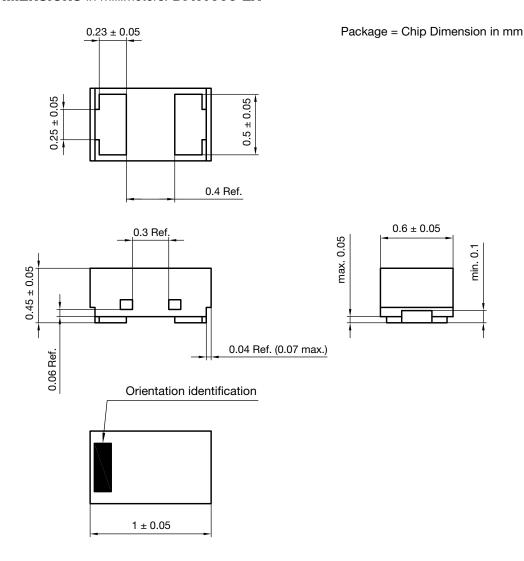


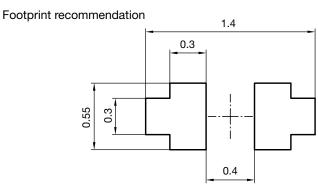
Fig. 2 - Typical Forward Voltage vs. Forward Current



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## PACKAGE DIMENSIONS in millimeters: DFN1006-2A



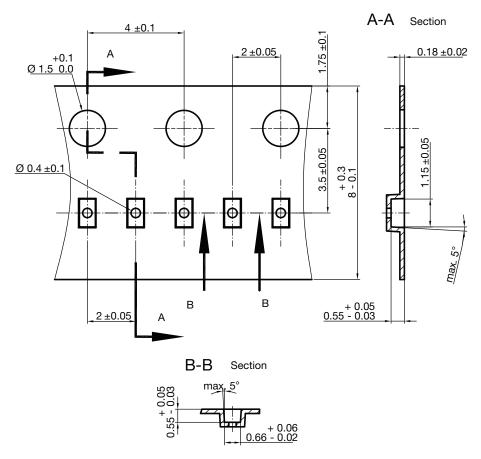


Document no.: S8-V-3906.04-059 (4) Created - Date: 11-Jul-2018 Rev.5 - Date: 17-Sep-2021

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## **CARRIER TAPE DFN1006-2A**



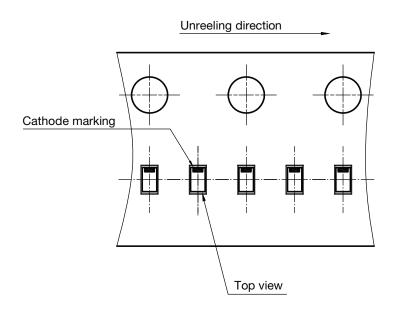
S8-V-3906.04-063 (4) created 28.10.2019

S8-V-3906.04-064 (4)

created 28.10.2019

surface resistance:  $10^5$  -  $10^{11} \frac{OHMS}{SQ}$ Cummulative tolerances of 10 sprocket holes is ± 0.2 mm

#### **ORIENTATION IN CARRIER TAPE DFN1006-2A**





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