FEATURES Trench MOS Schottky technology

- Low forward voltage drop, low power losses
- · High efficiency operation
- · Low thermal resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Vishay General Semiconductor

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters and reverse battery protection.

MECHANICAL DATA

Case: D²PAK (TO-263AB) Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

Polarity: as marked

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VB30100C	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	100	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)}	30	A	
	per diode		15		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	160	A	
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +150	°C	

ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode ⁽¹⁾	I _F = 5 A	T _A = 25 °C	- V _F	0.516	-	V	
	I _F = 7.5 A			0.576	-		
	I _F = 15 A			0.734	0.80		
	I _F = 5 A	T _A = 125 °C		0.455	-		
	I _F = 7.5 A			0.522	-		
	I _F = 15 A			0.627	0.68		
Reverse current per diode ⁽²⁾	V _R = 70 V	T _A = 25 °C	I _R	7.2	-	μA	
	$v_{\rm R} = 70$ v	T _A = 125 °C		8.0	-	mA	
	V 100 V	T _A = 25 °C		65	500	μA	
	V _R = 100 V	T _A = 125 °C		20	35	mA	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

Revision: 20-Jun-2018 Document Number: 87984 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000

Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.455$ V at $I_F = 5$ A

K 2 2
VB30100C
DRT TOOLS Click logo to get started

TMBS[®]

D²PAK (TO-263AB)

DESIGN SUPPO



PRIMARY CHARACTERISTICS			
I _{F(AV)}	2 x 15 A		
V _{RRM}	100 V		
I _{FSM}	160 A		
V_F at $I_F = 15 A$	0.63 V		
T _J max.	150 °C		
Package	D ² PAK (TO-263AB)		
Circuit configuration	Common cathode		

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RoHS COMPLIANT

HALOGEN

FREE

VB30100C



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THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER SYMBOL VB30100			UNIT	
Typical thermal resistance per diode	$R_{ ext{ heta}JC}$	2.5	°C/W	

ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
TO-263AB	VB30100C-M3/4W	1.39	4W	50/tube	Tube
TO-263AB	VB30100C-M3/8W	1.39	8W	800/reel	Tape and reel

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

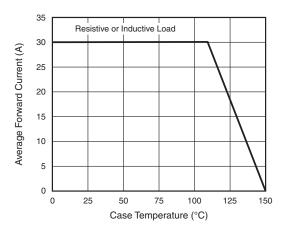


Fig. 1 - Forward Current Derating Curve

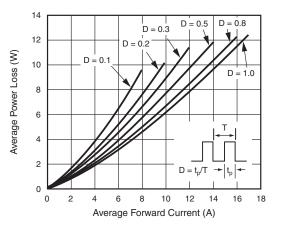


Fig. 2 - Forward Power Loss Characteristics Per Diode

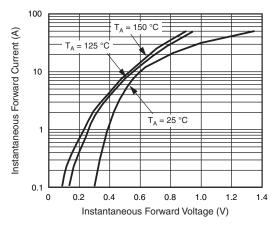


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

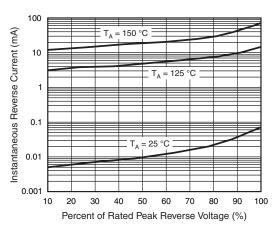


Fig. 4 - Typical Reverse Characteristics Per Diode





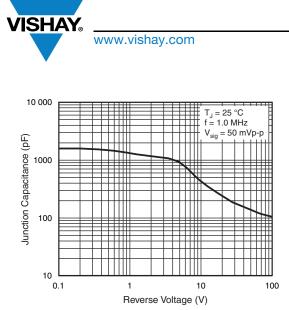


Fig. 5 - Typical Junction Capacitance

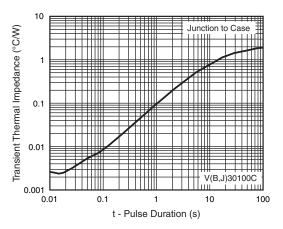
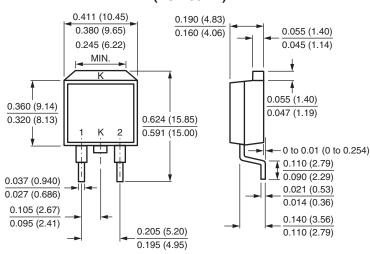


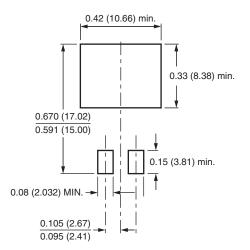
Fig. 6 - Typical Transient Thermal Impedance Per Diode





D²PAK (TO-263AB)

Mounting Pad Layout





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