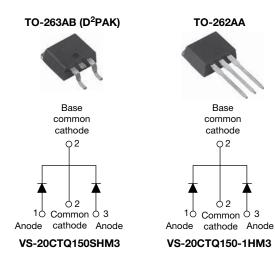


Vishay Semiconductors

epoxy

High Performance Schottky Rectifier, 2 x 10 A



PRODUCT SUMMARY							
Package	TO-263AB (D ² PAK), TO-262AA						
I _{F(AV)}	2 x 10 A						
V _R	150 V						
V _F at I _F	0.66 V						
I _{RM} max.	5.0 mA at 125 °C						
T _J max.	175 °C						
E _{AS}	1.0 mJ						
Diode variation	Common cathode						

FEATURES

High

- 175 °C T_J operation
- · Center tap configuration
- Low forward voltage drop
- High frequency operation



Guard ring for enhanced ruggedness and long term reliability

purity, high temperature

encapsulation for enhanced mechanical

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified meets JESD-201 class 1A whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	20	A						
V _{RRM}		150	V						
I _{FSM}	t _p = 5 μs sine	1030	A						
V _F	10 A_{pk} , T_J = 125 °C (per leg)	0.66	V						
TJ	Range	-55 to +175	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-20CTQ150SHM3 VS-20CTQ150-1HM3	UNITS					
Maximum DC reverse voltage	V _R	150	V					
Maximum working peak reverse voltage	V _{RWM}	150	v					

ABSOLUTE MAXIMUM RATINGS									
PARAMETER		SYMBOL	TEST CONDI	TEST CONDITIONS		UNITS			
Maximum average forward	per leg						10		
current See fig. 5	per device	I _{F(AV)}	50 % duty cycle at T _C = 154 °C	20	А				
Maximum peak one cycle		I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated		1030			
See fig. 7	non-repetitive surge current per leg See fig. 7		10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied		180			
Non-repetitive avalanche energ	y per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 2 mH		1.0	mJ			
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical		1	А			

Revision: 21-Aug-14

Document Number: 95739

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>

1



Vishay Semiconductors

ELECTRICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CO	TYP.	MAX.	UNITS					
Maximum forward voltage drop per leg See fig. 1		10 A	T _{.1} = 25 °C	0.80	0.88					
	V (1)	20 A	1j=25 C	0.90	1.0	v				
	V _{FM} ⁽¹⁾	10 A	T _{.1} = 125 °C	0.63	0.66					
		20 A	$I_{\rm J} = 125$ C	0.73	0.77					
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	3.0	25	μA				
See fig. 2	IRM ()	T _J = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	2.7	5.0	mA				
Typical junction capacitance per leg	C _T	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 °C			280	pF				
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body			8.0	nH				
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs				

Note

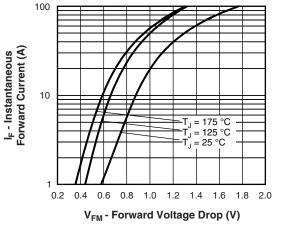
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 $\,\%$

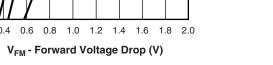
THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +175	°C			
Maximum thermal resistance,	per leg	П	DC operation	2.0				
junction to case	per package	R _{thJC}	DC operation	1.0	°C/W			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased (Only for TO-262)	0.50				
Approvimato weight				2	g			
Approximate weight				0.07	oz.			
Manuation to some	minimum			6 (5)	kgf · cm			
Mounting torque	maximum			12 (10)	(lbf · in)			
			Case style D ² PAK	20CTQ	150SH			
Marking device			Case style TO-262	20CTQ ²	I50-1H			

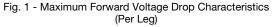
VS-20CTQ150SHM3, VS-20CTQ150-1HM3

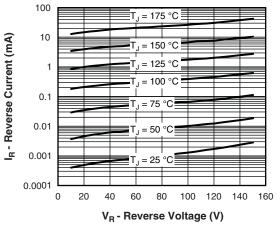
Vishay Semiconductors

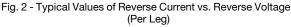












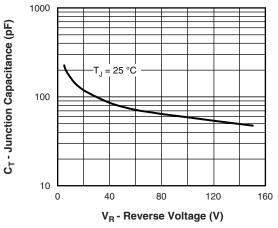


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

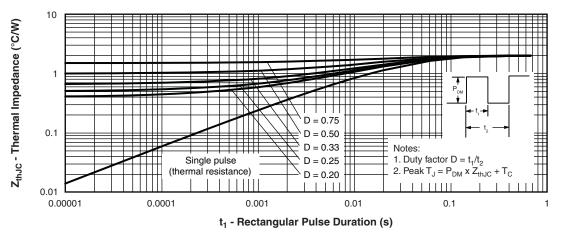
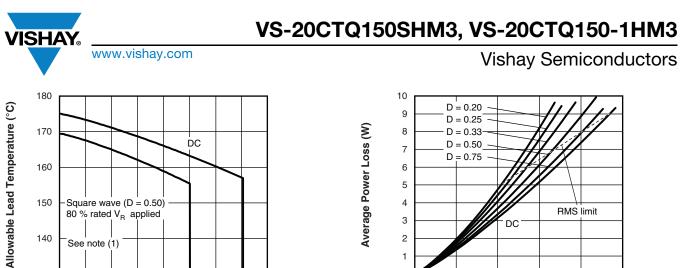


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

Revision: 21-Aug-14

3

For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000



4

3

2

1

0

0

3

RMS limit

12

15

DC

I_{F(AV)} - Average Forward Current (A)

Fig. 6 - Maximum Average Forward Dissipation vs.

Average Forward Current

9

6

1000 I_{FSM} - Non-Repetitive Surge Current (A) At any rated load condition and with rated \mathbf{V}_{RRM} applied following surge ΠΗΠ 100 100 1000 10 000 10



Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

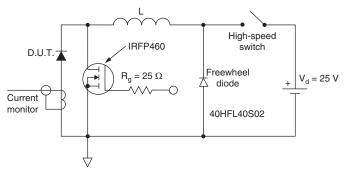


Fig. 8 - Unclamped Inductive Test Circuit

Note

150

140

130

0

Square wave (D = 0.50)

80 % rated V_R applied

10

I_{F(AV)} - Average Forward Current (A)

Fig. 5 - Maximum Average Forward Current vs.

Allowable Lead Temperature

12 14 16

See note (1)

2 4 6 8

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \\ \end{array}$

Revision: 21-Aug-14

4

For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000



Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code	VS-	20	С	т	Q	150	S	TRL	н	МЗ	
		2	3	4	5	6	7	8	9	10	
	1 - Vishay Semiconductors product										
	2	2 - Current rating (20 = 20 A)									
	3	3 - C = common cathode									
	4	- T = TO-220									
	5	- Sch	ottky "C)" series							
	6	- Volt	age rati	ng (150	= 150 \	/)					
	7	• S	= D ² PA	K							
		• -1	= TO-2	62							
	8	- • None = tube									
		• TI	 TRL = tape and reel (left oriented - for D²PAK only) 								
		• TI	 TRR = tape and reel (right oriented - for D²PAK only) 								
	9	- H=	AEC-Q	101 qua	alified						
	10	- M3	= halog	en-free,	RoHS	-complia	ant and	termina	ition lea	ld (Pb)-fi	

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-20CTQ150SHM3	50	1000	Antistatic plastic tubes						
VS-20CTQ150STRLHM3	800	800	13" diameter reel						
VS-20CTQ150STRRHM3	800	800	13" diameter reel						
VS-20CTQ150-1HM3	50	1000	Antistatic plastic tubes						

LINKS TO RELATED DOCUMENTS							
Dimensions	TO-263AB (D ² PAK)	www.vishay.com/doc?95046					
	TO-262AA	www.vishay.com/doc?95419					
	TO-263AB (D ² PAK)	www.vishay.com/doc?95444					
Part marking information	TO-262AA	www.vishay.com/doc?95443					
Packaging information		www.vishay.com/doc?95032					

Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches

www.vishay.com

SHA



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

1

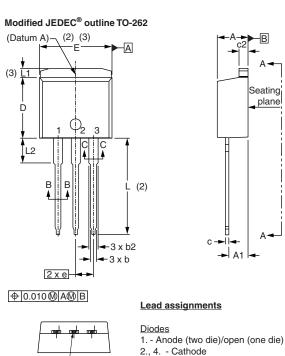
Outline Dimensions



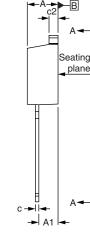
Vishay Semiconductors

TO-262

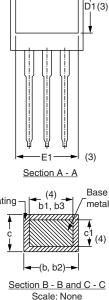
DIMENSIONS in millimeters and inches



Lead tip -



E1 Plating



Е

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. А 4.06 4.83 0.160 0.190 2.03 A1 3.02 0.080 0.119 b 0.51 0.99 0.020 0.039 b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 1.14 1.73 0.045 0.068 4 b3 0.38 0.74 0.015 0.029 С 0.38 0.58 0.015 0.023 4 c1 1.14 1.65 0.045 0.065 c2 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2, 3 E1 7.90 8.80 0.311 0.346 3 0.100 BSC 2.54 BSC е L 13.46 14.10 0.530 0.555 L1 _ 1.65 0.065 3 _ 3.36 0.132 0.146 L2 3.71

3. - Anode

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC TO-262 except A1 (maximum), (6) b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

Revision: 11-Jul-2019

Document Number: 95419

1 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.