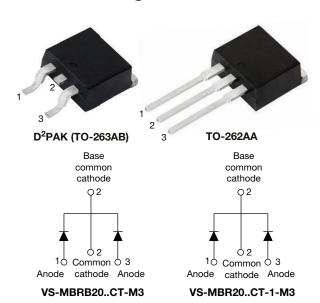
Vishay Semiconductors

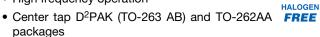
## High Performance Schottky Rectifier, 2 x 10 A



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 10 A				
$V_{R}$	80 V, 90 V, 100 V				
V <sub>F</sub> at I <sub>F</sub>	0.70 V				
I <sub>RM</sub> max.	6 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
E <sub>AS</sub>	7 mJ				
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA				
Circuit configuration	Common cathode				

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation



- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I <sub>F(AV)</sub>	Rectangular waveform (per device)	20	۸				
I <sub>FRM</sub>	T <sub>C</sub> = 133 °C (per leg)	20	А				
V <sub>RRM</sub>		80 to 100	V				
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	850	Α				
V <sub>F</sub>	10 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.70	V				
T <sub>J</sub>	Range	-65 to +150	°C				

VOLTAGE RATINGS							
PARAMETER	SYMBOL			VS-MBRB20100CT-M3 VS-MBR20100CT-1-M3	UNITS		
Maximum DC reverse voltage	$V_R$	80	90	100	V		
Maximum working peak reverse voltage	$V_{RWM}$	00	90	100	V		



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ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL TEST CONDITIONS		VALUES	UNITS			
Maximum average per leg		$T_C = 133$ °C, rated $V_R$	10				
forward current per device	I <sub>F(AV)</sub>	TC = 133 C, Tated VR	20				
Peak repetitive forward current per leg	I <sub>FRM</sub>	Rated V <sub>R</sub> , square wave, 20 kHz, T <sub>C</sub> = 133 °C	20				
New yearstiting most surge or went		5 μs sine or Following any rated load condition 3 μs rect. pulse and with rated V <sub>RRM</sub> applied	850	А			
Non-repetitive peak surge current	I <sub>FSM</sub>	Surge applied at rated load conditions halfwave, single phase, 60 Hz	150				
Peak repetitive reverse surge current	I <sub>RRM</sub>	2.0 μs, 1.0 kHz	0.5				
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	$T_J = 25 ^{\circ}\text{C}$ , $I_{AS} = 2 \text{A}$ , $L = 12 \text{mH}$	24	mJ			

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS			
		10 A	T <sub>.1</sub> = 25 °C	0.80			
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	20 A	- IJ=25 C	0.95	V		
	VFM ('')	10 A	T 405.00	0.70			
		20 A	- T <sub>J</sub> = 125 °C	0.85			
Maximum instantaneous	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	Rated DC voltage	0.10	mA		
reverse current	'RM ('')	T <sub>J</sub> = 125 °C	- hated DC voltage	6			
Threshold voltage	V <sub>F(TO)</sub>	T T mayimum			V		
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		15.8	mΩ		
Maximum junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz), 25 °C		400	pF		
Typical series inductance	L <sub>S</sub>	Measured from top of term	8.0	nΗ			
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs		

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MEC	HANICAL	SPECIFIC	ATIONS			
PARAMETER		SYMBOL	MBOL TEST CONDITIONS		UNITS	
Maximum junction temperature range		TJ		-65 to 150	°C	
Maximum storage tempe	erature range	T <sub>Stg</sub>		-65 to 175		
Maximum thermal resista junction to case per leg	ance,	R <sub>thJC</sub>	DC operation	2.0		
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50	°C/W	
Maximum thermal resistance, junction to ambient		R <sub>thJA</sub>	DC operation	50		
A nove viscote vysialst				2	g	
Approximate weight				0.07	OZ.	
Mounting toward	minimum		Non-lubricated threads	6 (5)	kgf · cm (lbf · in)	
Mounting torque	maximum		Non-lubricated threads	12 (10)		
Marking device			Case style D <sup>2</sup> PAK (TO-263AB)	MBRB2 MBRB2 MBRB2	090CT	
			Case style TO-262AA	MBR203 MBR203 MBR201	90CT-1	

## Vishay Semiconductors

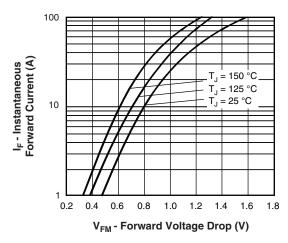


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

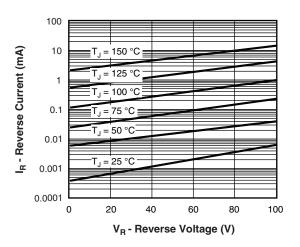


Fig. 2 - Typical Values of Reverse Current vs.Reverse Voltage (Per Leg)

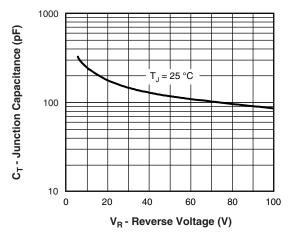


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

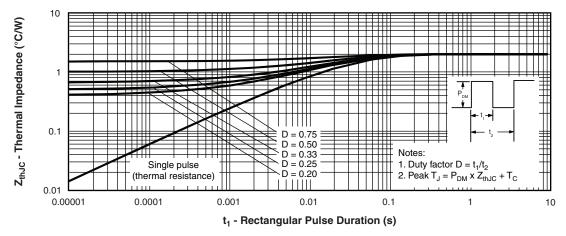


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

## Vishay Semiconductors

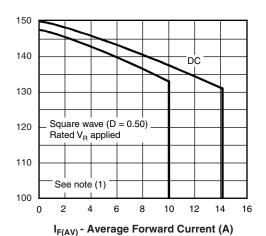


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

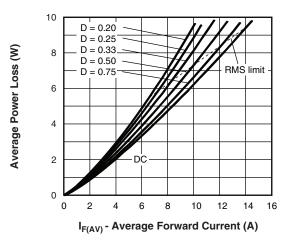


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

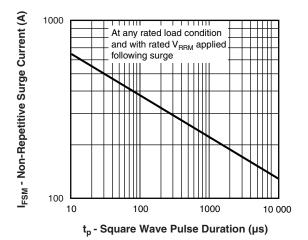


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

#### Note

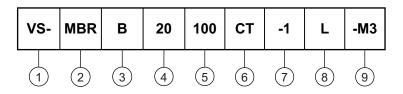
Allowable Case Temperature (°C)

 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = rated V<sub>R</sub>

# Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

Essential part number

• B = D<sup>2</sup>PAK (TO-263AB)7 None

• None = TO-262AA 7 = -1

- Current rating (20 = 20 A) 80 = 80 V 5 - Voltage ratings 90 = 90 V

5 - Voltage ratings - 90 - 90 V 100 = 100 V

7 - None =  $D^2PAK$  (TO-263AB) 3 = B

• -1 = TO-262AA 3 None

None = tube (50 pieces)

• L = tape and reel (left oriented - for D<sup>2</sup>PAK (TO-263AB) only)

• R = tape and reel (right oriented - for D<sup>2</sup>PAK (TO-263AB) only)

9 - -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION						
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION				
VS-MBRB20100CTL-M3	800	13" diameter plastic tape and reel				
VS-MBRB20100CT-M3	50	Antistatic plastic tubes				
VS-MBRB20100CTR-M3	800	13" diameter plastic tape and reel				
VS-MBRB2080CTL-M3	800	13" diameter plastic tape and reel				
VS-MBRB2080CT-M3	50	Antistatic plastic tubes				
VS-MBRB2080CTR-M3	800	13" diameter plastic tape and reel				
VS-MBRB2090CT-M3	50	Antistatic plastic tubes				
VS-MBR20100CT-1-M3	50	Antistatic plastic tubes				
VS-MBR2080CT-1-M3	50	Antistatic plastic tubes				
VS-MBR2090CT-1-M3	50	Antistatic plastic tubes				

LINKS TO RELATED DOCUMENTS					
Dimensions	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96164			
Dimensions	TO-262AA	www.vishay.com/doc?96165			
Part marking information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444			
Part marking information	TO-262AA	www.vishay.com/doc?95443			
Packaging information		www.vishay.com/doc?96424			



## Vishay Semiconductors

### D<sup>2</sup>PAK

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INC	INCHES		ES SYMBOL	MILLIM	ETERS	INC	HES	NOTES	
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inches
- (7) Outline conforms to JEDEC® outline TO-263AB

Revision: 13-Jul-17 Document Number: 96164

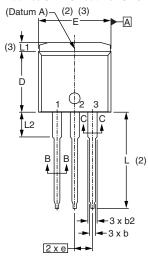


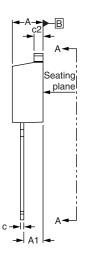
### Vishay Semiconductors

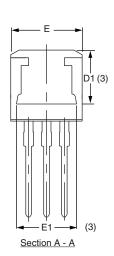
### **TO-262AA**

### **DIMENSIONS** in millimeters and inches

#### Modified JEDEC® outline TO-262







**⊕** 0.010 **M** A**M** B

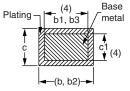
#### Lead assignments



**Diodes** 1. - Anode (two die)/open (one die)

2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

SYMBOL	MILLIM	IETERS	INC	INCHES			
	MIN.	MAX.	MIN.	MAX.	NOTES		
Α	4.06 4.83		0.160	0.190			
A1	2.03	3.02	0.080	0.119			
b	0.51	0.99	0.020	0.039			
b1	0.51	0.89	0.020	0.035	4		
b2	1.14	1.78	0.045	0.070			
b3	1.14	1.73	0.045	0.068	4		
С (	0.38	0.74	0.015	0.029			
c1	0.38	0.58	0.015	0.023	4		
c2	1.14	1.65	0.045	0.065			
D	8.51	9.65	0.335	0.380	2		
D1	6.86	8.00	0.270	0.315	3		
Е	9.65	10.67	0.380	0.420	2, 3		
E1	7.90	8.80	0.311	0.346	3		
е	2.54	BSC	0.10	0 BSC			
L	13.46	14.10	0.530	0.555			
L1	-	- 1.65		0.065	3		
L2	3.56	3.71	0.140	0.146			

#### **Notes**

(4) Dimension b1 and c1 apply to base metal only

Controlling dimension: inches

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC® TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), L1 (max.), L2 (min., max.)



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