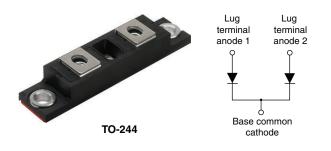
VS-VS5HD240CW60

Vishay Semiconductors

FRED Pt[®] Gen 5, Ultrafast Rectifier Diode, 600 V, 240 A



www.vishay.com

PRIMARY CHARACTERISTICS					
I _{F(AV)} at 95 °C (per module)	240 A				
V _R	600 V				
Q _{rr} (typical)	260 nC				
t _{rr}	52 ns				
Туре	Modules - diode, FRED Pt [®]				
Package	TO-244				
Circuit configuration	Two diodes common cathode				

FEATURES

- Ultrafast and optimized Q_{rr}
- Best in class forward voltage drop and switching losses trade off
 COMPLIANT
- Optimized for high speed operation
- 175 °C maximum operation junction temperature
- UL approved file E222165 **FL**
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- Reduced RFI and EMI
- Higher frequency operation
- Reduced snubbing
- Reduced parts count

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses the FRED Pt[®] Gen 5 is the right choice for soft switched and resonant converters, as well as medium frequency hard switching converters.

These devices are also ideally suited for HF welding, power converters, and other applications where switching losses are significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V _R		600	V	
		T _C = 25 °C	229		
Continuous forward current per diode	I _{F(DC)}	T _C = 85 °C	160		
		T _C = 115 °C	120	А	
Non-repetitive single pulse forward current per diode	I _{FSM}	T _C = 25 °C	1300		
Maximum power dissipation per diode	P _D	T _C = 25 °C	395	W	
		T _C = 115 °C	158	VV	
Storage temperature range	T _{Stg}		-40 to +150	°C	
Operating junction temperature range	TJ		-40 to +175	°C	

ELECTRICAL SPECIFICATIONS PER LEG ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage	V _{BR}	I _R = 200 μA	600	-	-		
Forward voltage V _{FM}		I _F = 120 A	-	1.52	1.68	v	
		I _F = 240 A	-	1.67	1.96		
	I _F = 120 A, T _J = 150 °C	-	1.17	-			
		I _F = 240 A, T _J = 150 °C	-	1.46	-		
Reverse leakage current	I _{RM}	T _J = 150 °C, V _R = 600 V	-	0.18	0.5	mA	
Series inductance	L _S	From top of terminal hole to mounting plane	-	5	-	nH	
Maximum junction capacitance per leg	CT	V _{DC} = 5 V, f = 1 MHz, 25 °C	-	-	0.7	nF	

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS			TYP.	MAX.	UNITS
		T _J = 25 °C		-	52	-	
Reverse recovery time t _{rr}	T _J = 125 °C		-	135	-	ns	
Deals receivers ourrent	1	T _J = 25 °C	l _F = 50 A, dI _F /dt = 200 A/µs,	-	4.0	-	^
Peak recovery current I _{RRM}	T _J = 125 °C	$V_{\rm B} = 300 \text{V}$	-	11.0	-	A	
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	260	-	nC
	Q _{rr}	T _J = 125 °C		-	1530	-	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Thermal resistance,	per leg	P	-	-	0.38		
junction to case	per module	R _{thJC}	-	-	0.19	°C/W	
Thermal resistance, case	to heatsink	R _{thCS}	-	- 0.10 -			
147-1-1-1			-	68	-	g	
Weight			-	2.4	-	oz.	
Mounting torque Mounting torque center hole			30 (3.4)	-	40 (4.6)		
			12 (1.4)	-	18 (2.1)	lbf · in (N · m)	
Terminal torque			30 (3.4)	-	40 (4.6)	(11.11)	
Vertical pull			-	-	80	lbf ⋅ in	
2" lever pull			-	-	35	ni · tai	
Case style			TO-244				

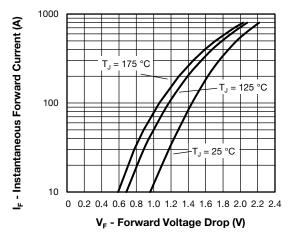
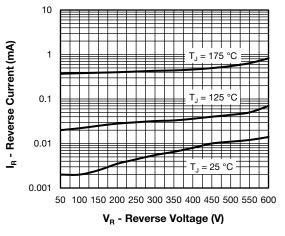
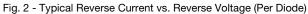


Fig. 1 - Typical Forward Voltage Drop vs. Instantaneous Forward Current (Per Diode)





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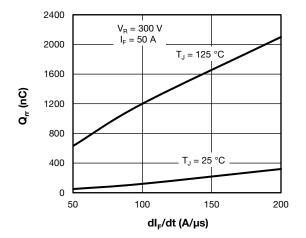


Fig. 3 - Typical Reverse Recovery Charge vs dl_F/dt (Per Diode)

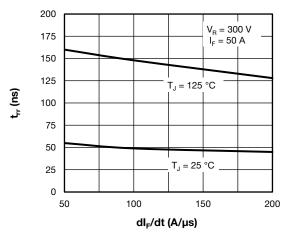


Fig. 4 - Typical Reverse Recovery Time vs dl_F/dt (Per Diode)

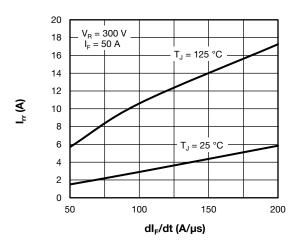
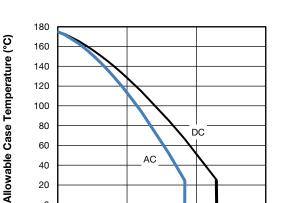


Fig. 5 - Typical Reverse Recovery Current vs dI_F/dt (Per Diode)



I_c - Continuous Collector Current (A)

200

300

100

0

0

Fig. 6 - Maximum Continuous Forward Current vs. Case Temperature

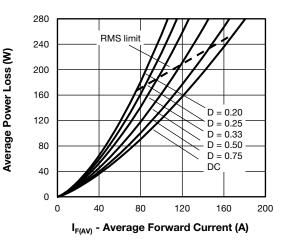


Fig. 7 - Average Power Loss vs. Average Forward Current (Forward Power Loss Characteristics)

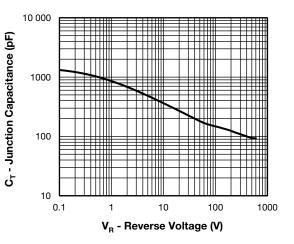


Fig. 8 - Typical Junction Capacitance vs. Reverse Voltage

VS-VS5HD240CW60

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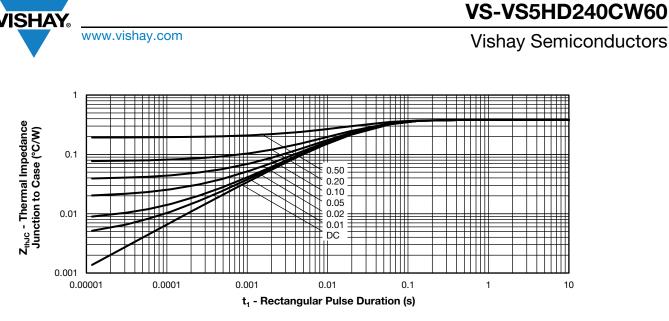


Fig. 9 - Z_{thJC} Maximum Thermal Impedance Junction to Case vs. t₁ Rectangular Pulse Duration

ORDERING INFORMATION TABLE

Device code	vs-vs	5HD	240	С	w	60	
		2	3	4	5	6	
	1 -	Vish	ay Semi	conduct	ors pro	duct	
	2 -	5HD	= high s	speed F	RED Pť	® Gen 5	5
	3 - Current rating (240 = 240 A)						
	4 - Circuit configuration:						
	C = two diodes common cathode						
	5 -	W =	TO-244	wire bo	ndable	not isola	ated
	6 -	Volta	age ratin	ig (60 =	600 V)		

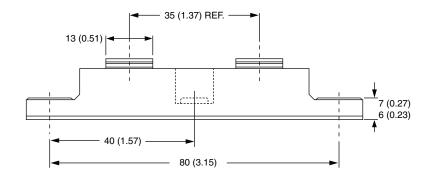
CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Two diodes common cathode	С	Lug Lug terminal anode 1 anode 2 Base common cathode			

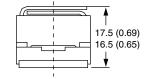
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			

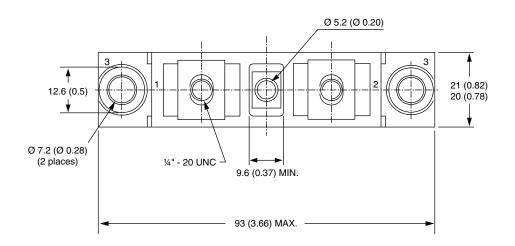
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DIMENSIONS in millimeters (inches)







VS-VS5HD240CW60

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