

Description

The FML-24S is a fast recovery diode of 400 V / 10 A. The maximum $t_{\rm rr}$ of 50 ns is realized by optimizing a life-time control.

Features

•	V_{RM}	400	V
•	• I _{F(AV)}	- 10	A
•	V _F	1.3	V
•	t _{rr1}	50	ns

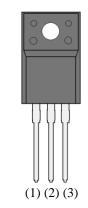
- Bare Lead Frame: Pb-free (RoHS Compliant)
- Flammability: Equivalent to UL94V-0

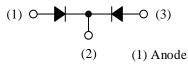
Applications

- Secondary Side Rectifier Diode (Flyback Converter, LLC Converter, etc.)
- Freewheel Diode (Offline Buck and Buck-boost Converter)

Package

TO220F-3L





(2) Cathode

(3) Anode

Not to scale

FML-24S

Absolute Maximum Ratings

Unless otherwise specified, $T_A = 25$ °C.

Parameter	Symbol	Conditions	Rating	Unit
Nonrepetitive Peak Reverse Voltage ⁽¹⁾	V _{RSM}		400	V
Repetitive Peak Reverse Voltage ⁽¹⁾	V_{RM}		400	V
Average Forward Current	I _{F(AV)}	See Figure 1 and Figure 2	10	A
Surge Forward Current ⁽¹⁾	I _{FSM}	Half cycle sine wave, positive side, 10 ms, 1 shot	70	A
I ² t Limiting Value ⁽¹⁾	I ² t	$1 \text{ ms} \le t \le 10 \text{ ms}$	24.5	A^2s
Junction Temperature	T_{J}		-40 to 150	°C
Storage Temperature	T_{STG}		-40 to 150	°C

Electrical Characteristics

Unless otherwise specified, $T_A = 25$ °C.

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Formand Walters Dury(1)	V_{F}	$T_J = 25 ^{\circ}\text{C}, I_F = 5 \text{A}$	_	_	1.3	V
Forward Voltage Drop ⁽¹⁾		$T_J = 100 ^{\circ}\text{C}, I_F = 5 \text{A}$	_	0.94	_	V
Reverse Leakage Current ⁽¹⁾	I_R	$V_R = V_{RM}$		_	100	μΑ
Reverse Leakage Current under High Temperature ⁽¹⁾	$H \cdot I_R$	$V_R = V_{RM}, T_J = 150 ^{\circ}C$	_		200	μA
	t _{rr1}	$I_F = I_{RP} = 100 \text{ mA},$ 90% recovery point, $T_J = 25 \text{ °C}$	_	_	50	ns
Reverse Recovery Time ⁽¹⁾	t _{rr2}	$\begin{split} I_F &= 100 \text{ mA}, \\ I_{RP} &= 200 \text{ mA}, \\ 75\% \text{ recovery point}, \\ T_J &= 25 \text{ °C} \end{split}$	_	_	30	ns
Thermal Resistance ⁽²⁾	R _{th(J-C)}			_	4.0	°C/W

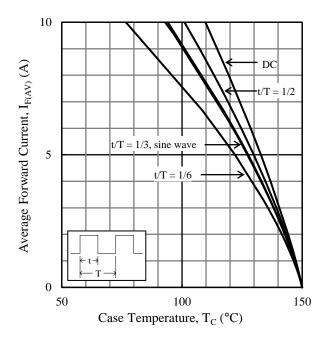
Mechanical Characteristics

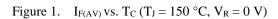
Parameter	Conditions	Min.	Тур.	Max.	Unit
Heatsink Mounting Screw Torque		0.490	_	0.686	N·m
Package Weight		_	1.8	_	g

⁽¹⁾ Specifies a value per chip; the FML-24S consists of two chips.

 $^{^{(2)}}R_{th (J-C)}$ is thermal resistance between junction and the case. The case temperature is measured at the back side near the screw hole.

Derating Curves





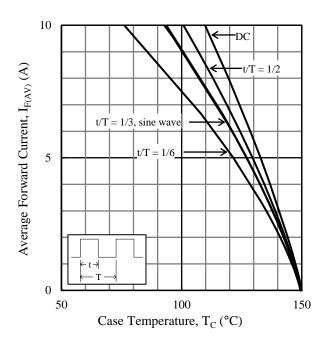


Figure 2. $I_{F(AV)}$ vs. T_C ($T_J = 150$ °C, $V_R = 400$ V)

Characteristic Curves

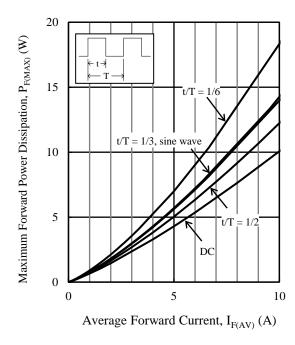


Figure 3. $P_{F(MAX)}$ vs. $I_{F(AV)}$ ($T_J = 150$ °C)

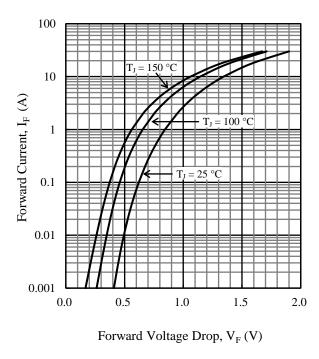


Figure 5. Typical Characteristics: I_F vs. V_F

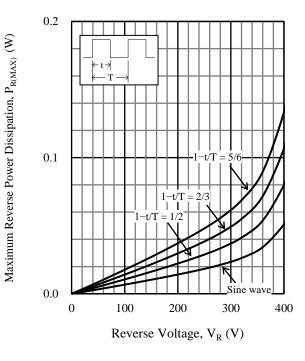


Figure 4. $P_{R(MAX)}$ vs. V_R ($T_J = 150$ °C)

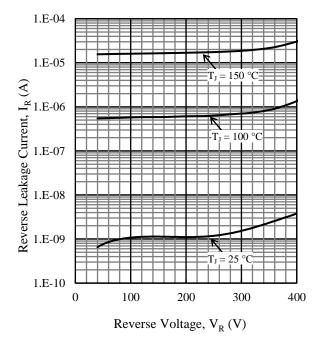


Figure 6. Typical Characteristics: I_R vs. V_R

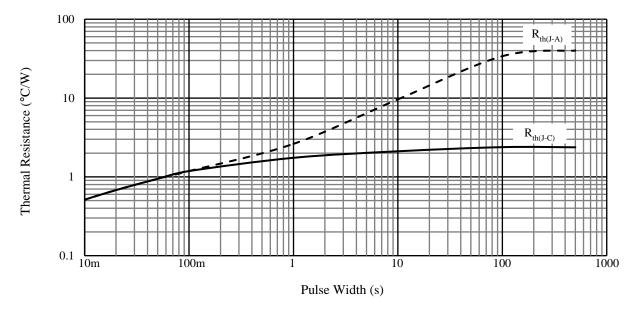
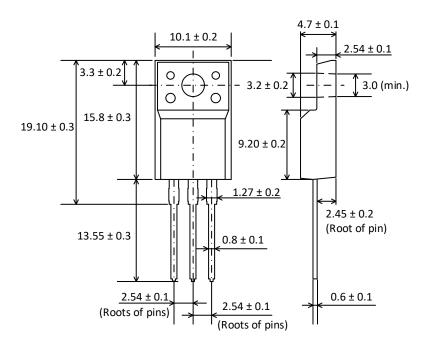


Figure 7. Typical Transient Thermal Resistance Characteristics

Physical Dimensions

• TO220F-3L



NOTES:

- Dimensions in millimeters
- All the dimensions exclude mold flashes.
- Bare lead frame: Pb-free (RoHS compliant)
- When soldering the products, it is required to minimize the working time within the following limits:

Flow: 260 °C / 10 s, 1 time

Soldering Iron: 350 °C / 3.5 s, 1 time

Soldering should be at a distance of at least 1.5 mm from the body of the product.

Marking Diagram

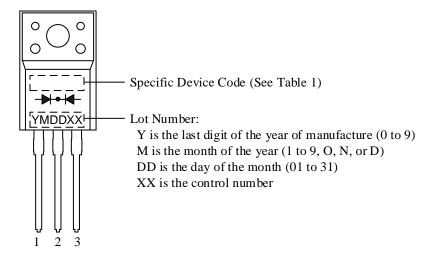


Table 1. Specific Device Code

Specific Device Code	Part Number		
FML24S	FML-24S		

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