

Taiwan Semiconductor

# 1A, 600V - 900V High Efficient Rectifier

### FEATURES

- AEC-Q101 qualified available
- High efficiency, Low  $V_F$
- High efficient recovery time
- 175°C operating junction temperature
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

### APPLICATIONS

- DC to DC converter
- Switching mode converters and inverters
- Freewheeling application

### **MECHANICAL DATA**

- Case: DO-204AC (DO-15)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Pure tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.400g (approximately)

KEY PARAMETERS		
PARAMETER	VALUE	UNIT
I <sub>F</sub>	1	А
V <sub>RRM</sub>	600 - 900	V
I <sub>FSM</sub>	35	А
T <sub>J MAX</sub>	150 - 175	°C
Package	DO-204AC (DO-15)	
Configuration	Single	die







Cathode Anode

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25	°C unless otherw	ise noted)		
PARAMETER	SYMBOL	MUR160	MUR190	UNIT
Marking code on the device		MUR160	MUR190	
Repetitive peak reverse voltage	V <sub>RRM</sub>	600	900	V
Reverse voltage, total rms value	V <sub>R(RMS)</sub>	420	630	V
Forward current	I <sub>F</sub>	1		А
Surge peak forward current, 8.3ms single half sine wave superimposed on rated load	I <sub>FSM</sub>	35		А
Junction temperature	TJ	-55 to +175	-55 to +150	°C
Storage temperature	T <sub>STG</sub>	-55 to +175	-55 to +150	°C





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THERMAL PERFORMANCE			
PARAMETER	SYMBOL	ТҮР	UNIT
Junction-to-ambient thermal resistance	R <sub>eja</sub>	50	°C/W

ELECTRICAL SPECIFI	CATIONS	$T_A = 25^{\circ}C$ unless other	erwise noted)			
PARAMETER		CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
Forward voltage <sup>(1)</sup>	MUR160	- I <sub>F</sub> = 1A, T <sub>J</sub> = 25°C	V <sub>F</sub>	-	1.25	V
	MUR190			-	1.70	V
	MUR160	I <sub>F</sub> = 1A, T <sub>J</sub> = 150°C		-	1.05	V
	MUR190			-	1.50	V
Reverse current @ rated V <sub>R</sub> <sup>(2)</sup>		$T_J = 25^{\circ}C$	I <sub>R</sub>	-	5	μA
		T <sub>J</sub> = 125°C		-	150	μA
Junction capacitance	MUR160	1MHz, V <sub>R</sub> = 4.0V	CJ	27	-	pF
	MUR190			15	-	pF
Deveree receiver time	MUR160	$I_F = 0.5A, I_R = 1.0A, I_{rr} = 0.25A$	t <sub>rr</sub>	-	50	ns
Reverse recovery time	MUR190			-	75	ns

#### Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms

RDERING INFORMATION		
ORDERING CODE <sup>(1)(2)</sup>	PACKAGE	PACKING
MUR1x	DO-204AC (DO-15)	3,500 / Tape & Reel
MUR1x A0G	DO-204AC (DO-15)	1,500 / Ammo box
MUR1xH	DO-204AC (DO-15)	3,500 / Tape & Reel
MUR1xHA0G	DO-204AC (DO-15)	1,500 / Ammo box

#### Notes:

1. "x" defines voltage from 600V (MUR160) to 900V (MUR190)

2. "H" means AEC-Q101 qualified



**MUR160** 

**MUR190** 

10

100

### **CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25°C unless otherwise noted)

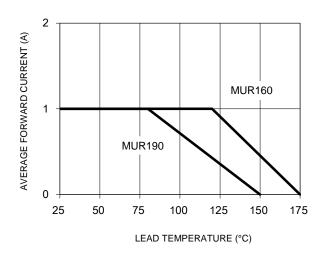


Fig.1 Forward Current Derating Curve

#### **Fig.3 Typical Reverse Characteristics**

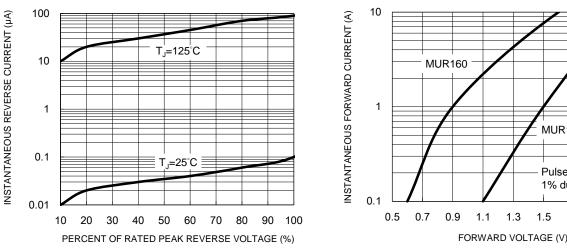


Fig.5 Maximum Non-Repetitive Forward Surge Current 40 PEAK FORWARD SURGE CURRENT (A) 35 8.3ms single half sine wave 30 25 20 15 10 5 0 10 100 1

#### **Fig.2 Typical Junction Capacitance**

100

10

1

0.1

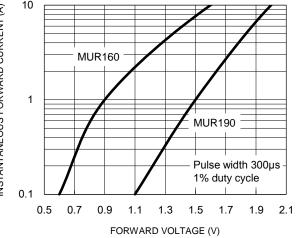
CAPACITANCE (pF)

**Fig.4 Typical Forward Characteristics** 

REVERSE VOLTAGE (V)

f=1.0MHz Vsig=50mVp-p

1

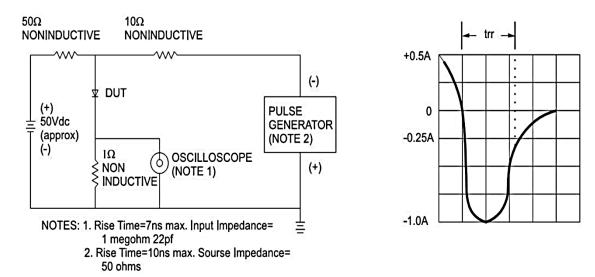


Version: L2107



### **CHARACTERISTICS CURVES**

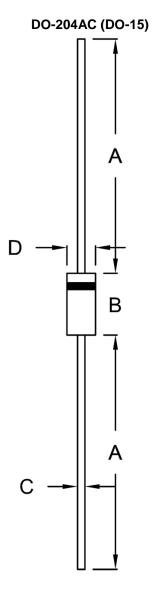
 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 



#### Fig.6 Reverse Recovery Time Characteristic and Test Circuit Diagram



**5** TAIWAN SEMICONDUCTOR



DIM. Unit		(mm)	Unit (	Unit (inch)	
	Min.	Max.	Min.	Max.	
А	25.40	-	1.000	-	
В	5.80	7.60	0.228	0.299	
С	0.70	0.90	0.028	0.035	
D	2.60	3.60	0.102	0.142	

### **MARKING DIAGRAM**



P/N	= Marking Code
G	= Green Compound
YWW	= Date Code
F	= Factory Code



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