



70V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	RDS(on) max	I _D T _A = +25°C
70V	0.13Ω @ V _{GS} = 10V	3.8A

Description and Applications

This new generation of trench MOSFETs utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high-efficiency, low-voltage power management applications.

- DC-DC converters
- Power management functions
- Disconnect switches
- Motor controls
- Class-D audio output stages

Features and Benefits

- Low On-Resistance
- Fast Switching Speed
- Low Threshold
- Low Gate Drive
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- An automotive-compliant part is available under separate datasheet (ZXMN7A11GQ)

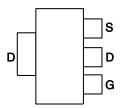
Mechanical Data

- Package: SOT223
- Package Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.112 grams (Approximate)

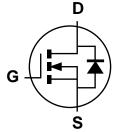




Top View



Pin Out - Top View



Equivalent Circuit

Ordering Information (Note 4)

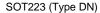
Part Number	Daekaga	Packing		
	Package	Qty.	Carrier	
ZXMN7A11GTA	SOT223 (Type DN)	1,000	Tape & Reel	

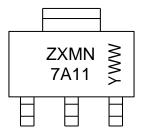
Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/



Marking Information





ZXMN 7A11 = Product Type Marking Code YWW = Date Code Marking Y or \overline{Y} = Last Digit of Year (ex: 3 = 2023) WW or $\overline{W}W = Week Code (01 to 53)$

Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	70	V
Gate-Source Voltage		Vg	±20	V
Continuous Drain Current, V _{GS} = 10V	$T_A = +25^{\circ}C \text{ (Note 6)}$ $T_A = +70^{\circ}C \text{ (Note 6)}$ $T_A = +25^{\circ}C \text{ (Note 5)}$	lσ	3.8 3.0 2.7	А
Maximum Continuous Body Diode Forward Current (Note 6)		Is	3.8	Α
Pulsed Drain Current	I _{DM}	10	Α	
Pulsed Source Current (Body Diode)	Isм	10	А	

Thermal Resistance (@TA = +25°C, unless otherwise specified.)

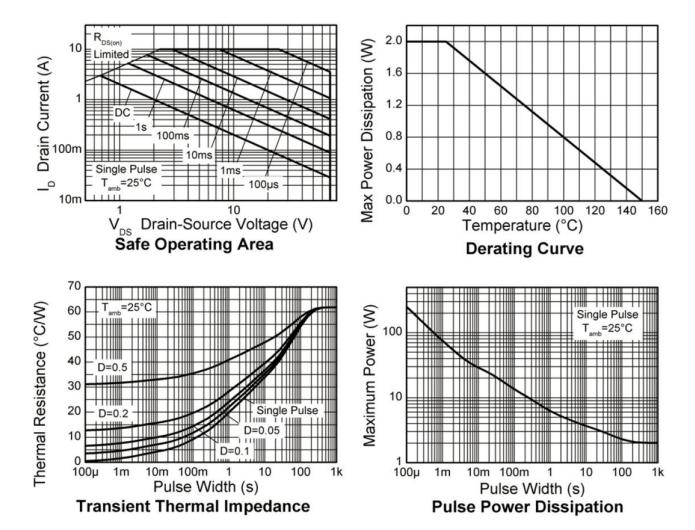
Characteristic	Symbol	Value	Unit
Total Power Dissipation at T _A = +25°C (Note 5) Linear Derating Factor (Note 5)	PD	2.0 16	W mW/°C
Total Power Dissipation at T _A = +25°C (Note 6) Linear Derating Factor (Note 6)	PD	3.9 31	W mW/°C
Thermal Resistance, Junction to Ambient (Note 5)	RθJA	62.5	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	RθJA	32	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Notes:

- 5. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
- 6. For a device surface mounted on FR4 PCB measured at t ≤ 5 sec.
 7. Repetitive rating 25mm x 25mm FR4 PCB, D=0.05 pulse width=10µs pulse width limited by maximum junction temperature.



Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)





Electrical Characteristics ($@T_A = +25$ °C, unless otherwise specified.)

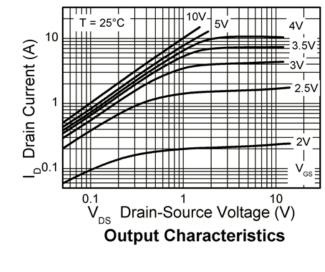
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	70	_	_	V	V _G S = 0V, I _D = 250μA	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μΑ	V _{DS} = 70V, V _{GS} = 0V	
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V _{GS(th)}	1.0	_	_	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance (Note 8)	C	_	_	0.13	Ω	V _{GS} = 10V, I _D = 4.4A	
Static Drain-Source On-Resistance (Note 6)	R _{DS(on)}	_	_	0.19	12	V _{GS} = 4.5V, I _D = 3.8A	
Forward Transfer Admittance	g fs	_	4.66	_	S	V _{DS} = 15V, I _D = 4.4A	
Diode Forward Voltage (Note 8)	VsD	_	0.85	0.95	V	T _J = +25°C , V _{GS} = 0V, I _S = 2.5A	
DYNAMIC CHARACTERISTICS (Notes 9 &10)				•			
Input Capacitance	Ciss	_	298	_		V _{DS} = 50V, V _{GS} = 0V f = 1.0MHz	
Output Capacitance	Coss	_	35	_	pF		
Reverse Transfer Capacitance	Crss	_	21	_			
Total Gate Charge	Qg	_	4.35	_	nC	V _{DS} = 35V, V _{GS} = 5.0V, I _D = 4.4A	
Total Gate Charge	Qg	_	7.4	_		V _{DS} = 35V, V _{GS} = 10V, I _D = 4.4A	
Gate-Source Charge	Qgs	_	1.06	_	nC		
Gate-Drain Charge	Qgd	_	1.8	_			
Turn-On Delay Time	t _{D(on)}	_	1.9	_		$V_{DS}=35V,V_{GS}=10V,$ $I_{D}=1A,R_{G}\cong6.0\Omega$	
Turn-On Rise Time	tr	_	2	_			
Turn-Off Delay Time	t _{D(off)}	_	11.5	_	ns		
Turn-Off Fall Time	t _f	_	5.8	_			
Body Diode Reverse Recovery Time	t _{rr}	_	19.8	_	ns	T _J = +25°C, I _S = 2.5A,	
Body Diode Reverse Recovery Charge	Qrr		14		nC	dl/dt = 100A/µs	

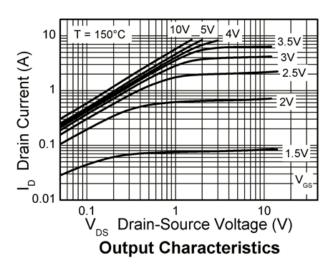
Notes:

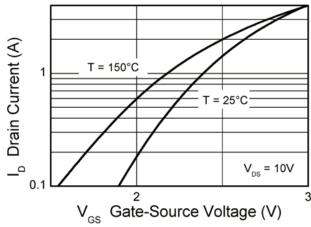
^{8.} Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤ 2%.
9 .Switching characteristics are independent of operating junction temperature.
10. For design aid only, not subject to production testing.

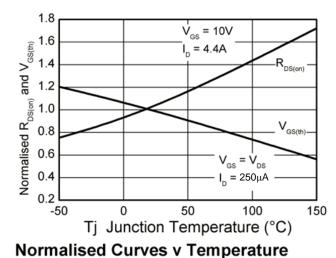


Typical Characteristics

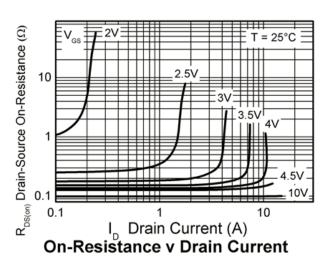


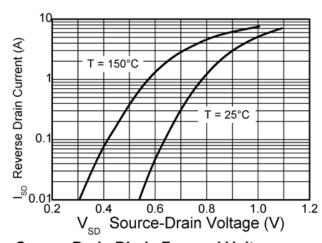








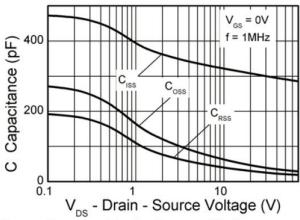




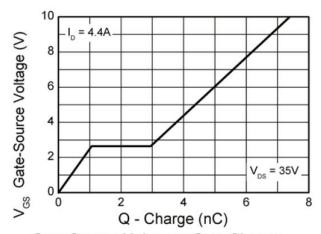
Source-Drain Diode Forward Voltage



Typical Characteristics (continued)



Capacitance v Drain-Source Voltage



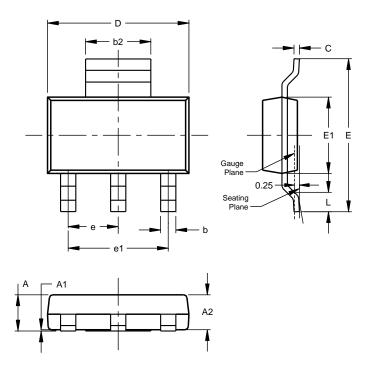
Gate-Source Voltage v Gate Charge



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223 (Type DN)

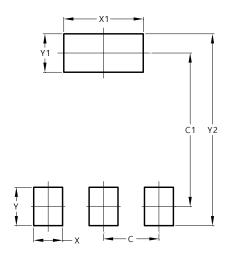


SOT223 (Type DN)				
Dim	Min	Max	Тур	
Α		1.70		
A1	0.01	0.15		
A2	1.50	1.68	1.60	
b	0.60	0.80	0.70	
b2	2.90	3.10		
С	0.20	0.32		
D	6.30	6.70		
Е	6.70	7.30		
E1	3.30	3.70		
е			2.30	
e1			4.60	
L	0.85			
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223 (Type DN)



Dimensions	Value (in mm)
С	2.30
C1	6.40
Х	1.20
X1	3.30
Υ	1.60
Y1	1.60
Y2	8 00



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