MMBD717LT1G

Common Anode Schottky Barrier Diodes

These Schottky barrier diodes are designed for high speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand held and portable applications where space is limited.

Features

- Extremely Fast Switching Speed
- Extremely Low Forward Voltage 0.28 V (Typ) @ $I_F = 1$ mAdc
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_J = 125°C unless otherwise noted)

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	20	V
Forward Power Dissipation @ T _A = 25°C Derate above 25°C	P _F	200 1.6	mW mW/°C
Operating Junction Temperature Range	T _J	-55 to +150	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

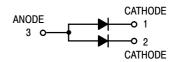
Rating	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage (I _R = 10 μA)	V _{(BR)R}	20	-	-	٧
Total Capacitance (V _R = 1.0 V, f = 1.0 MHz)	C _T	-	2.0	2.5	P _F
Reverse Leakage (V _R = 10 V) (For each individual diode while the second diode is unbiased)	I _R	_	0.05	1.0	μAdc
Forward Voltage (I _F = 1.0 mAdc)	V _F	_	0.28	0.37	Vdc



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20 VOLT SCHOTTKY BARRIER DETECTOR AND SWITCHING DIODES





SC-70 / SOT-323 CASE 419 STYLE 4

MARKING DIAGRAM



B3 = Device Code M = Date Code*

= Pb-Free Package

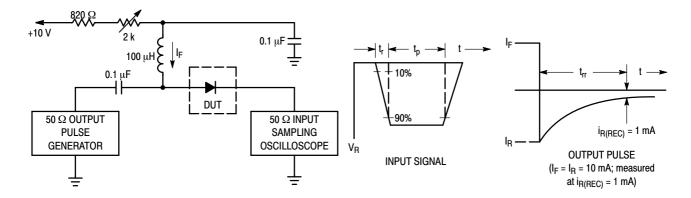
(Note: Microdot may be in either location)
*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBD717LT1G	SC-70 (Pb-Free)	3,000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MMBD717LT1G



NOTES: 1. A 2.0 $k\Omega$ variable resistor adjusted for a Forward Current (I_F) of 10 mA.

- 2. Input pulse is adjusted so I_{R(peak)} is equal to 10 mA.
- 3. t_p » t_{rr}

Figure 1. Recovery Time Equivalent Test Circuit

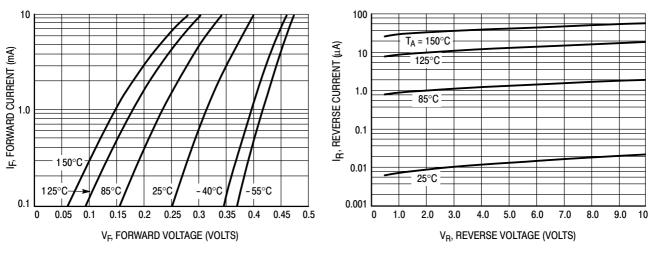
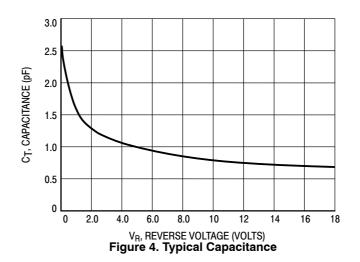


Figure 2. Typical Forward Voltage

Figure 3. Reverse Current versus Reverse Voltage







SC-70 (SOT-323) **CASE 419** ISSUE R

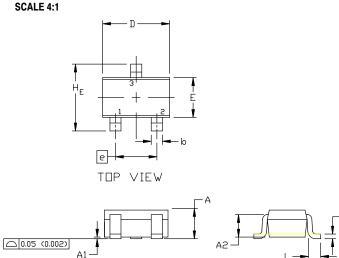
END VIEW

DATE 11 OCT 2022

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH

	MILLIMETERS				INCHES	
DIM	MIN.	N□M.	MAX.	MIN.	N□M.	MAX.
Α	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2	0.70 REF			0.028 BSC		
b	0.30	0.35	0.40	0.012	0.014	0.016
С	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.00	2.20	0.071	0.080	0.087
E	1.15	1.24	1.35	0.045	0.049	0.053
е	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65 BSC			0.026 BSC		
L	0.20	0.38	0.56	0.008	0.015	0.022
HE	2.00	2.10	2.40	0.079	0.083	0.095



GENERIC MARKING DIAGRAM

SIDE VIEW

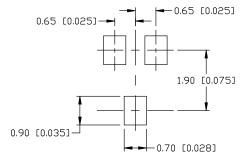


= Specific Device Code XX

Μ = Date Code

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.



For additional information on our Pb-Free strategy and soldering details, please download the ID Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

SOLDERING FOOTPRINT

STYLE 1: CANCELLED	STYLE 2: PIN 1. ANODE 2. N.C. 3. CATHODE	STYLE 3: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 5: PIN 1. ANODE 2. ANODE 3. CATHODE	
STYLE 6: PIN 1. EMITTER	STYLE 7: PIN 1. BASE	STYLE 8: PIN 1. GATE	STYLE 9: PIN 1. ANODE	STYLE 10: PIN 1. CATHODE	STYLE 11: PIN 1. CATHODE
2. BASE	2. EMITTER	2. SOURCE	2. CATHODE	2. ANODE	2. CATHODE
COLLECTOR	COLLECTOR	3. DRAIN	CATHODE-ANODE	3. ANODE-CATHODE	CATHODE

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DESCRIPTION:	SC-70 (SOT-323)		PAGE 1 OF 1	

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